

ENEE 303_2 Spring 2019 03/06-05/02/19			
Paper Choices			
Name/Paper		Dates	Commentating dates
Ajuo, Tracy Ongodia		Tu 04/23/19 d)	Th 05/02/19 d) (as of 04/16/19)
Ina Toteva and Anna Andonova, "Modeling NMOS Snapback Characteristic Using PSpice," Proceedings of the 35th Int. Spring Seminar on Electronics Technology, IEEE e-paper 2012, pp. 335 - 338.			
An, Wei		Th 04/25/19c)	Tu 05/07/19 b)
<b>Nidhi Bansal ; Rishikesh Pandey</b> , "A Novel Current Subtractor Based on Modified Wilson Current Mirror Using PMOS Transistors," Proceedings of the 2016 International Conference on Micro-Electronics and Telecommunication Engineering (ICMETE), iee e-paper, pp. 444-449 [changed to the above 04/25/29] Po-Chun Lai, Chih-Lung Lin, and Jerzy Kanicki, "Novel Top-Anode OLED/a-IGZO TFTs Pixel Circuit for 8K4K AM-OLEDs," IEEE TRANSACTIONS ON ELECTRON DEVICES, VOL. 66, NO. 1, JANUARY 2019, pp. 436 - 444.			
Anato, Messan Kodjovi		Tu 05/07/19 c)	Th 04/25/19 c)
CHUNYUAN FENG, "The design of OTA C filter based on the prototype of LC filter," Journal of Theoretical and Applied Information Technology 10th March 2013. Vol. 49 No.1 pages 144 - 148 [changed to above 04/19/19] Jader A. De Lima, "A Low-Voltage Programmable-Gain Current-Mode Amplifier," IEEE e-papper 2002, pp. 33 - 36.			
Ayoade, Israel Abdulsalam		Tu 04/30/19 f)	Th 05/02/19 c)
R. J. Widlar, "Some Circuit Design Techniques for Linear Integrated Circuits," IEEE Transactions on Circuit Theory, Vol. CT-12, No. 4, December 1965, pp. 586 - 590.			
Bannourah, Tamer Marwan		Tu 04/23/19 a)	Th 05/02/19 e)
<b>Sanjay K.Wadhwa</b> , "A Low Voltage CMOS Bandgap Reference Circuit," Proceedings of the 2008 IEEE International Symposium on Circuits and Systems IEEE e-paper 2008, pp. 2693 - 2696.			
Caspari, Wilson Michael		Tu 04/23/19 c)	Th 04/25/19 a) RWN
Chien-Chang Huang, Wei-Ting Chen, and Kuan-Yu Chen, "High Efficiency Linear Power Amplifier for IEEE 802.11g WLAN Applications," IEEE MICROWAVE AND WIRELESS COMPONENTS LETTERS, VOL. 16, NO. 9, SEPTEMBER 2006, pp. 508 - 510.			
Clarke, Fara Marie		Th 04/18/19 f)	Tu 04/23/19 d)
Reid R. Harrison, Paul T. Watkins, Ryan J. Kier, Robert O. Lovejoy, Daniel J. Black, "A Low-Power Integrated Circuit for a Wireless 100-Electrode Neural Recording System", <i>IEEE Journal of Solid-State Circuits</i> , vol. 42, no. 1, 2007, pp. 135 - 132.			
Do, Alvin Nhat-Quang		Tu 04/23/19 b)	Th 04/25/19 d)
Eric Lindberg, K. Murali and Arunas Tamasevicius, "The Smallest Transistor-Based Nonautonomous Chaotic Circuit," IEEE Transactions on Circuits and Systems II, Vol. 52, No. 10, October 2005, pp. 661 - 664.			

Eichensehr, Phillip Joseph		Th 04/25/19 e)	Th 04/25/19 f)
ZHAO Weiqiang and WEI Peimin, "Pspice System Simulation Application in Electronic Circuit Design," Proceedings of the 32 <sup>nd</sup> Chinese Control Conference, Xi'an, Chinam, July 26-28, 2013, pp. 8634 - 8636.			
Falco, Samantha Rose		Th 05/02/19 c)	Tu 04/30/19 b)
Xiaobin Cui, Mingming Teng and Junhi Hu, "PSPICE-Based Analysis of the Vibration Energy Harvester System with Multiple Piezoelectrc Units," Canadian Journal of Electrical and Computer Engineering, Vol. 38, No. 2, Summer 2015, pp. 246 - 250.			
Friedman, Draisly		Tu 04/30/19 d)	Th 04/18/19 f)
Chutham Sawigun and Prajuab Pawarangkoon, "A Compact Subthreshold CMOS 2nd-Order gm-C Lowpass Filter," ieeeee e-paper, 2015, 4 pages [changed to above 04/25/19]Guangyi Lu, Yuan Wang*, Xuelin Zhang, Song Jia, Ganggang Zhang, and Xing Zhang, "A Power Clamp Circuit Using Current Mirror for On-chip ESD Protection," IEEE e-paper 2012, 3 pages.			
Grimes, Benjamin Clark		Tu 04/25/19 b) RWN (as 04/16/19)	Tu 04/16/19 a) RWN
Hector Trujillo), Agnes Nagy, and Fabriciano Rodriguez, "PSPICE modelling of self-heating effects on lateral bipolar magneto-transistors," Sensors and Actuators 87 2000 38-45.			
Huang, Wei		Tu 04/30/19 e)	Th 05/02/19 a)
Adullah Sunaca, Oguzham Cicekoglu, and Gunhan Dunbar, "MOS Only Simulated Grounded Negative Resistors," IEEE e-paper, 2011, pp. 328 - 331. [changed to above 04/30/19]R. L. PRITCHARD, "Transistor Equivalent Circuits," PROCEEDINGS OF THE IEEE, VOL. 86, NO. 1, JANUARY 1998, pp. 150 - 162.			
Jilani, Ali Arif		Tu 04/23/19 f)	Th 04/18/19 b) RWN
Marco Purschel, "Pspice simulation of the power stage for DC brush motors using state of the art power MOSFETs," 2009 IEEE Vehicle Power and Propulsion Conference, Dearborn, MI, 2009, pp. 1567-1572.			
Johnson, Shane Lars		Tu 05/07/19 a)	Th 04/18/19 e) RWN
Raguvaran.E, Deepak Prasath.N, Alexander.J, Prithiviraj.N and Santhanalakshmi.M, "A Very-High Impedance Current Mirror For Bio-medical Applications," IEEE e-paper, 2011, pp. 828-830.			
Karelia, Neel		Tu 05/07/19 f)	Tu 04/23/19 f)
Hongyan Yan, Manish Biyani, and Kenneth K. O, "A High-Speed CMOS Dual-Phase Dynamic-Pseudo NMOS ((DP)^2) Latch and Its Application in a Dual-Modulus Prescaler," IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 34, NO. 10, OCTOBER 1999, pp. 1400 - 1404.			

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Eric Lindberg, K. Murali and Arunas Tamasevicius, "The Smallest Transistor-Based Nonautonomous Chaotic Circuit," IEEE Transactions on Circuits and Systems II, Vol. 52, No. 10, October 2005, pp. 661 - 664. (duplicates Do) [changed to above 04/18/19] Marcus Leinweber, Pawel Zmarz, Peter Buchmann, Paul Argast, Mark Hübener, Tobias Bonhoeffer, and Georg B. Keller, "Two-photon Calcium Imaging in Mice Navigating a Virtual Reality Environment," Journal of Visualized Experiments, Date Published: 2/20/2014.			
Li, Jeffrey Hans		Tu 04/30/19 b)	Th 04/18/19 d)
Eduard E Sackinger and Walter Guggenbuhl, "a high-swing, high-impedance MOS cascode circuit", IEEE Journal of Solid-State Circuits, Vol. 25, No. 1, February 1990, pp. 289 - 298.			
Liu, Justin Chuan		Th 04/18/19 d)	Tu 04/30/19 a)
Daniel Johannesson and Muhammad Nawaz, "Development of a Simple Analytical PSpice Model for SiC-Based BJT Power Moduels," IEEE Transactions on Power Electronics, Vol. 31, No. 6, June 2016, pp. 4517 - 4525.			
Manangan, Reumer		Th 04/18/19 e)	Th 04/18/19 c) RWN
Reid R. Harrison, and Cameron Charles, "A Low-Power Low-Noise CMOS Amplifier for Neural Recording Applications," IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 38, NO. 6, JUNE 2003, pp. 958 - 965.			
Mariano, Brandon Michael		Th 05/02/19 b)	Th 05/02/19 f)
Seyed Ghavamoddin Nabavi, Emad Gad, Michel Nakhla, and Ram Acha, ""Efficient Statistical Analysis of Microwave Circuits using Decoupled Polynomial Chaos," Proceedings of the 2014 IEEE MTT-S International Microwave Symposium (IMS2014), 2014, pp. 1-4			
Mohamad Shaipudin, Nur Sya		Th 05/02/19 a)	Tu 04/30/19 e)
Adosal Monpapassora, "High Frequency/Low Voltage CMOS Adder," Thammasat International Journal of Science and Technology, Vol. 5, No. 2, May-August 2000, pp. 48 - 51. [changed to above 05/02/19] Ahmed Shaker and Abelhalim Zekry, "A Modified PSPICE Model for the Power PIN Diode," Proceedings of the 22 <sup>nd</sup> International Conference on Microelectronics (ICM 2010), IEEE e-paper, 2009, 4 pages.			
Nemorin, Anne-Laurence Chr		Th 05/02/19 d) (as of 04/16/19)	Tu 04/23/19 c)
Tao Xhao and Qunjing Wang, "Application of MATLAB/SIMULINK and PSPICE simulation in teaching power electronics and electric drive system," Proceedings of the IEEE Conference on Electrical Machines and Systems, September 27-29, 2005, 10 pages (IEEE e-paper) (changed to above 04/09/19)Fábio Mendes da Silva, Nelson J. Camelo, Osvaldo Saavedra, "PHOTOVOLTAIC INVERTER CONTROLLER-FOR PUBLIC LIGHTING SYSTEMS," proceedings of the Congresso Brasileiro de Eletrônica de Potência, January 2003., 5 pages. Available at <a href="https://www.researchgate.net/publication/260629651">https://www.researchgate.net/publication/260629651</a>			

Pathak, Rohit		Tu 04/30/19 c)	Tu 04/30/19 d)
Lindberg, E., Murali, K., & Tamasevicius, A., "The Smallest Transistor-Based Nonautonomous Chaotic Circuit," <i>IEEE Transactions on Circuits and Systems. Part 2: Express Briefs</i> , Vol. 52, No. 10, October 2005, pp. 661-664.			
Perez Oviedo, Mauricio		Th 05/02/19 f)	Th 05/02/19 b)
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Pressley, Kyle Raymond		Tu 05/07/19 d)	Tu 05/07/19 e)
<b>H. Fouad ; E. El-Diwanly ; K. Sharaf ; H. El-Hennawy</b> , "High frequency noise simulations of MOSFET's short channel effects using PSpice," <a href="#">Proceedings of the Nineteenth National Radio Science Conference</a> , Alexandria, Egypt, 21 March 2002, 14 pages, DOI: <a href="#">10.1109/NRSC.2002.1022655</a>			
Pretera, Derek Anthony		Th 04/18/19 a)	Tu 04/23/19 e)
A.Balikci, E. Akpinar, K. Vardar, and B. Turan Azizoglu "Design and P Spice Analysis of a Grid Connected Multilevel Converter with Reduced Number of Switches," <a href="#">Proceedings of the 10th IET International Conference on AC and DC Power Transmission (ACDC 2012)</a> , 2012, 6 pages; IEEE e-paper.			
Rancic, Mihailo Stefan		Th 04/25/19 a)	Tu 05/07/19 a)
Jirawat Hirunporm and Montree Siripruchyanun "A Current-mode PID Controller Using Voltage Differencing Gain Amplifiers", <a href="#">Proceedings of the 2018 18th International Symposium on Communications and Information Technologies (ISCIT)</a> , 2018, pp. 261 – 265.			
Sackalosky, Corey Matthew		Tu 05/07/19 b)	Th 05/07/19 f)
Andriy Semenov and Oleksandr Osadchuk, "Modeling the Deterministic Chaos Microelectronic Oscillator Based on the Bipolar Transistor Structure with Negative Resistance," 2017 IEEE First Ukraine Conference on Electrical and Computer Engineering (UKRCON), pp. 704 – 708.			
Schaefer, Matthew Ryan		Tu 04/23/19 e)	Tu 04/30/19 f)
Jetwara Tangjit, Jetsdaporn Satansup, Worapong Tangsirat, Wanlop Surakamponorn, "Simple Design Technique for Realizing Low-Voltage Low-Power CMOS Current Multiplier," <a href="#">Proceedings of the 2015 7th International Conference on Information Technology and Electrical Engineering (ICITEE)</a> , Chiang Mai, Thailand, pp. 110 – 113. [changed to above 04/23/19] D. T. Zweidinger, S. -. Lee and R. M. Fox, "Compact modeling of BJT self-heating in SPICE," <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , vol. 12, no. 9, pp. 1368-1375, Sept. 1993.			

Schweizer, Ryan James		Tu 05/07/19 e)	Tu 05/07/19 d)
B. Razavi, "A Study of Injection Locking and Pulling in Oscillators," IEEE Journal of Solid-State Circuits, Vol. 39, No. 9, September 2004, pp. 1415 - 1424.			
Shoja, Adil Omid		Tu 04/30/19 a)	Tu 04/25/19 e)
Chia-Yu Yao and Alan N. Wilson, Jrl, "One-Neuron Circuitry for Carry Generation in a 4-Bit Adder," IEEE e-paper, Pp. III-179 - III-184.			
Sinha, Rohit		Th 04/18/19 c)	Th 04/18/19 a)
S. Porwal, P. Chhawchharia and R. Pandey, "Simplified half wave rectifier model for improved laboratory simulation using PSpice," 2009 International Conference on Power Electronics and Drive Systems (PEDS), Taipei, 2009, pp. 973-978. doi: 10.1109/PEDS.2009.5385656 (changed to above 04/09/19) Zhi-cheng Wang, Xiaowen Xu and Zhi-yong Lu, "EMI filter design in Pspice," Proceedings of the 2013 IEEE INTERNATIONAL CONFERENCE ON MICROWAVE TECHNOLOGY & COMPUTATIONAL ELECTROMAGNETICS, Qingdao, IEEE e-paper, 2013, pp. 16-18.			
Smith, Teresa Lynn		Th 05/02/19 e)	Tu 04/39/19
Prashant Kumar, Navaneet Singh Bhandari, Lokesh Bhargav, Rashmi Rathi, and S C Yadav, "Design of Low Power and Area Efficient Half Adder using Pass Transistor and Comparison of various Performance Parameters," Proceedings of the International Conference on Computing, Communication and Automation (ICCCA2017) [IEEE e-paper], pp. 1477-1482. [changed to above 04/12/19] Ronald M. Meixner, Holger H. Göbel, Haidi Qiu, Cihan Ucurum, Wilfried Klux, Roland Stenzel, Faruk Altan Yildirim, Wolfgang Bauhofer, and Wolfgang H. Krautschneider, "A Physical-Based PSPICE Compact Model for Poly(3-hexylthiophene) Organic Field-Effect Transistors," IEEE TRANSACTIONS ON ELECTRON DEVICES, VOL. 55, NO. 7, JULY 2008, pp. 1176 - 1781.			
Tran, Alan Toan		Th 04/25/19 f)	Tu 05/07/19 c)
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Walsh, Robert Francis		Th 04/25/19 d)	Tu 04/23/19 b)
Rajeev Kumar and Vimal Kant Pandey, "A New 5-Transistor XOR-XNOR Circuit Based on the Pass Transistor Logic," Proceedings of the 2011 World Congress on Information and Communication Technologies IEEE-e-paper 2011 pp. 667 - 671. (changed to above 04/15/19) Raghvendra Sahai Saxena, Arun Panwar, S. K. Senwal, P. S. Rana, Sudha Gupta and R. K. Bhan, "PSPICE circuit simulation of Microbolometer infrared detectors with noise sources," Infrared Physics & Technology, Vol. 55, 2012, pp. 527 - 532,			

Westra, Joseph		Tu 04/16/19 b) RWN	Th 04/25/19 b) RWN (per 04/16)
<p>F. A. Farag, C. Galup-Montoro, and M. C. Schneider, "Digitally Programmable Switched-Current FIR Filter for Low-Voltage Applications," IEEE Journal of Solid-State Circuits, Vol. 35, No. 4, April 2000, pp. 637-641.  (changed to above 04/16/19)Akio OGIHARA, Shinsuke YAMASHITA and Shojiro YONEDA, "A PITCH SYNCHRONOUS SWITCHED CAPACITOR DISCRETE FOURIER TRANSFORM CIRCUIT," IEEE e-paper pp. 1693 - 1696.</p>			

Number of students = 37