ENEE 417 Spring 2017   Microelectronics Design Laboratory
Time Schedule: "lectures" Tu 2:00-2:50; ~~EGR 2154~~ AVW 1334; Laboratory AVW 1334
Section 1: M 2:00-5:00 pm ; Section 2: W 2:00-5:00pm; Section 3: Th 2:00-5:00

Week starting Wednesday	Week starting Wednesday
01/25/17 Familiarize with equipment LabVIEW & DAQ tutorials; DAQ for transistor curves Tektronix and Spice transistor curve tracing Initiate Basic Paper choice	02/01/17 Spice, LabVIEW & DAQ tutorials; Basic Paper preliminary choice Spice modeling from curve tracings Special parts list
02/08/17 Continue learning LabView Final Basic Paper choice Parts List for Basic Paper	02/15/17 Op-amp curve tracer
02/22/17 Continue Op-amp curve tracer and start Ring Oscillator	03/01/17 Ring Oscillator
03/08/17 Solar Cell and optional General Impedance Converter Piezoelectric Circuit	03/15/17 [Base paper designs Catch up on all test projects using DAQ & LabView]
03/22/17 Spring braak	03/29/17 Basic paper circuit design & testing Present main ideas on Tuesday
04/05/17 Basic paper circuit measurements Base paper circuit VLSI layout	04/12/17 Finish Base paper circuit Meet on Tuesdays in Lab from now on
04/19/17 Finish Base Paper Circuit and Power Point for Presentations	04/26/17 Oral Reports in Laboratory Using Power Point
05/03/17 Last week of Labs Written Reports on Base Paper Circuit Due	05/10/17 Last day of labs {Th 05/11/17 last class day}
No Final scheduled	