

file:G:\courses\spring2016\307\307timS16.doc RWN 01/20/16

ENEE 307 Spring 2016 Electronic Circuits Design Laboratory (2 credits)  
 Time Schedule: "lectures" W 09:00-09:50 PHY 1410; Laboratory AVW 1330  
 9 sections M11,M2, Tu8, Tu2, W2, Th8, Th2, F9, F12

Week starting Monday	Week starting Monday
01/25/16 Familiarize with equipment Spice transistor curve tracing	02/01/16 Lab1 Goldsman Diodes/rectifiers
02/08/16 Lab1 cont. OpAmp circuits	02/15/16 Lab2 CE amplifier
02/22/16 Lab2 cont Multistage amplifier	02/29/16 Lab3 Power and audio amp
03/07/16 Lab3 cont. Catch up	03/14/16 spring break.
03/21/16 Lab4 Frequency Response	03/28/16 Lab4 cont
04/04/16 Lab5 Differential amplifiers	04/11/16 Lab 6 MOS amplifiers/inverters
04/18/16 MOS Logic Circuits Set 5.2	04/25/16 555 timer Catch Up
05/02/16 In Lab Final (worth 45%)	{Tu 05/10/15 Last classes of semester}