

file:G:\courses\spring2015\417\417timS15.doc RWN 01/24/15

ENEE 417 Spring 2014 Microelectronics Design Laboratory

Time Schedule: "lectures" Tu 12:30-1:20 AVW 1334; Laboratory AVW 1334

Section 1: M 2:00-5:00 pm ; Section 2: W 2:00-5:00pm

Week starting Monday	Week starting Monday
<p>01/26/15 Familiarize with equipment LabVIEW & DAQ tutorials; DAQ for transistor curves Tektronix and Spice transistor curve tracing Initiate Basic Paper choice</p>	<p>02/23/15 Spice, LabVIEW & DAQ tutorials Basic Paper preliminary choice Spice modeling from curve tracings Special parts list</p>
<p>02/09/15 Final Basic Paper choice Op-amp curve tracer If desired Learn VLSI layout Parts List for Basic Paper</p>	<p>02/16/15 Op-amp curve tracer Op-Amp THD If desired Learn VLSI layout</p>
<p>02/23/15 Possible choices: Night light solar cells or Ring Oscillator Using DAQ & LabView</p>	<p>03/02/15 Possible choices: Piezoelectricity circuits or Phase Detectors for PLLs Using DAQ & LabView</p>
<p>03/09/15 Possible choices: Negative impedance circuit or From above DAQ & LabView</p>	<p>03/16/15 Spring braak</p>
<p>03/23/15 [Base paper designs Catch up on all test projects using DAQ & LabView]</p>	<p>03/30/15 Basic paper circuit design & testing Present main ideas on Tuesday</p>
<p>04/06/15 Basic paper circuit measurements Possible base paper circuit VLSI layout</p>	<p>04/13/15 Finish Base paper circuit</p>
<p>04/20/15 Finish Base Paper Circuit and Power Point for Presentations</p>	<p>04/27/15 Oral Reports in Laboratory Using Power Point</p>
<p>05/04/15 Last week of Labs Written Reports on Base Paper Circuit Due</p>	<p>{Tu 05/12/15 Last day of semester}</p>
<p>No Final scheduled</p>	