

ENEE 417 - Spring 2012

Weeks #8-#12

Weeks beginning M 03/26/12

Catch Up and Base Paper Circuit Study

After catching up on anything critical still outstanding, use these periods to concentrate upon your circuit taken from the base paper you have chosen.

1. Design the circuit using design equations where possible.
2. Run Spice simulations; then construct and test. Use automated testing where possible.
3. If of interest make a vlsi layout for your circuit, do a Spice extraction and compare with the results from your simulations and measurements.
{If interested and willing to make measurements on the fabricated chip, you may make a submission to MOSIS for which you will need to insert the validated circuit in a set of pads}.
4. Write a term paper based upon your results and summarizing your study. Be sure to emphasize what you did and your results while including a study of the base paper circuit. This should be like a journal paper in the IEEE literature.
5. Prepare a Power Point computer presentation for an oral report on your base paper study.
6. Submit your written report by the lab period of the week of May 7, 2012. A good portion of the course grade will be based upon the written report and the experimentation behind it.