

	ENEE 417 S11 paper choices 04/14/11		1 <sup>st</sup> presentation	2 <sup>nd</sup> presentation/ Commentator  M 04/25/11 or Th 04/28/11
<b>Section 1 M 2-5</b>				
	Akinso, Semiyu Soledayo		#14	#7/#2
Afshin Rezayee and Ken Martin, "A Coupled Two-Stage Ring Oscillator," Proceedings of the IEEE Conference on ???, 2001, pp. 878 - 881.				
	Berkovich, Andrew S		#15	#1/#5
Wei Tang and Eugenio Culurciello, "A Low-Power High-Speed Ultra-Wideband Pulse Radio Transmission System," IEEE Transactions on Biomedical Circuits and Systems, Vol. 3, No. 5, October, 2009, pp. 286 -292.				
	Freyman, Laura E		#11	#5/#1
Alireza S. Bakhtiar, M. Sadegh Jalali, and Shahriar Mirabbasi, "A High-Efficiency CMOS Rectifier for Low-Power RFID Tags," Proceedings of the IEEE RFID, 2010, pp. 83 - 88.				
	Hirsh, Matthew Thomas		#10	#4/#6
Yi-zhong Yang and Guang-jun Xie, "Research and Design of a Self-adaptable Slope Compensation Circuit with Simple Structure," Proceedings of the IEEE Conference on ???, 2010, pp. 333 - 335.				
	Liu, Yifan		#3 (RWN)	#2/#7 (RWN)
Jaturong Wilas, Kamon Jirasereeamornkul and Pinit Kumhom, "Power Harvester Design for Semi-Passive UHF RFID Tag Using a Tunable Impedance Transformation," Proceedings of the IEEE conference ISCIT, 2009, pp. 1441 - 1445.				
	Oni, Oluseyi Mobolaji		#9	#6/#3
Akm S. Haque, Md. M. Hossain, W. Alan Davis, Howard T. Russell Jr., and Ronald L. Carter, "Design of Sinusoidal, Triangular, and Square Wave Generator Using Current Feedback Operational Amplifier (CFOA)," Proceedings of the IEEE Region 5 Conference, Kansas City, MO, 2008, 5 pages. [changed to above 03/28/11] P. Bruschi, D. Navarrini and M. Piotto, "A Class AB CMOS Operational Amplifier for Application as Rail-to-Rail High Current Drive Output Buffer," Proceedings of ESSCIRC 2002, pp. 731 - 734.				
	Zhou, Yuchen		#6	#3/#4
A. Cabrini, L. Gobbi and G. Torelli, "Design of Maximum-Efficiency Integrated Voltage Doubler," Proceedings of the IEEE Conference on ???, 2007, 317 - 320.				
<b>Section 2 Th 2-5</b>				
	Alhawamdeh, Nadia J		#8	#5/#1
Antonio J. Lopez-Martin, Alfonso Carlosena and Jaime Ramirez-Angulo, "Very Low Voltage MOS Translinear Loops Based on Flipped Voltage Followers," Analog Integrated Circuits and Signal Processing, Vol. 40, 2004, pp. 71 -74.				
	Chow, Justin L		#4	#3/#8
Barrie Gilbert, "A Precise Four-Quadrant Multiplier with Subnanosecond Respnse," IEEE Journal of Solid-State Circuits, Vol. 3, No. 4, 1968, pp. 365 - 373.				
	Gamage, Anthony		#12	#7/#4
Aung A. Latt and Ni N. Win, "Variable Speed Drive of Single Phase Induction Motor Using Frequency Control Method," Proceedings of the IEEE 2009 International Conference on Education Technology and Computer, 2009, pp. 30 - 34.				
	Li, Kun		#5	#4/#6
J. S. Adams, "Electronic Guitar Tuner," Electronic Systems News, Summer 1984, pp. 7 - 10. [changed to above 04/04/11] G. Ferrari, M. Farina, f. Guagliando, M. Carminati and M. Sampietro, "Ultra-low-noise CMOS current preamplifier from DC to 1 MHz," Electronics Letters, Vol. 45, No. 25, December 3, 2009, pp. ??.				
	Pandya, Purvi Girish		#2	#2/#3

Hyunchol Shin and Youngcho Kim, "A CMOS Active-RC Low-Pass Filter with Simultaneously Tunable High- and Low-Cutoff Frequencies for IEEE 802.22 Applications," IEEE Transactions on Circuits and Systems – II, Vol. 57, No. 2, February 2010, pp. 85 – 89.			
	Persetic, Raymond J		#1 #1/##5
Triet Le, Karti Mayaram and Terri S. Fiez, "Efficient Far-Field Radio Frequency Power Conversion System for Passively Powered Sensor Networks," Proceedings of the IEEE 2006 Custom Integrated Circuits Conference (CICC), 2006, pp. 293 – 296.			
	Rathod, Abhishek Manharlal		#7 #6/#2
M. M. Reja, I. M. Filanovsky and K. Moez, "Wide tunable CMOS active inductor," Electronics Letters, Vol. 44, No. 25, December 4, 2008, pp. ????			
	Schuhmacher, Enrique		#13 #8/#7
Z.-H. Kong, K.-S. Yao and C.=H. Chang, "Design of an area-efficient CMOS multiple-valued current comparator circuit," IEE Proceedings on Circuits, Devices, Systems, Vol. 152, No. 2, April 2003, pp. 151 – 158. [replaced by above 04/12/11] Khayrollah Hadidi, Hiroyuki Oshima, Masahiro Sasaki, and Takashi Matsumoto, "A Highly Linear Fully differential Low Power CMOS Line Driver," Proceedings of the IEEE Conference on ???, 2003, pp. 541 – 544.[received 03/29/11]			