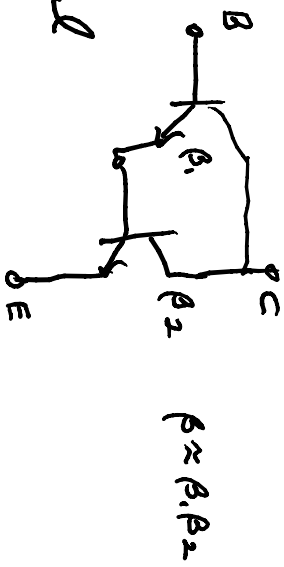


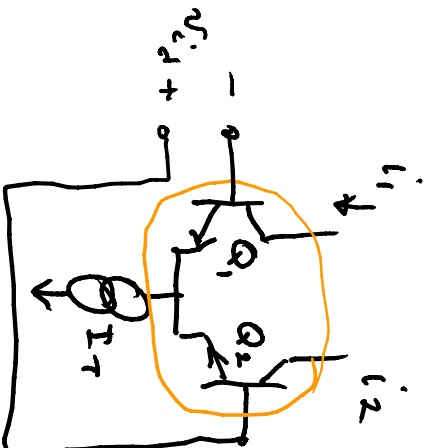
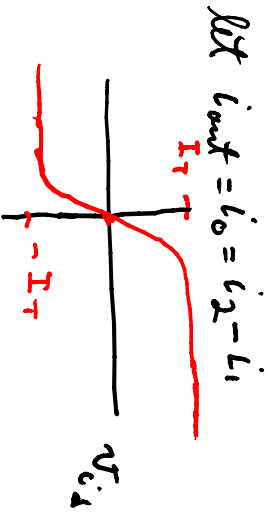
Darlington Transistor



page 613 = BJT differential pair

page 595 = MOS (OTA)

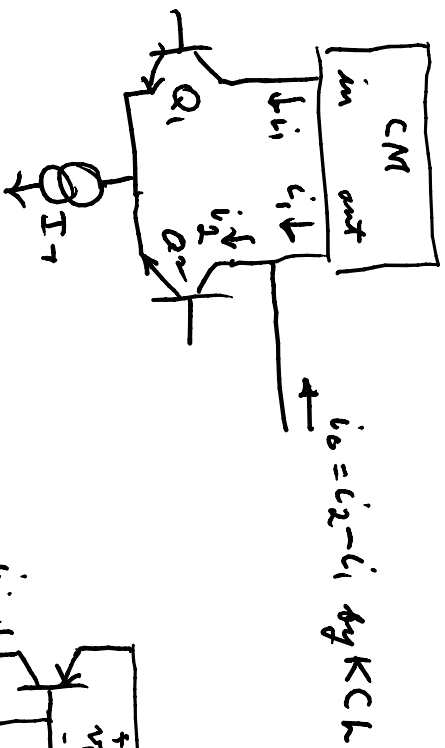
BJT



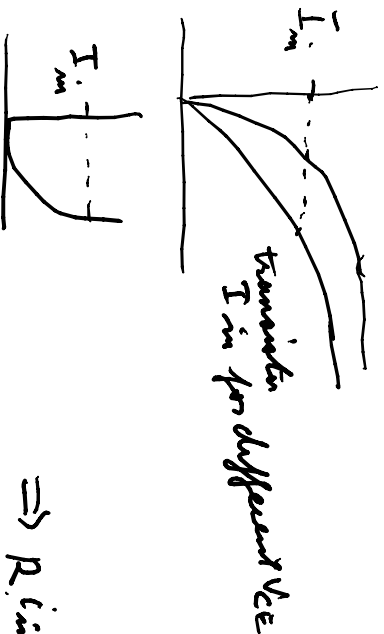
$I_T = \text{tail current}$

$$i_o \approx I_T \tanh(V_{id}/2V_T)$$

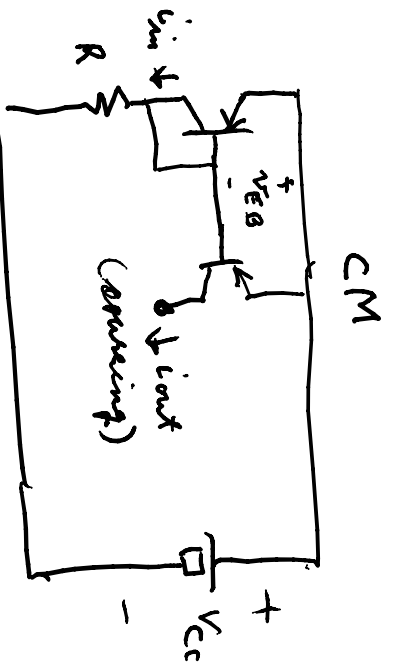
To get the difference $i_2 - i_1$ use a current mirror



$$i_o = i_2 - i_1 \text{ by KCL}$$



transmits I_m for different V_{id}



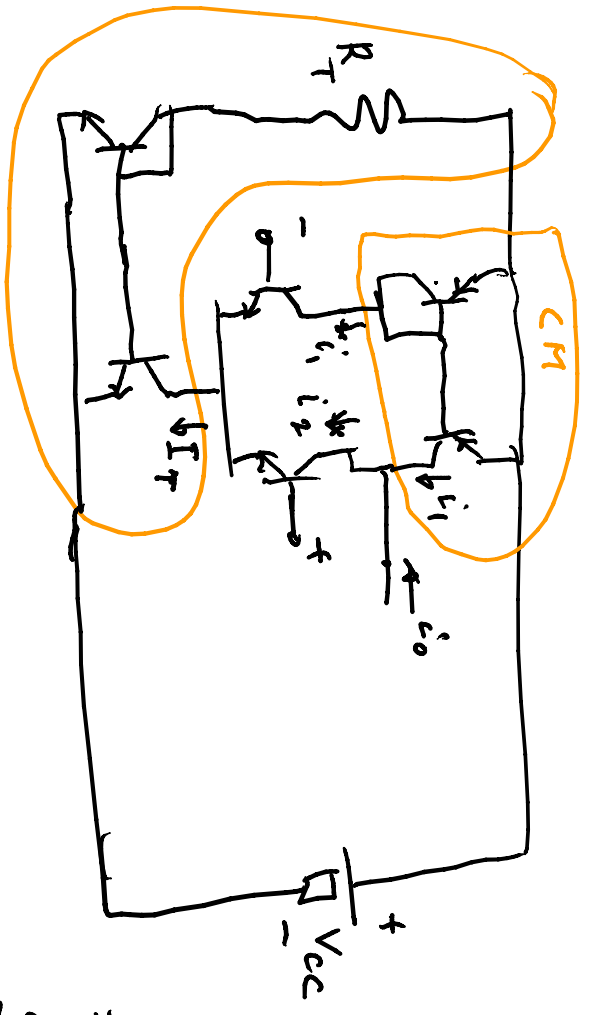
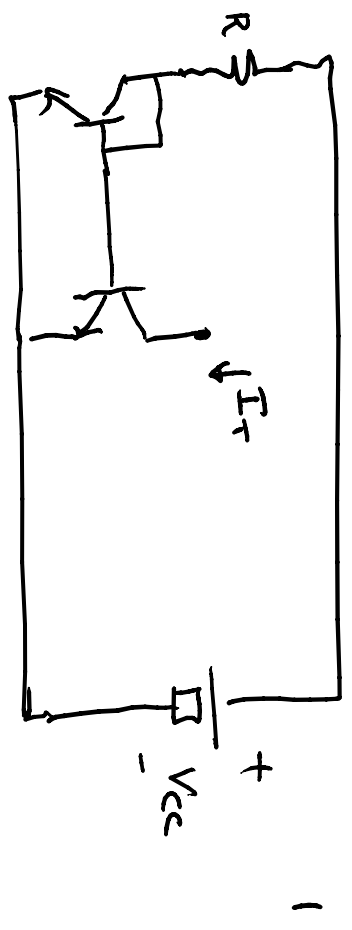
choose R for give input current

$$R \approx V_{EB} / 0.7$$

$$\Rightarrow R \cdot i_{in} = V_{CC} - V_{EB} \approx V_{CC} - 0.7$$

$$R = (V_{CC} - 0.7) / (I_T/2)$$

I_T makes I_T do the same but "sink" the current

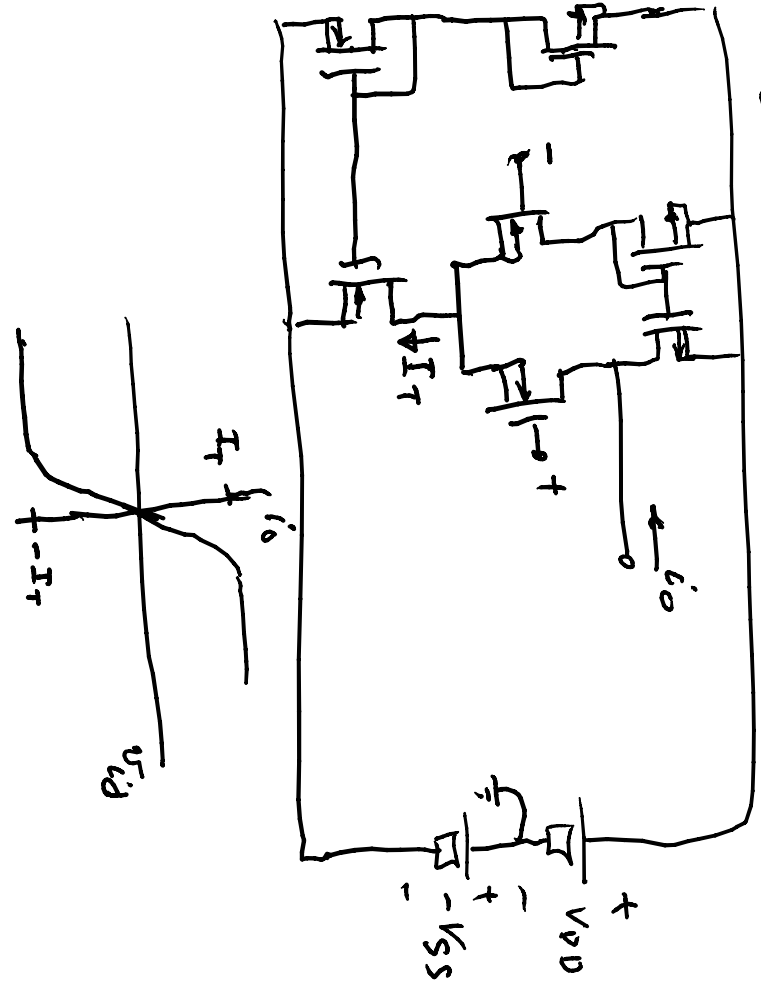


this is a
 DVCVS
 "differential
 voltage
 controlled
 voltage
 source"
 = OTA =
 operational
 transcon-
 ductance

For MOS

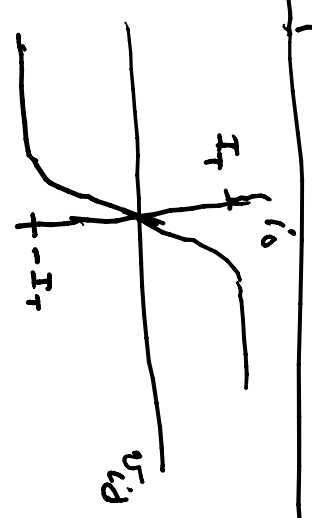
amplifier

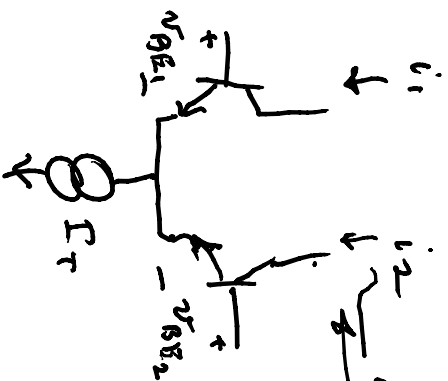
load
 $\approx R_{noise}$



1.595

For BST differential pair





$$-i_{E1} = I_{SE} e^{v_{BE1}/V_T}$$

$$-i_{E2} = I_{SE} e^{v_{BE2}/V_T}$$

$$i_1 = \alpha(-i_{E1})$$

$$i_2 = \alpha(-i_{E2})$$

if ignores i_{BQ} : KCL $\Rightarrow 0 = i_1 + i_2 - I_T \Rightarrow I_T = i_1 + i_2$
 $i_0 = i_2 - i_1$

$$v_{i_d} = v_{BE2} - v_{BE1}$$

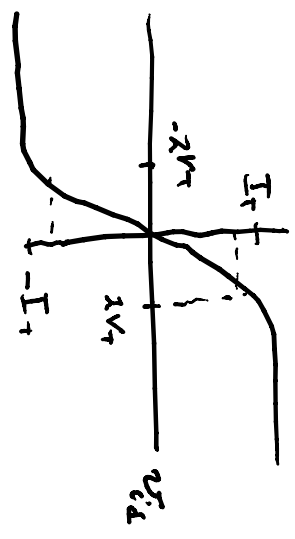
$$I_T = \alpha \left(e^{v_{BE2}/V_T} + e^{v_{BE1}/V_T} \right) = \alpha e^{v_{BE1}/V_T} \left(e^{(v_{BE2} - v_{BE1})/V_T} + 1 \right)$$

$$i_0 = \alpha \left(e^{v_{BE2}/V_T} - e^{v_{BE1}/V_T} \right) = \alpha e^{v_{BE1}/V_T} \left(e^{(v_{BE2} - v_{BE1})/V_T} - 1 \right)$$

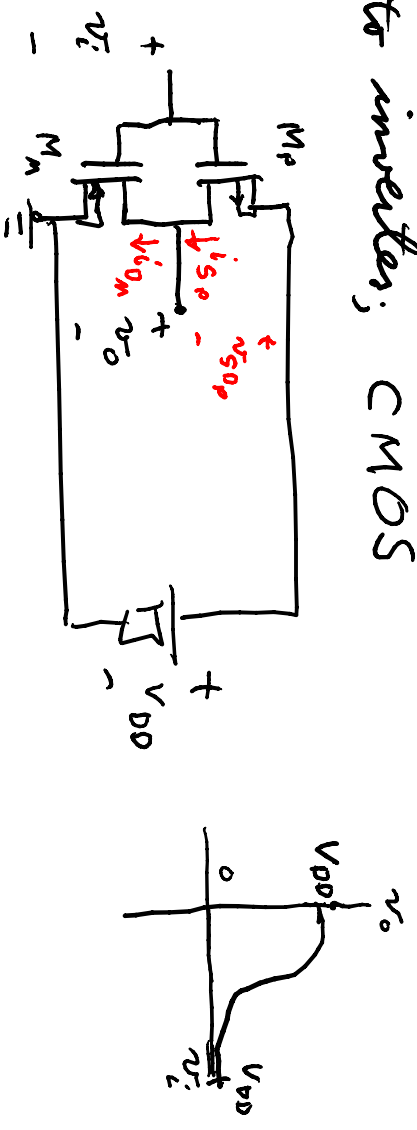
$$\frac{i_0}{I_T} = \frac{e^{v_{i_d}/V_T} - 1}{e^{v_{i_d}/V_T} + 1} = \frac{e^{v_{i_d}/V_T} - e^{-v_{i_d}/V_T}}{e^{v_{i_d}/V_T} + e^{-v_{i_d}/V_T}} = \tanh\left(\frac{v_{i_d}}{2V_T}\right)$$

$$i_o = I_T \cdot \tanh(v_{i2}/2V_T)$$

(also need k_m)



Turn to inverter; CMOS



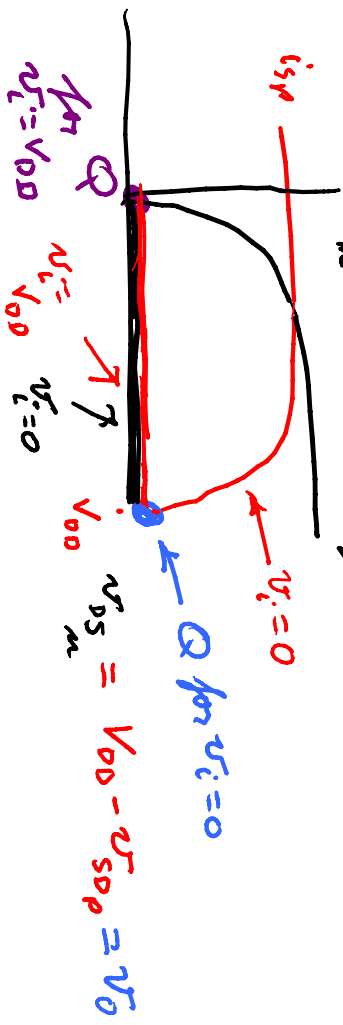
Take M_n as main transistor & M_p as load
 here if $v_i = 0$ then $v_o = V_{DD}$ & no current flows
 in either transistor as M_n is turned off
 as $V_{GS_n} < V_{TO_n}$. When $v_i = V_{DD}$ then M_p is off

As no current flows when v_i is 0 or V_{DD}

and then $v_0 = V_{DD}$ or 0

i_D for M_n

$v_i = V_{DD}$
 $v_i = v_{GSn}$



$\Rightarrow v_0 = V_{DD}$ when $v_i = 0$

$v_0 = 0$ when $v_i = V_{DD}$