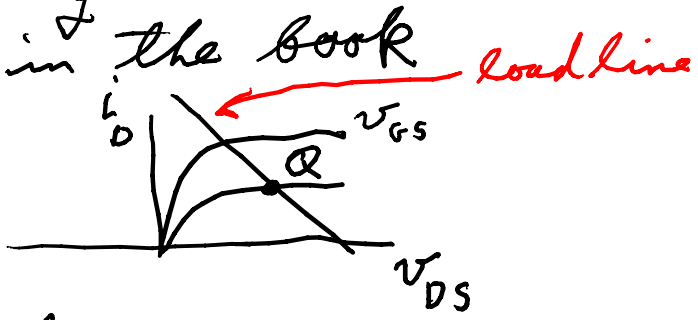
EE303

Note Title 1/24/2011

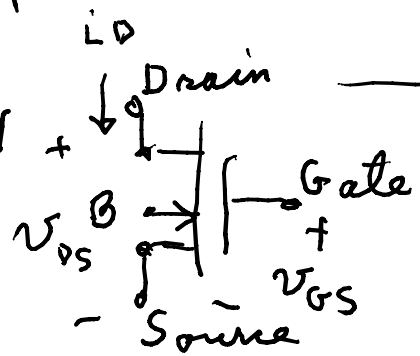
1st = get PSpice running

2nd = look at figures in the book

p274, fig. 5.31

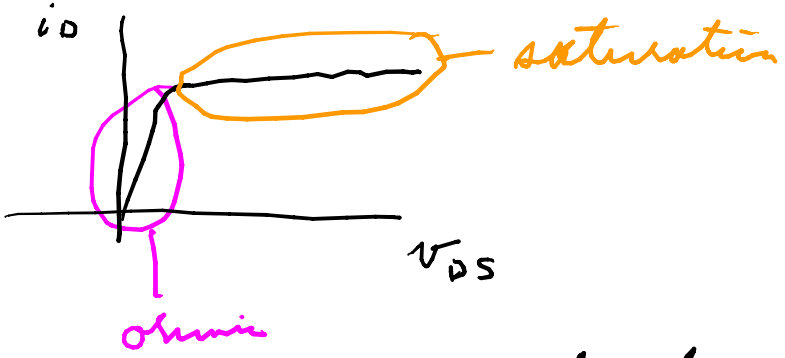


P.233, Fig. 5.1

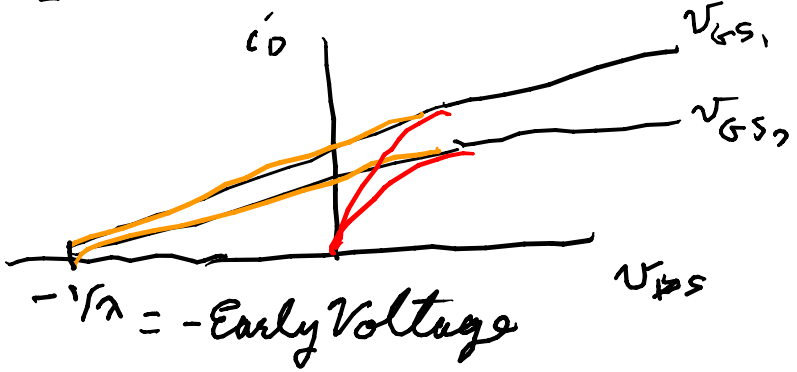


P. 243, eq. (5.20) $i_D = \frac{1}{2} k'_n \frac{W}{L} (v_{GS} - V_t)^2$ for saturation operation

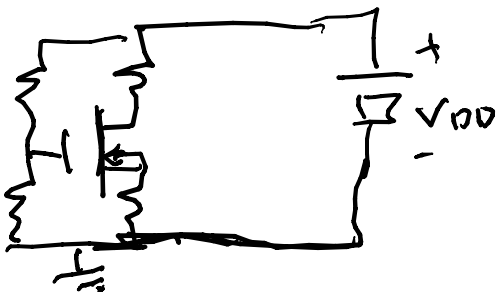
P. 241, eq (5.16) $i_D = k'_n \left(\frac{W}{L}\right) \left\{ (v_{GS} - V_t) v_{DS} - \frac{1}{2} v_{DS}^2 \right\}$ for triode = ohmic region



p. 254, eq. (5.23) $i_D = \frac{1}{2} k_n' \frac{W}{L} (v_{GS} - V_t)^2 (1 + \lambda v_{DS})$ in saturation



p. 308, Fig. 5.52 biasing (to put energy in

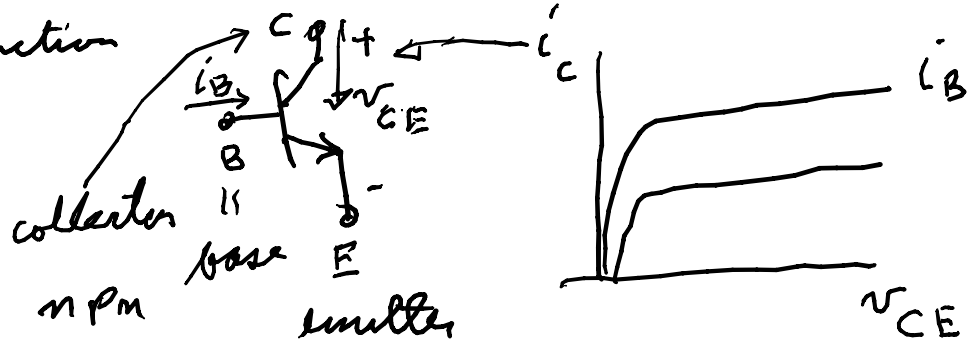


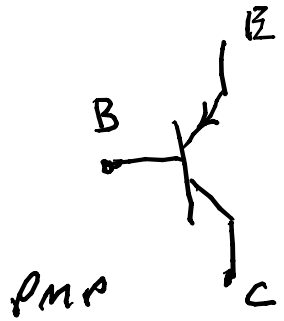
p. 366, 367

BJT

"bipolar"

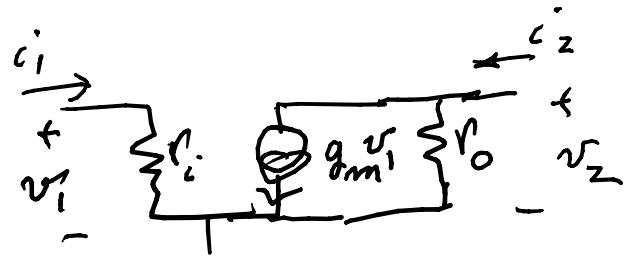
"eqs."



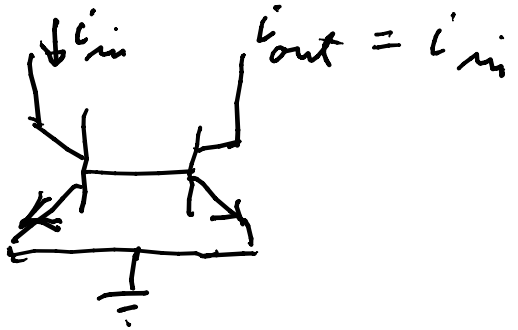


p. 410, Fig. 6.40

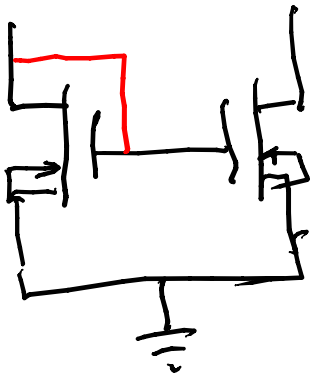
small signal admittance matrix equivalent



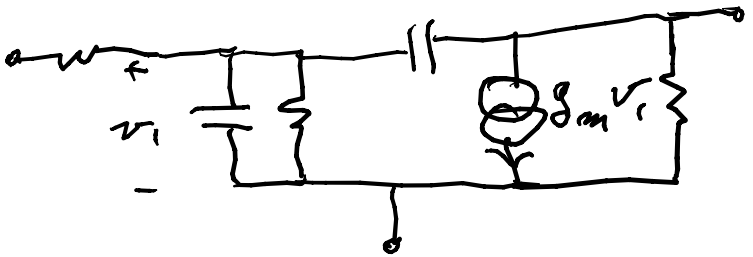
p. 452, Fig. 6.63 current mirror
npn



p. 528, Fig. 7.23
current mirror
NMOS

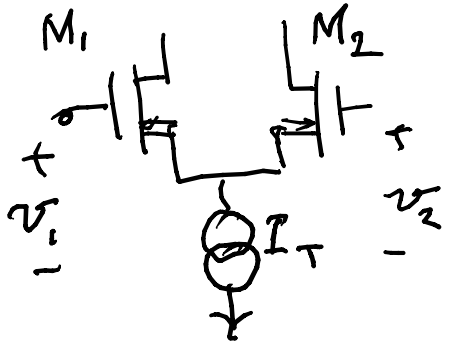


p. 558, Table 7.A.3



Hybrid π

P. 588, differential pairs



P. 738, fig. 9.22 amplifier equivalent for gain

P. 1133, inverter

P. 1153, pass transistor

P. 1114, Fig 15.31, 2 nor, nand (CMOS)

p. 1218, Fig. 15.12 SRAM

p. 1225, Fig 15.18 DRAM