

ENEE 303 Final Exam – Part two, one hour in class, Spring 2011

50 points, open book, open notes but not open computer. Good luck!

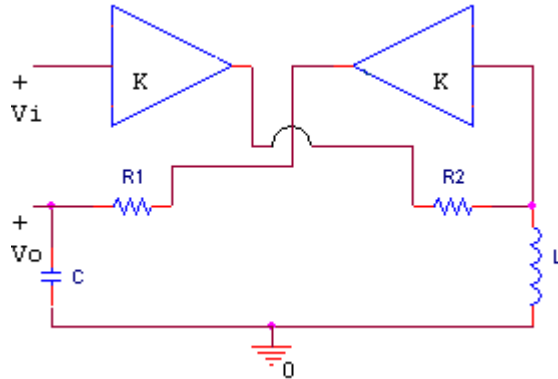
The 4007 Spice model parameters to be used are:

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.model M4007N nmos(Level=1 Tox=300n KP=20.54u W=144u L=8u VTO= 1.3  
+ LAMBDA=15m Cbd=4p Cbs=4p)  
.model M4007P pmos(Level=1 Tox=300n KP=10.32u W=328u L=8u VTO=-1.5  
+ LAMBDA=15m Cbd=8p Cbs=8p)
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1. (25 points; 15 minutes)

The following circuit is to be designed to oscillate. Assuming ideal voltage controlled voltage sources of equal gain K ,

- a) Find the transfer function $T(s) = \frac{V_o}{V_i}(s)$, where V_o and V_i are measured with respect to ground. Give also zeros and poles of $T(s)$.
- b) Place a short between the input and output nodes, which sets $V_o=V_i$. The circuit can then become an oscillator. Give the conditions for oscillation and the oscillation frequency.



2. (25 points, 15 minutes)

A 4007 NMOS transistor is operated as a current source for 2mA at a load voltage across it of $V_{DS} = 5V$. Give the designed V_{GS} when $LAMBDA$ is ignored and when it is taken into account and compare.

3. (50 points, 30 minutes)

Use the following ideal model for the OTA (with also zero currents into the + & - leads) for which I_T and V_S are real and positive.

$$I_o = \begin{cases} I_T & \text{for } V_S \leq V_{id} \\ G_m V_{id} & \text{for } -V_S \leq V_{id} \leq V_S \\ -I_T & \text{for } V_{id} \leq -V_S \end{cases}$$

For the following circuit:

- Give the equation for the load curve of I_L versus V_o and graph the curve.
- Graph $-I_L$ on the OTA curve of I_o versus V_o when $V_i = 0$ for the two cases of $G_m < G = 1/R$ and $G_m > G = 1/R$
- The circuit has hysteresis for certain values of $G = 1/R$. Determine the range of those values.
- Graph V_o versus V_i to show the hysteresis for the values of R of part c) and give the hysteresis output values and input voltage jump point values. .

