ENEE 417 Spring 2010   Microelectronics Design Laboratory
Time Schedule: "lectures" M 10:00-10:50 EGR 1104; Laboratory AVW 1334
Sections: M 14:00-16:50 or Th 14:00-16:50

Week of Monday	Week of Monday
01/25/10 Familiarize with equipment LabVIEW & DAQ tutorials; DAQ for transistor curves Tektronix and Spice transistor curve tracing Initiate Basic Paper choice	02/01/10 Spice, LabVIEW & DAQ tutorials; GPIB control Basic Paper preliminary choice Spice modeling from curve tracings Special parts list
02/08/10 Final Basic Paper choice Learn MAGIC and VLSI layout Op-amp curve tracer Parts List for Basic Paper	02/15/10 Op-amp curve tracer Learn MAGIC and VLSI layout
02/22/10 Ring Oscillator Using DAQ & LabView	03/01/10 Voltage Doubler Using DAQ & LabView
03/08/10 Negative impedances DAQ & LabView	03/15/10 Spring Break
03/22/10 Base paper designs Catch up on all test projects using DAQ & LabView	03/29/10 Basic paper circuit design & testing
04/05/10 Basic paper circuit measurements Base paper circuit VLSI layout	04/12/10 Finish Base paper circuit
04/19/10 Finish Base Paper Circuit and Power Point for Presentations	04/26/10 Oral Reports in Laboratory Using Power Point
05/03/10 Last week of Labs Written Reports on Base Paper Circuit Due	05/10/10 Last class
Final scheduled W 05/19/10; 08:00-10:00	