ENEE 417 S10 paper choices 04/16/10			
Section 1 M 2-5			
Ai, Jonathan T			
R. Spawi, R. L. S. Chee, S. K. Sahari, and	l N. Julai. "Performan	L ce of CMOS Sch	mitt
Trigger," Proceedings of the International			
Kuala Lumpur, May 13 - 15, 2008, pp. 1317		ي ا	٠,
Cascio, Michael Louis			
J. Yoon, H. Kim, C. Park, J. Yang, H. Song	, S. Lee and B. Kim,	"A New RF CMOS	Gilbert
Mixer with Improved Noise Figure and Linearity," IEEE Transactions on Microwave Theory			
and Techniques, Vol. 56, No. 3, March 2008	3, pp. 626 - 631.	T	
Murphy, Owen Francis			
Received 03/25/10			
B. J. Plunkett and L. L. Kushner, "Foot Co	<del>_</del>		
Circuit for Musical Instruments," US Pater	1 3,530,224 Issued Se	ptember 22, 13	Tu. 6 pages.
Partovi, Bashir M. Milkovic, "Current Gain High-Frequency	CMOS Operational Amai	ifiere " TEEE	Journal of
Solid-State Circuits, Vol. SC-20, No. 4, A			JULIIAL UL
Turner, John Andrew	inguse 1903, pp. 013	031	
L. Wang, Y. Fukatsu, and K. Watanabe, "Cha	l practerization of Curr	L ent-Mode CMOS	L R-2R Ladder
Digital-to-Analog Converters," IEEE Transa			
50, No. 6, December 2001, pp. 1781 - 1785.			•
Wienecke, Steven Michael			
R. W. Newcomb, "Neural-Type Microsystems:	Some Circuits and Con	siderations,"	Proceedings
of the IEEE International Conference on Ci	rcuits and Computers,	ICC80, Octobe	r 103, 1980,
Port Chester, NY, Vol. 2, pp. 1072 - 1074.			
Section 2 Th 2-5		T	
Cervino, Noel Pilip			
Y. Savaria, D. Chtchvyrkov, and J. F. Currie, "A Fast CMOS Voltage-Controlled Ring Oscillator," Proceedings of ???, IEEE / oo, 359 – 362.			
Doha, Arish			
G. Palmisano and S. Pennisi, "High-frequency CMOS an	nolifier with improved bandwig	l dth nerformance " l	=lectronics
Letters, Vol. 35, No. 14, July 8, 1999, pp. 1126 – 1127.	ipinier with improved bandwic	atti periormanee, i	_10011011103
04/12/10 replaces			
C. R. Gauthier and R. B. Brown, "Active Current Mirror R	eceiver," Proceedings of the	IEEE Midwest Sym	posium on
Circuits and Systems, Lansing MI, August 8-11, 2000, pp	o. 706 – 709		
Hicks, Jasmine Sade'			
T. Serrano-Gotarrredona, B. Linares-Barranco, and A. G	. Andreou, "Very Wide Range	Tunable CMOS/B	ipolar Current
Mirrors with Voltage Clamped Input," IEEE Transactions	on Circuits and Systems -1, \	Vol. 46, No. 11, No	vember 1999,
pp. 1398 – 1407.		T	
Mumper, James Shenk			
P. J. Lim and B. A. Wooley, "A High-Speed Sample-and-	Hold Technique Using a Mille	er Hold Capacitanc	e," Proceedings
of the 1990 Symposium on VLSI Circuits, pp. 31 – 32.		T	
Perez, Edwin David			
G. Nicollini, P. Confalonieri, and D. Senerowicz, "A Fully Differential Sample-and-Hold Circuit for High-Speed			
Applications," IEEE Journal of Solid-State Circuits, Vol. 2	:4, №. 5, October 1989, pp. 1	461 – 1465.	
Roberts, Kyle Andrew	I I I I I I I I I I I I I I I I I I I		- L - II
L. Ravezzi, D. SToppa and GF. Dalla Betta, "Simple hig	gn-speed CMOS current comp	parator," Electronic	s Letters, Vol.
33, No. 22, October 23, 1997, pp. 1829 – 1830. 04/15/10 Replaces			
G. M. Yin, F. Op'tEynde, and W. Sansen, "A High-Speed	CMOS Comparator with 9 h	Resolution " IEEE	Journal of Solid
G. W. Till, F. Op (Lyride, and W. Sansen, A High-Speed	OwiCO Comparator with 6-D	TICOUIUIIOII, ILLE	ocurriai di Suliu-

State Circuits, Vol.. 27, No. 2, February 1992, pp. 208 – 211.