

ENEE 303 Analog and Digital Electronics – Base Paper Choices

name	Section/ email	First talk date	Second talk date	Commentator dates
Paper				
Amirtharaj, Mark Jeyaseela		Th 04/01/10 #4 RWN	Tu 05/04/10 #4 RWN	Tu 03/30/10 #2 RWN Tu 05/04/10 #2 RWN
S. R. Bandi and P. R. Mukund, "A Compensation Technique for Transistor Mismatch in Current Mirrors," Proceedings of the IEEE Conference on ???, 2004, IEEE Xplore, pp. 320 - 323				
Atahua, Alex		Th 04/10/10 #5	Tu 04/20/10 #2	Th 04/01/10 #3 RWN Tu 04/27/10 #4 RWN
M. E. Kim, A. K. Oki, G. M. Gorman, D. K. Umemeoto, and J. B. Camou, "GaAs Heterojunction Bipolar Transistor Device and IC Technology for High-Performance Analog and Microwave Applications," IEEE Transactions on Microwave Theory and Techniques, Vol. 37, No. 9, September 1989, pp. 1286 - 1296+?				
Betancourt, Allison Barret		Th 04/01/10 #6	Th 04/22/10 #3	Tu 04/06/10 #4 Tu 04/20/10 #2
Y. K. Ramadass and A. P. Chandrakasan, "An Efficient Piezoelectric Energy Harvesting Interface Circuit Using a Bias-Flip Rectifier and Shared Inductor," IEEE Journal of Solid-State Circuits, Vol. 45, No. 1, January 2010, pp. 189 - 204.				
Bishop, Samuel Stennis		Th 04/01/10 #3	Th 04/22/10 #2	Th 04/01/10 #1 RWN Tu 05/04/10 #1
Dropped 04/16/10 M. Mobarak, M. Onabajo, J. Silva-Martinez, and E. Sanchez-Sinencio, "Attenuation-Predistortion Linearization of CMOS OTAs with Digital Correction of Process Variations in OTA-C Filter Applications," IEEE Journal of Solid-State Circuits, Vol. 45, No. 2, February 2010, pp. 351 - 367.				
Bledsoe, Benjamin Christop		Th 04/08/10 #5	Tu 05/04/10 #3	Th 04/01/10 #2 RWN Tu 04/27/10 #3 RWN
T. K. Horiuchi, "A Spike-Latency Model for Sonar-Based Navigation in Obstacle Fields," IEEE Transactions on Circuits and Systems - I, Vol. 56, No. 11, November 2009, pp. 2393 - 2401.				
Boyette, Keanne Leigh		Tu 04/06/10 #4	Tu 04/27/10 #3	Tu 04/06/10 #5 Th 04/22/10 #4 RWN
E. Tielo-Cuautle, L. A. Sarmiento-Reyes, "Biasing Analog Circuits Using the Nullor Concept," Proceedings of the IEEE Conference on ???, IEEE Xplore, 2000, pp. 27 - 30.				
Carrillo, Estefany C		Tu 04/06/10 #7	Tu 04/27/10 #1	Tu 03/30/10 #3 RWN Th 04/29/10 #2

K. D. Clark and D. J. Comer, "Tuned negative capacitance circuitry for CMOS amplifier bandwidth extension," International Journal of Electronics, Vol. 96, No. 1, January 2009, pp. 1 - 9.				
Chibsa, Gemechis Hailu		Th 04/08/10 #6 RWN	Th 04/22/10 #1 RWN	Tu 03/30/10 #1 RWN Tu 04/13/10 #2 RWN
[Received 03/11/10] Y.-H. Yu, Y.-S. Yang and Y.-J. Chen, "A Compact Wideband CMOS Low Noise Amplifier with Gain Flatness Enhancement," IEEE Journal of Solid-State Circuits, Vol. 45, No. 3, March 2010, pp. 502 - 509.				
Chiosi, Brian James		Th 04/01/10 #1 RWN	Tu 04/20/10 #4 RWN	Th 04/01/10 #6 RWN Th 04/22/10 #3 RWN
Dropped 04/16/10				
Duty, Matthew Heffner		Th 04/08/10 #4	Tu 05/04/10 #2	Th 04/01/10 #7 RWN Th 04/22/10 #1 RWN
Y. V. Pershin and M. DeVentra, "Practical Approach to Programmable Analog Circuits with Memristors," IEEE Transactions on Circuits and Systems - I, Vol. ?, No. ? (to be published; now on IEEE Xplore), pp. 1 - 8.				
Ebinama, Chima Jeffery		Tu 03/30/10 #4	Tu 04/13/10 #1 RWN	Th 04/08/10 #1 Th 04/15/10 #1
Y. Shim, C.-W. Kim, J. Lee, and S.-G. Lee, "Design of Full Band UWB Common-Gate LNA," IEEE Microwave and Wireless Components Letters, Vol. 17, No. 10, October 2007, pp. 721 - 723.				
Etesam, Amirhesam		Th 04/08/10 #1	Th 04/29/10 #3	Tu 04/06/10 #1 RWN Tu 04/20/10 #4 RWN → 04/20/10 Tu 05/04/10 #1
M. Milkovic, "Current Gain High-Frequency CMOS Operational Amplifiers," IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 4, August 1985, pp. 845 - 851. Changed (02/28/10) from: D. Y. Kim, O. S. Kwon, and J. G. Bang, "The Design of the High Speed Amplifier Circuit for Using in the Analog Subsystems," Proceedings of the IEEE Conference on ???, 1992, IEEE Xplore, pp. 485 - 488.				
Gomez, Raul Antonio		Th 04/08/10 #2	Th 04/29/10 #2	Tu 03/30/10 #6 RWN Tu 04/27/10 #1
C.-H. Lin, C.-Y Hsieh, and K-H. Chen, "A Li-Ion Battery Charger with Smooth Control Circuit and Built-In Resistance Compensator for Achieving Stable and Fast Charging," IEEE Transactions on Circuits and Systems - I, Vol. 57, No. 2, February 2010, pp. 506 - 517.				
Gregory, Jason Michael		Tu 04/06/10 #2	Tu 04/27/10 #4	Tu 03/30/10 #4 Tu 04/13/10 #1
C. Laoudias and C. Psychalinos, "Low-voltage CMOS adjustable current mirror," Electronics Letters, Vol. 46, No. 2, January 21, 2010, pp.??				

Harris, Michael Raymond		Th 04/01/10 #7	Tu 04/13/10 #2 RWN	Tu 04/06/10 #6 Th 04/22/10 #2 → 04/20/10 Th 04/22/10 #3
H. Sato, A. Hyogo, and K Sekine, "A Low Voltage OTA Using MOSFET in the Triode Region and Cascode Current Mirror," Proceedings of the ???, from IEEE Xplore, pp. 1 - 4.				
Hasan, Mahmudul		Tu 03/30/10 #7 RWN	Tu 04/13/10 #3 RWN	Th 04/01/10 #5 Tu 04/20/10 #1
D. Y. Kim, O. S. Kwon, and J. H. Bang, "The Design of the High Speed Amplifier Circuit for Using in the Analog Subsystems," Proceedings of ???, from IEEE Xplore, pp. 485 - 488.				
Kent, Stephen J		Tu 03/30/10 #5 RWN	Tu 04/13/10 #3 RWN	Th 04/08/10 #3 Th 04/29/10 #4
R. S. Assaad, and J. Silva-Marinez, "The Recycling Folded Cascode: A General Enhancement of the Folded Cascode Amplifier," IEEE Journal of Solid-State Circuits, Vol. 44, No. 9, September 2009, pp. 2535 - 2542.				
Li, Wen		Tu 04/06/10 #1	Th 04/22/10 #4	Tu 04/06/10 #7 Tu 04/13/10 #3 RWN
M. Safi-Harb and G. W. Roberts, "Embedded Measurement of GHz Digital Signals with Time Amplification in CMOS," IEEE Transactions on Circuits and Systems - I, Vol. 55, No. 7, August 2008, pp. 1884 - 1896.				
Mathes, David Brian		Tu 04/06/10 #3	Th 04/27/10 #2	Th 04/08/10 #2 Tu 04/13/10 #4 RWN
J.-M. Redoute and M. S. J. Steyaert, "EMI-Resistant CMOS Differential Input Stages," IEEE Transactions on Circuits and Systems - I, Vol. 57, No. 2, February 2010, pp. 323 - 331.				
Mazilu, Mihai Tiberiu		Tu 03/30/10 #2	Tu 04/20/10 #1	Th 04/08/10 #4 RWN Tu 04/20/10 #3 RWN
R. M. Sirsi, P. J. Meza, D. P. Laude, and R. C. Strawbrich, "Integrated Electronic Subscriber Line Interface Circuit with Ringing on Chip," IEEE Journal of Solid-State Circuits, Vol. SC-18, No. 6, December 1983, pp. 665 - 677.				
O'Donnell, John H		Tu 03/30/10 #3 RWN	Th 04/15/10 #4 RWN	Th 04/01/10 #4 Tu 05/04/10 #4
Received 03/29/10: M. Somdunayakanok, T. Pattanthadapong and P. Prommee, "Accurate Tunable Current-mirror and Its Applications," Proceedings of the 2008 International Symposium on Communications and Information Technologies, ISCIT 2008, pp. 56-61, via IEEE explore,				
Palmiotto, Michael J		Tu 04/06/10 #6	Tu 05/04/10 #1	Tu 03/30/10 #5 RWN Th 04/29/10 #1
E. Yuce "A Novel CMOS-Based Voltage-Mode First-Order Phase Shifter Employing a Graded Capacitor," Circuits Systems and Signal Processing, Vol. 29, 2010, pp. 235 - 245. Changed on 04/20/10 from R. Raut and N. Guo, "A Few CMOS Transconductors Optimized for Low Power and Wide Band Operation," Circuits, Systems and Signal Processing, Vol. 20, No. 6, 2001, pp. 655 - 675.				

Paruchuri, Vineet Kumar		Tu 03/30/10 #1 RWN	Tu 04/15/10 #2 RWN	Th 04/08/10 #7 RWN Th 04/15/10 #4 RWN
Dropped 04/16/10				
Saboori, Alireza		Th 04/08/10 #3	Th 04/29/10 #4	Tu 03/30/10 #7 Th 04/29/10 #3
S. K. Webster, "An All-Transistor Hearing Aid," Transactions of the IRE (on Adio?) 1954, pp. 65 - 70.				
Sczudlo, Walter Christop H		Tu 04/06/10 #5	Th 04/29/10 #1	Th 04/08/10 #5 Tu 05/04/10 #3
E. Seevinck, "A current sense-amplifier for fast CMOS SRAMs," Proceedings of the 1990 Symposium on VLSI Circuits, from IEEE Xplore, pp. 71 - 72.				
Thomas, Nicole Venyse		Tu 03/30/10 #6	Th 04/15/10 #3 RWN	Tu 04/06/10 #2 Th 04/15/10 #3 RWN
S.-B. Park, J. E. Wilson, and M. Ismail, "Peak Detectors for Multistandard Wireless Receivers," IEEE Circuits & Devices Magazine, Vol. ?, No.?, November/December 2006, pp. 6 - 9.				
Tucci, Phillip M		Th 04/01/10 #2	Tu 04/20/10 #3	Tu 04/06/10 #3 Tu 04/27/10 #2
V. Dhanasekaran, J. Silva-Martinez, and E. Sanchez-Sinencio, "Design of Three-Stage Class-AB 16 Ohm Headphone Driver Capable of Handling Wide Range of Load Capacitance," IEEE Journal of Solid-State Circuits, Vol. 44, No. 6, June 2009, pp. 1734 - 1744.				
Yebassew, Mewael		Th 04/08/10 #7	Th 04/15/10 #4 RWN	Th 04/08/10 #6 RWN Th 04/15/10 #3 RWN
[Received 03/11/10] J. Wang, T. Zhao, J. Li, A. Q. Huang, R. Callanan, F. Husna, and A. Agarwal, "Characterization, Modeling, and Application of 10-jV SiC MOSFET," IEEE Transactions on Electron Devices, Vol. 55, No. 8, August 2008, pp. 1798 - 1804.				

Number of students = 28 => 25 (04/16/10)