

ENEE 303 Analog and Digital Electronics – Base Paper Choices

name	Section/ email	First talk date	Second talk date	Commentator dates
Paper				
Abdelsalam, Mahmoud Mohame		04/09/09 #3	04/30/09 #2	04/09/09 #2 04/30/09 #1
C. L. Ler, A. K. B. A'ain and A. V. Kordesh, "CMOS source degenerated differential active inductor," Electronics Letters, Vol. 44, No. 3, January 31, 2008, pp. ???				
Al-Turki, Abdulaziz Musaad		04/09/09 #2	04/30/09 #1	04/09/09 #3 04/30/09 #2
M. M. Reja, I. M. Filanovsky and K. Moez, "Wide tunable CMOS active inductor," Electronics Letters, Vol. 44, No. 25, December 4, 2008, pp. ???				
Finkelstein, Mark		04/07/09 #2	04/23/09 #1	03/31/09 #2 Missed ==> 04/09/09 #1 04/16/09 #1
A. A. Mazreah, M. R. Sahebi, M. T. Manzuri, and S. J. Hosseini, "A Novel Zero-Aware Four-Transistor SRAM Cell for High Density and Low Power Cache Application," Proceedings of the 2008 International Conference on Advanced Computer Theory and Engineering (IEEE Computer Society), city?, date?, pp. 571-575. Replaced 04/07/09:S. Suzuki and M. Hirata, "Threshold Difference Compensated Sense Amplifier," IEEE Journal of Solid-State Circuits, Vol. SC-14, No. 6, December 1979, pp. 1066 - 1070.				
Fuller, Lauren Michelle		04/02/09 #3	04/28/09 #2	04/02/09 #1 05/05/09 #1
S.-B. Park, J. E. Wilson and M. Ismail, "Peak Detectors for Multistandard Wireless Receivers," IEEE Circuits & Devices Magazine, Vol. ??, No. ??, November/December 2006, pp. 6 - 9.				
Giles, Nathan Louis		03/31/09 #2	04/14/09 #1	04/02/09 #3 04/23/09 #1
C.-K. Pham, M. Korehisa and M. Tanaka, "Chaotic Behavior and Synchronization Phenomena in a Novel Chaotic Transistors Circuit," IEEE Transactions on Circuits and Systems - I, Vol. 43, No. 12, December 1996, pp. 1006 - 1011.				
Kaya, Asaf		04/09/09 #1	04/21/09 #1	04/07/09 #2 ==> 03/31/09 #1 04/21/09 #2
R. Yu and Y. P. Xu, "Bandpass Sigma-Delta Modulator Employing SAW Resonator as Loop Filter," IEEE Transactions on Circuits and Systems - I, Vol. 54, No. 4, April 2007, pp. 723 - 734.				
Luo, George Chu		03/31/09 #1	04/21/09 #2	04/09/09 #1 ==> 03/31/09 #2 04/28/09 #2
Y. B. Guo and K. W. Current, "Voltage Comparator Circuits for Multiple-Valued CMOS Logic," Proceedings of the 32 nd IEEE International Symposium on Multiple-Valued Logic, 2002, pp. ???				
Nguetsap, Liliane Niapiamb		04/07/09 #1	04/23/09 #2	04/02/09 #4 04/28/09 #1
H. D. Dammak, S. Bensalem, S. Zouari M. Loulou, "Design of Folded Cascode OTA in Different Regions of Operation through gm/ID Methodolgy," International Journal of Electrical Systems Science and Engineering, Vol. 1, No. 3, Summer 2008, pp. 178 - 183.				

Roberts, Kyle Andrew		04/02/09 #1	04/28/09 #1	04/02/09 #2 04/14/09 #1
???, "???", Circuits and Devices Magazine, Vol. ??, No. ??, November 2000, pp. ??? - 31.				
Sovich, Nicholas R		04/02/09 #4	04/14/09 #2	04/07/09 #1 04/23/09 #2
Rcd 04/01/09: B. E. Lobdell and J. B. Allen, "A model of the VU (volume-unit) meter, with speech applications," Journal of the Acoustical Society of America, Vol. 121, No. 1, January 2007, pp. 279 - 285.				
Yoo, Justin Chanwoo		04/07/09 #4	04/16/09 #1	04/07/09 #3 04/16/09 #2==> 05/05/09 #2
Y. Berg, O. Mirmoahari and S. aUNET, "Ultra Low Voltage AND, NOR, and XOR CMOS Gates," IEEE ????, 2008, pp. 846 - 849.				
Zhang, Brian Bonan		04/02/09 #2	05/05/09 #1	03/31/09 #1 Missed ==> 04/07/09 #2 04/21/09 #1
K. Sakamoto, N. Fuchigami, K. Takagawa and S. Ohtaka, "A Three-Terminal Intelligent Power MOSFET with Built-In Reverse Battery Protection for Automotive Applications," IEEE Transactions on Electron Devices, Vol. 46, No. 11, November 1999, pp. 2228 - 2234.				
Zhu, Mo		04/07/09 #3	04/16/09 #2==> 05/05/09 #2	04/07/09 #4 04/14/09 #2
L. Ding and P. Mazumder, "On Circuit Techniques to Improve Noise Immunity of CMOS Dynamic Logic," IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 12, No. 9, September 2004, pp. 910 - 924.				

Number of students = 13