

1. Design a BJT OTA to give  $I_{out}=I_T \tanh(V_{in}/(2V_T))$  using a differential pair formed from the 2N3904 transistors for the main differential pair and tail current and the 2N3906 transistors for the output current mirror.. Use  $V_{cc}=-V_{ee}=5V$  and the tail current to be 2mA using a circuit such as that of Figure 6.10 of page 569. Check operation by DC Spice simulations over the range  $V_{ee}<V_{in}<V_{cc}$  (use an F component as a load with a resistor on its output; ground one of the inputs [repeat with the other input grounded]).
2. Replace each BJT by an MOS one, use the 1.2 micron ones, and compare the outputs of the two over a range of inputs,  $V_{ss}=V_{ee}<V_{in}<V_{dd}=V_{cc}$ .
3. For the following circuit (for which the op-amps are ideal) show that

$$Z_{in}(s) = Z_0(s) + (Z_1(s) \cdot Z_3(s) \cdot Z_5(s)) / (Z_2(s) \cdot Z_4(s))$$

Evaluate, and comment upon the results, when

- a)  $Z_0=0, Z_1(s)=Z_2(s)=Z_3(s)=Z_5(s)=R, Z_4(s)=1/(Cs)$
- b)  $Z_0=0, Z_1=Z_3=Z_5=R, Z_2(s)=Z_4(s)=1/(Cs)$

