

ENEE 303 Analog and Digital Electronics – Base Paper Choices

name	email	First talk date	Second talk date	Commentator dates
Paper				
Abaza, Mohammed Saeed		Th 03/27/08 #3	Tu 04/29/08 #2	Tu 04/08/08 #3 Th 04/17/08 #2
I. A. Khan, "DC-to-DC Converters for Electric and Hybrid Vehicles," ??? ref data???, pp. 113 - 122.				
Achiam, Yoav		Th 04/03/08 #2	Tu 05/06/08 #1	Th 04/03/08 #3 Tu 05/06/08 #2
B. Fotouhi, "A;;-MOS Voltage-to-Current Converter," IEEE Journal of Solid-State Circuits, Vol. 36, No. 1, January 2001, pp. 147 - 151.				
Dinavahi, Venkat		Th 04/03/08 #4	Tu 04/15/08 #1	Tu 04/08/08 #5 Th 05/01/08 #1
J. F. Kruey and F. T. Duben, "Integrated Conditioned Or and Inhibited OR Logic Circuits," IEEE Journal of Solid-State Circuits, Vol. SC-I, No. 2, December 1966, pp. 81 - 85.				
Fu, Anqi		Tu 04/01/08 #5	Th 04/24/08 #2	Tu 04/08/08 #4 Tu 05/06/08 #3
Y. Zhang and C. E. Saavedra, "A Broadband CMOS Frequency Tripler Using a Third-Harmonic Enhanced Technique," IEEE Journal of Solid-State Circuits, Vol. 42, No. 10, October 2007, pp. 2197 - 2203.				
Graves, Thomas Isiah		Th 03/27/08 #5	Th 04/24/08 #3	Th 03/27/08 #4 Tu 04/15/08 #3
W. Steiger, "Frequency Discriminators Using Complementary Transistors," IEEE Transactions on Circuit Theory, Vol. 10, No.2, June 1963, pp. 206 - 212.				
Haghani, Nagveed Ebadollahz		Tu 04/08/08 #5	Tu 04/29/08 #1	Th 04/03/08 #4 Th 05/08/08 #3
04/02/08 W. J. McCalla, "An Integrated IF Amplifier," IEEE Journal of Solid State Circuits, Vol. SC-8, No. 6, December 1973 1973, pp. 440 - 447.				
Horn, Joshua J		Tu 04/01/08 #3	Tu 04/29/08 #3	Tu 04/01/08 #2 Th 04/24/08 #2
R. Genov and G. Cuwenberghs, "Dynamic MOS Sigmoid Array Folding Analog-to-Digital Conversion," IEEE Transactions on Circuits and Systems - I, Vol. 51, No. 1, January 2004, pp. 182 - 186.				
Kaur, Navjot		Th 04/03/08 #1	Th 04/24/08 #1	Th 04/08/08 #1 Tu 04/22/08 #3
H. Veenstra, G. A. M. Hurkx, D. vanGoor, H. Brekelmans, and J. R. Long, "Analyses and Design of Bias Circuits Tolerating Output Voltages above BVCEO," IEEE Journal of Solid-State Circuits, Vol. 40, No. 10, October 2005, pp. 2008 - 2018.				
Lee, Mathew		Tu 03/25/08 #1	Tu 04/22/08 #2	Tu 03/25/08 #3 Tu 04/22/08 #1
T. Mury and V. F. Fusco, "Inverse Class-E Amplifier with Transmission-Line Harmonic Suppression," IEEE Transactions on Circuits and Systems - I, Vol. 54, No. 7, July 2007, pp. 1555 - 1561.				
Leung, Jennifer Lourdes		Tu 04/08/08 #1	Tu 05/06/08 #3	Th 04/03/08 #1 Th 04/24/08 #1
K. Sanborn, D. Ma and V. Ivanov, "A Sub-1-V Low-Noise Bandgap Voltage Reference," IEEE Journal of Solid-State Circuits, Vol. 42, No. 11, November 2007, pp. 2467 - 2481.				
Lin, Willington		Th 03/27/08 #1	Th 04/10/08 #1	Th 04/08/08 #2

Chun				Th 04/10/08 #2
K.-Y. Toh, R. G. Meyer, D. C. Soo, G. M. Chin and A. M. Voshchenkov, "Wide-Band, Low-Noise, Matched-Impedance Amplifiers in Submicrometer MOS Technology," IEEE Journal of Solid-State Circuits, Vol. SC-22, No. 6, December 1987, pp. 1031 - 1040.				
Lindley, Adam Calr		Th 04/03/08 #3	Tu 05/06/08 #2	Th 04/03/08 #2 Tu 05/06/08 #1??
V. Kakani and F. F. Dai, "Design and Analysis of a Transversal Filter RFIC in SiGe Technology," IEEE Design & Test of Computers, 2008, pp. 10 - 16.				
Luo, Renfei		Th 04/01/08 #4	Th 04/17/08 #1	Tu 03/25/08 #1 Th 04/17/08 #3
X. Zhang and E. I. El-Masry, "A Novel CMOS OTA Based on Body-Driven MOSFETs and its Applications in OTA-C Filters," IEEE Transactions on Circuits and Systems - I, Vol. 54, No. 6, June 2003, pp. 1204 - 1211.				
Ma, Elisha		Tu 03/25/08 #1	Th 04/17/08 #3	Th 04/01/08 #4 Th 04/17/08 #1
R. A. Pease, "The Design of Band-Gap Reference Circuits: Trials and Tribulations," Proceedings of the IEEE 1990 Bipolar Circuits and Technology Meeting, ppr 9.3, ??? year and city???, pp. 214 - 218.				
McGraw, Robert H.		Tu 04/08/08 #3	Th 05/01/08 #1	Th 03/27/08 #3 Tu 04/29/08 #1
R. R. Harrison and C. Charles, "A Low-Power Low-Noise CMOS Amplifier for Neural Recording Applications," IEEE Journal of Solid-State Circuits, Vol. 38, No. 6, June 2003, pp. 958 - 965.				
Meekins, Daniel William		Th 04/03/08 #4	Th 05/08/08 #3	Th 04/03/08 #4 Tu 04/15/08 #1
C.-H. Huang and J.-S. Wang, "Hig-Performance and Power-Efficient CMOS Comparators," IEEE Journal of Solid-State Circuits, Vol. 38, No. 2, February 2003, pp. 254 - 262.				
Newby, Robert D R		Tu 03/25/08 #3	Tu 04/22/08 #1	Tu 03/25/08 #2 Tu 04/22/08 #2
V. von Kaenel, D. Aebischer, C. Piguet and E. Kijkstra, "A 320 MHz, 1.5mW @ 1.35V CMOS PLL for Microprocessor Clock Generation," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, pp. 1715 - 1722.				
Pennington, Shawn		Th 03/27/08 #4	Tu 04/15/08 #3	Th 03/27/08 #5 Th 04/24/08 #3
S.-B. Park, J. E. Wilson and M. Ismail, "Peak Detectors for Multistandard Wireless Receivers," IEEE Circuits and Devices Magazine, Vol., No., November/December 2006, pp. 6 - 9				
Subramanian, Arvind		Tu 03/25/08 #5	Th 05/01/08 #2	Tu 04/01/08 #1 Th05/01/08 #3
B. Gosselin, M. Sawan and C. A. Chapman, "A Low-Power Integrated Bioamplifier with Active Low-Frequency Suppression," IEEE Transactions on Biomedical Circuits and Systems, Vol. 1, No. 3, September 2007, pp. 184 - 192.				
Szrom, Jeffrey Michael		Tu 04/01/08 #1	Th 05/01/08 #3	Tu 03/25/08 #5 Th 05/01/08 #2
G. De Geronimo, A. Ksnfsdsmy and P. O'Connor, "Analog Peak Detector and Derandomizer for High-Rate Spectroscopy," IEEE Transactions on Nuclear Science, Vol. 49, No. 4, August 2002, pp. 1769 - 1773. 03/06/08:Replaced by above P. Corbishley and E. Rodriguez-Villegas, "A Nanopower Bandpass Filter for Detection of an Acoustic Signal in a Wearable Breathing Detector," IEEE Transactions on Biomedical Circuits and Systems, Vol. 1, No. 3, September 2007, pp. 163 - 170.				
Tiglao, Percival Ross		Tu 04/08/08 #4	Th 05/08/08 #2	Tu 04/01/08 #5 Tu 04/29/08 #2

M. L. Chiang, T. C. Lu and J. B. Quo, "Analogue adaptive neural network circuit," IEE Proceedings - G, Vol. 138, No. 6, December 1991, pp. 717 - 723.				
Wilhelm, Robert McDoniel		Tu 04/01/08 #2	Tu 04/22/08 #3	Tu 04/01/08 #3 Tu 04/29/08 #3
Yousufzai, Aamir Abbas		Tu 04/08/08 #2	Th 04/10/08 #2	Th 03/27/08 #1 Th 04/10/08 #1
D. Y. Kim, O. S. Kwon and J. H. Bang, "The Design of the High Speed Amplifier Circuit for Using in the Analog Subsystems," ??? reference data???, pp. 485 - 488.				
Zimsou, Merine Priscille		Th 03/27/08 #2	Th 04/17/08 #2	Tu 03/25/08 #4 RWN Th 05/08/08 #1 RWN
P. R. Gray and R. G. Meyer, "MOS Operational Amplifier Design - A Tutorial Overview," IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 6, December 1982, pp. 969 - 982.				

Number of students = 25