File: E:/courses/spring2007/303/hmwrk2.doc RWN 02/01/07
Homework Set 2 due Friday 02/09/07

1. [50 points] (small signal mos equivalent)

For the mnmosis transistor with $\mathrm{W}=2 \mathrm{~L}=20 \mathrm{u}$ plot in PSpice the curves of $\mathrm{i}_{\mathrm{D}}$ versus $\mathrm{v}_{\mathrm{DS}}$ with $\mathrm{v}_{\mathrm{GS}}$ as a parameter.
a) From those curves graphically determine the values for gm and ro for the following equivalent circuit when the transistor is biased at $\mathrm{V}_{\mathrm{GS}}=3 \mathrm{~V}=\mathrm{V}_{\mathrm{DS}}$.

b) Using the equations of the book, see page 255 , calculate gm and ro and compare with those found in part a).
c) repeat parts $a$ ) and b) for $V_{G S}=4 V=V_{D S}$.
2. [50 points] (load line on NMOS curves)

Use the transistor of problem 1 above and apply a drain bias battery, Vdd, of voltage 5 V and a gate DC supply to set the Q point.
a) Determine the value of a resistor $R_{D}$ to give the $Q$ point of part a) of problem 1 for the following circuit.

b) Simultaneously show the load line of part a) on the transistor curves and designate on them the Q point.

