

ENEE 303 Spring 2007

Analog and Digital Electronics; sections 0101, 0102

Time Schedule

MWF 10:00-10:50; KEB 1110; 0101=Tu 11:00 (JMP 1109); 0102=Tu 09:30 (EGR 1102)

Monday	Wednesday	Friday	Monday	Wednesday	Friday
	01/24/07 Organize Transistors, Spice Chap 3, diodes, pp. 147-163	01/26/07 Chap 4 MOS transistor pp. 235-261	01/29/07 Begin paper choice Chap 5, BJT, pp.377-402; 487-490	01/31/07 Biasing and Load lines Chap.4 pp. 262-286	02/02/07 Small signal Equivalents MOS=pp. 288- 301; BJT= pp. 443-459
02/05/07 Preliminary paper choice Chap 7 diff pairs, pp. 687-718, 727-734	02/07/07 Chap 10 inverters pp. 955-962; pass transistor logic and 982-991	02/09/07 Final paper choice BJT circuits pp. 467-485; hybrid pi p. 487-490	02/12/07 Current source & mirrors pp. 562-571 Miller effect pp. 578-579	02/14/07 Chap 10 CMOS Logic Gates, pp. 963- 974	02/16/07 ECL Chap 11 pp. 1052-1056
02/19/07 Signup for presentations Catch up	02/21/07 Choose Commentators Chap 11, Memory, ring oscillator pp. 1013-1027	02/23/07 Frequency Response MOS=pp. 326- 336 BJT=pp. 491- 503	02/26/07 Chap 9, D/A- A/D pp. 922-934 Chap 14; RTL, TTL	02/28/07 Chapter 12 filters,	03/02/07 Chap 13, Schmitt trigger, pp. 1188-1196
03/05/07 Chap 13 Crystal Osc. pp. 1183-1184 555, pp. 1198-1203	03/07/07 Catch up	03/09/07 Chap 11, sense amplifiers, addressing, pp. 1038-1048	03/12/07	03/14/07 Midterm	03/16/07
03/19/07 Spring recess	03/21/07 Spring recess	03/23/07 Spring recess	03/26/07 Begin Circuit Paper Presentations	03/28/07	03/30/07
04/02/07	04/04/07	04/06/07 Begin Results Presentations	04/09/07	04/11/07	04/13/07
04/16/07	04/18/07	04/20/07	04/23/07	04/25/07	04/27/07
04/30/07	05/02/07	05/04/07 End Results Presentations	05/07/07 Reports Due	05/09/07 Last Class	
	F 05/18/07 Final Exam 08:00 - 10:00				