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ENEE 303 Spring 2006

Topics to study for final - **update**

1. Current mirrors (BJT, CMOS, "source" and "sink")
2. CMOS Spice parameters (KP, VTO, lambda) and transistor characteristics (including effect of VTO changes for NMOS from "enhancement" mode, $V_{TO} > 0$ to "depletion" mode, $V_{TO} < 0$)
3. Diode characteristics, large and small signal parameters - **diode connected BJT transistors and Ebers-Moll model**
4. Small signal parameters, including Y matrix ones, from power series, **and for inverters.**
5. Topics from papers of Berhane, Gomez and Walid.

Check the web on Friday and Monday afternoons for any updates

Notebooks are due at the end of the final exam.