

EE 303
04/10/06

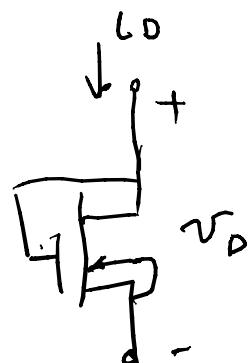
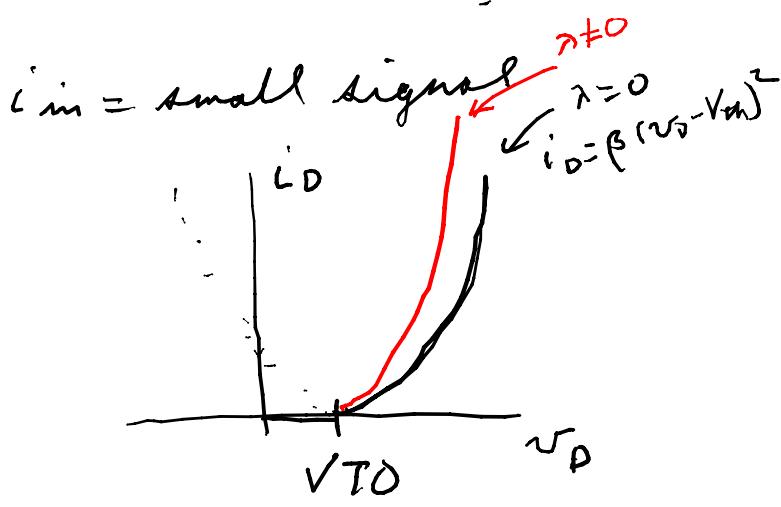
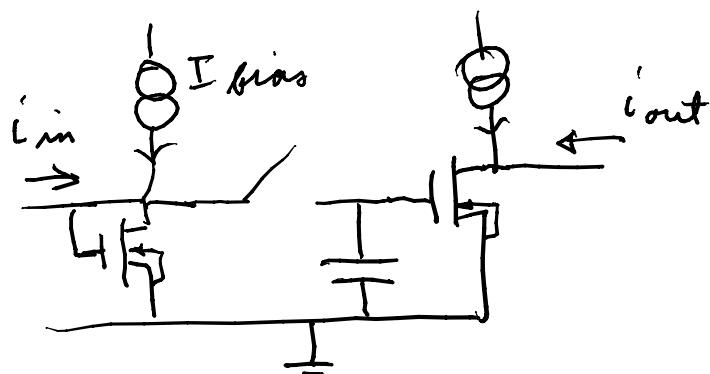
Homework 5 due M 04/17/06

current mode



current add to zero into
a node KCL

switched current circuits



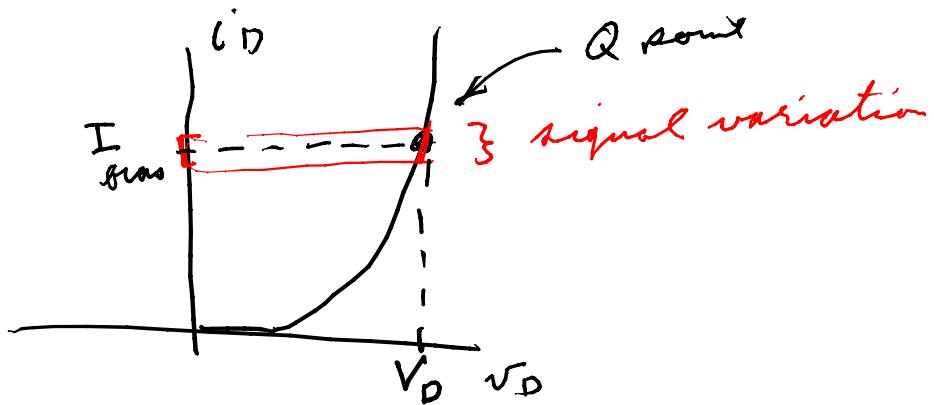
$$\text{here } V_{DS} = V_D \\ V_{GS} = V_G$$

$$\text{so } V_{DS} \geq V_{GS} - V_{TO}$$

if assume $V_{TO} > 0$ (enhancement mode)

in saturation region if $V_D > V_{TO}$

$$i_D = \frac{KP}{2} \frac{W}{L} (V_D - V_{TO})^2 (1 + \gamma V_S)$$



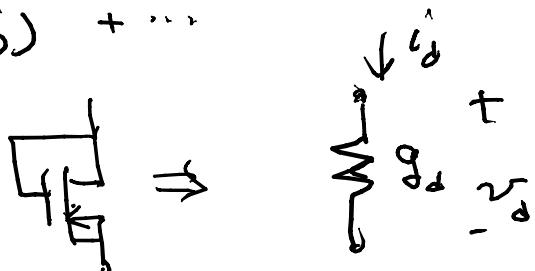
small signal analysis

$$i_D = \beta (V_D - V_{th})^2 (1 + \lambda V_D)$$

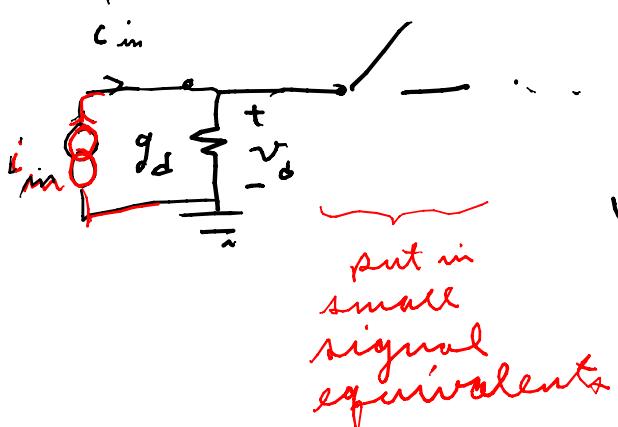
$$\frac{\partial i_D}{\partial V_D} = \beta \{ 2(V_D - V_{th})(1 + \lambda V_D) + (V_D - V_{th})^2(\lambda) \} = g_d$$

$$i_d = I_{bias} + g_d \cdot (v_D - V_D) + \dots$$

$$i_d = g_d \cdot v_d$$



small signal



for g_d need V_D

(if λ small)

$$V_D = V_{th} + \sqrt{\frac{I_{bias}}{\beta_R} \frac{\frac{K_P}{2} W}{L}}$$

