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ENEE 303 Analog and Digital Electronics – Base Paper Choices

name	email	First talk date	Second talk date	Commentator date
paper				
Bergsman, Elie Joseph		#4 W 03/29/06	#2 F 04/21/06	#1 F 04/14/06 (RWN)
J. Wang, X. Zhu and D. Pavlidis, "Low-power InP/GaAsSb/InP DHBT cascode transimpedance amplifier with GBP/Pdc of 7.2 GHz/mW," Electronics Letters, Vol. 42, No. 1, January 5, 2006, pp. 25 - 27				
Berhane, Yosief		#2 W 03/29/06	#1 W 04/26/06	#2 F 04/28/06
H. W. Abbott and L. D. Wechsler, "Diode Amplifiers," ???? , pp, 57 - 59.				
Burke, Timothy Patrick		#2 W 03/29/06	#1 W 04/12/06	#1 W 04/19/06
W. Steiger, "Frequency Discriminators Using Complementary Transistors," IEEE Transactions on Circuit Theory, Vol. ??, No. ??, June 1963, pp. 206 - 212.				
Chiu, Harry Ben		#2 F 03/31/06	#2 M 04/17/06	#1 M 04/17/06
B. M. Ballweber, R. Gupta, and D. J. Allstot, "A Fully Integrated 0.5-5.5-GHz CMOS Distributed Amplifier," IEEE Transactions on Solid-State Circuits, Vol. 35, No. 2, February 2000, pp. 231 - 239				
Chong, Charles Young		#2 M 04/03/06	#2 M 05/01/06	#2 W 05/03/06
R. Kadefors, "Simple Generator for Currents in the Low Microampere Region Utilizing the FET Source-Follower," IEEE Journal of Solid-State Circuits, Vol. SC-5, No. 6, December 1970, pp. 364 - 365.				
Colbert, Jaryn			Added to F 04/28/06	
04/27//06 g. Palmisano, G. Palumbo, and S. Pennisi, "High-Drive CMOS Current Amplifier," IEEE Journal of Solid-State Circuits, Vol. 33, No. 2, February 1998, pp. 228 - 236.				
Demme, John David		#4 M 04/03/06	#2 F 05/05/06	#2 M 05/08/06
C.-L. Chen and Y.-C.Chang, "Self-biased cross-coupled low-cost fully-differential CMOS operational amplifier," Electronics Letters, Vol. 41, No. 9, April 28, 2005, pp. ???				
Diltz, Derek Michael		#1 M 04/03/06	#2 W 05/03/06	#2 M 05/01/06
K. Kimura, "Low Voltage Techniques for Bias Circuits," IEEE Transactions on Circuits and Systems - I, Vol. 44, No. 5, May 1997, pp. 459 - 577.				
Dunay, Christopher John		#3 W 04/05/06	#2 M 05/08/06	#2 F 05/05/06
X. Zhang, and E. I. El-Masry, "Regulated Body-Driven CMOS Current Mirror for Low-Voltage Applications," IEEE Transactions on Circuits and Systems - II, Vol 51, No. 10, October 2004, pp. 571 - 577.				
Gartner, Douglas Henry		#2 M 03/27/06	#1 F 04/14/06	#2 W 04/19/06
J. Rhee and Y. Joo, "Dual-mode wide dynamic range CMOS active pixel sensor," Electronics Letters, Vol. 41, No. 24, November 24, 2005, pp. ???				
Gomes, Leo P		#5 F 03/31/06	#2 F 04/28/06	#1 M 04/24/06
H. Ikeda, "MOST Variable Resistor and Its Application to an AGC Amplifier," IEEE Journal of Solid-State Circuits, Vol. ???, No. ??? February 1970, pp. 43 - 45.				
Gunatileka, Sameera A		#3 F 03/31/06	#1 M 04/24/06	#1 W 04/26/06

S. R. K. Dutta and M. Vidyasagar, "Worst-Case Design of DC Transistor Circuits," IEEE Transactions on Circuits and Systems, Vol. ???, No. ???, May 1977, pp. 273 - 274.				
Hogan, Mark James		#2 W 04/05/06	#1 M 05/08/06	#1 F 05/05/06
C. T. Chuang, K. Chin, P. F. Liu, and H. J. Shin, "High-Speed Low-Power Darlington ECL Circuit," IEEE Journal of Solid-State Circuits, Vol. 28, No. 2, December 1993, pp. 1374 - 1376.				
Kim, Jae-Wan		#1 M 03/27/06 =>#6W03/29/06	#2 W 04/26/06	#2 W 05/10/06
Y. Ding and R. Harjani, "A High-Efficiency CMOS +22-dBm Linear Power Amplifier," IEEE Journal of Solid-State Circuits, Vol. 40, No. 9, September 2005, pp. 1895 – 1900.				
Mirek, Maciej Stanislaw		#1 W 03/29/06	#1 M 04/17/06	#2 M 04/17/06
B. Razavi, "A 60-GHz CMOS Receiver Front-End," IEEE Journal of Solid-State Circuits, Vol. 41, No. 1, January 2006, pp. 17 - 22.				
Mohammed, Anwar Yassin		#3 F 03/31/06	#2 M 04/24/06	#1 W 05/03/06
K. M. Al-Ruwaihi and J. M. Noras, "Programmable CMOS current comparator circuit for analogue VLSI neural networks utilizing identical small-dimension MOS transistors," International Journal of Electronics, Vol. 78, no. 2, 1995, pp. 347 - 358. [03/01/06 changed from]K. V. Ramanan and R. C. Varshney, "New Short-Circuit-Stable Negative-Resistance Circuit with no Internal Bias Supplies," Electronics Letters, Vol. 3, No. 5, May 1967, pp. 186 - 188.				
Orlicki, Peter		#3 W 03/29/06	#2 W 04/19/06	#2 F 04/21/06
F. Gros dÁillon, "Capacitor Discharge Logarithmic Amplifier," IEEE Journal of Solid-State Circuits, Vol. SC-13, No. 2, April 1978, pp. 271 - 273.				
Raja, Bilal Ahmad		#5 M 03/27/06 =>#6F03/31/06	#2 F 04/14/06	#2 W 04/12/06 (RWN)
V. Friedman and S. Liu, "Dynamic Logic CMOS Circuits," IEEE Journal of Solid-State Circuits, Vol. SC-19, No. 2, April 1984, pp. 263 - 266.				
Sharifzada, Ali Reza		#5 W 04/05/06	#1 M 05/01/06	#2 F 04/14/06
03/22/06: J. Bauwelnick, P. Ossieur, X. Z. Qiu and J. Vandewege, "High-speed active-input cascode current mirror," Electronics Letters, Vol. 42, no. 3, February 2, 2006, pp. ??-??. [changed from, 03/22/06]V. Singh, "Realization of Operational Floating Amplifier Based Equivalent Forms of the Conventional 1-Op-Amp Based Sinusoidal Oscillators," IEEE Transactions on Circuits and Systems - I, Vol. 48, No. 3, March 2001, pp. 377 - 381.				
Shin, Andrew Jaewon		#5 W 03/29/06	#1 W 04/19/06	#1 F 04/28/06
J.-Y. Lee, S.-E. Kim, S.-J. Song, J.-K. Kim, S. Kim and H.-J. Yoo, "A Regulated Charge Pump with Small Ripple Voltage and Fast Start-Up," IEEE Journal of Sold-State Circuits, Vol. 41, No. 2, February 2006, pp. 425 - 431+???				
Stewart, Ersil M		#5 M 04/03/06	#1 F 04/28/06	#1 F 04/21/06
C. Galup-Montoro, M. C. Schneider, H. Klimach, and A. Arnaud, "A Compact Model of MOSFET Mismatch for Circuit Design," IEEE Journal of Solid-State Circuits, Vol. 40, No. 8, August 2005, pp. 1649 – 1657.				
Takougang, Isidore		#3 M 04/03/06	#2 W 05/10/06	#2 W 04/26/06
Q. S. Song and S.-S. Song, "High voltage output circuit using n- and n-LDMOSFET with thick gate oxide for PDP driver IC," Electronics Letters, Vol. 40, No. 16, August 5, 2004, pp. 989 - 990.				
Thomas, Brian Jospeh		#3 M 03/27/06	#1 W 05/10/06	#2 M 04/24/06

B. Blesser, "Ultralinear Transistor Configuration Under Conditions of Minimal Power-Supply Drain Current," IEEE Journal of Solid-State Circuits, Vol. ??, No. ??, June 1970, pp. 125 - 126.				
Walid, Mohammed Adnan		#4 W 04/05/06	#1 W 05/03/06	#1 M 05/01/06
M. Milkov, "Current Gain High-Frequency CMOS Operational Amplifiers," IEEE Solid-State Circuits, Vol. SC-20, No. 4, August 1985, pp. 845 - 851.				
Wasko, Christopher James		#1 W 04/05/06	#1 F 05/05/06	#1 M 05/08/06
S. Sansen, and Z. Y. Chang, "Feedforward Compensation Techniques for High-Frequency CMOS Amplifiers," IEEE Journal of Solid-State Circuits, Vol. 25, No. 6, December 1990, pp. 1590 - 1595.				
Worku, Girum Telila		#1 F 03/31/06	#1 F 04/21/06	#1 W 05/10/06
A. A. Ciubotaru, "Square-law circuit using junction field-effect transistors," Electronics Letters, Vol 33. No. 7, March 27, 1997, pp. 544 – 545.				
Yousefian, Narbeh		#4 03/27/06	#2 W 04/12/06	#1 W 04/12/06 (RWN)
C. T. Chung, K. Chin, H. J. Shin, and P. F. Lu, "High-Speed Low-Power ECL Circuit with AC-Coupled Self-Biased Dynamic Current Source and Active-Pull-Down Emitter-Follower Stage," IEEE Journal of Solid-State Circuits, Vol. 37, No. 8, August 1992, pp. 1207 – 1210.				

Total number of students: 27