

ENEE 303 Spring 2006

Analog and Digital Electronics; sections 0101, 0102

Time Schedule

MWF 09:00-09:50; CHM 0128; 0101=Tu 11:00 (JMP 1109); 0102=Tu 09:30 (EGR 1102)

Monday	Wednesday	Friday	Monday	Wednesday	Friday
	01/25/06 Organize Transistors, Spice Chap 3, diodes, pp. 147-163	01/27/06 Chap 4 MOS transistor pp. 235-261	01/30/06 Begin paper choice Chap 5, BJT, pp.377-402; 487-490	02/01/06 Biasing and Load lines Chap.4 pp. 262-286	02/03/06 Small signal Equivalents MOS=pp. 288- 301; BJT= pp. 443-459
02/06/06 Preliminary paper choice Chap 7 diff pairs, pp. 687- 718	02/08/06 Chap 10 inverters pp. 955-962; pass transistor logic and 982-991	02/10/06 Final paper choice BJT circuits pp. 467-485; hybrid pi p. 487-490	02/13/05 Current source & mirrors pp. 562-571	02/15/06 Chap 10 CMOS Logic Gates, pp. 963- 974	02/17/06 ECL Chap 11 pp. 1052-1056
02/20/06 Signup for presentations Catch up	02/22/06 Choose Commentators Chap 11, Memory, ring oscillator pp. 1013-1027	02/24/06 Frequency Response MOS=pp. 326- 336 BJT=pp. 491- 503	02/27/06 Chap 9, D/A- A/D pp. 922-934 Chap 14; RTL, TTL	03/01/06 Chapter 12 filters,	03/03/06 Chap 13, Schmitt trigger, pp. 1188-1196
03/06/06 Chap 13 555, pp. 1198-1203	03/08/06 Catch up	03/10/06 Chap 11, sense amplifiers, addressing, pp. 1038-1048	03/13/06	03/15/06 Midterm	03/17/06
03/20/06 Spring recess	03/22/06 Spring recess	03/24/06 Spring recess	03/27/06 Begin Circuit Paper Presentations	03/29/05	03/31/06
04/03/06	04/05/06	04/07/06 Begin Results Presentations	04/10/06	04/12/06	04/14/06
04/17/06	04/19/06	04/21/06	04/24/06	04/26/06	04/28/06
05/01/06	05/03/06	05/05/06	05/08/06 End Results Presentations	05/10/06 Last Class Reports Due	
	W 05/17/06 Final Exam 08:00 - 10:00				