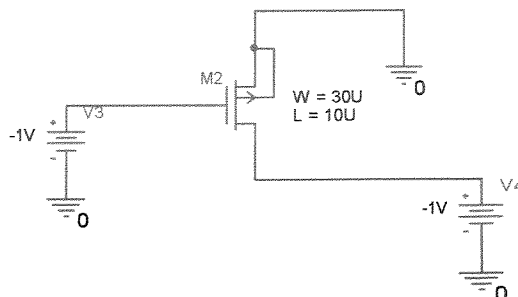


Solutions

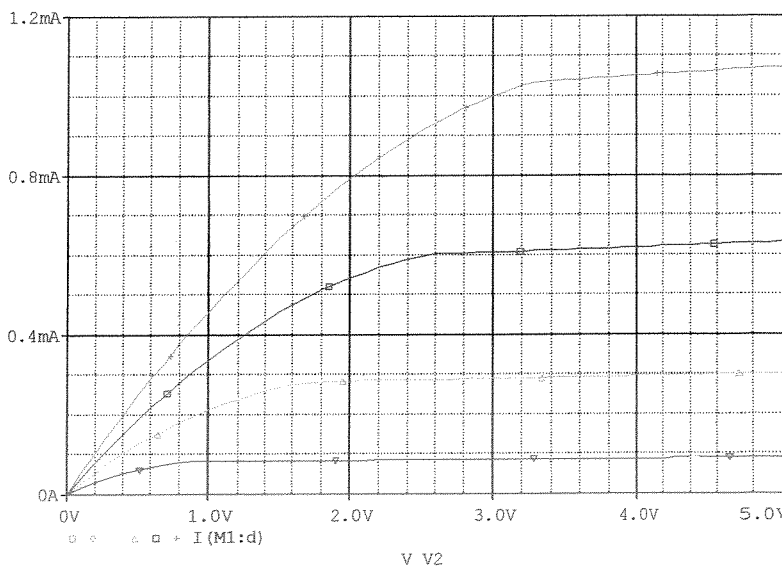
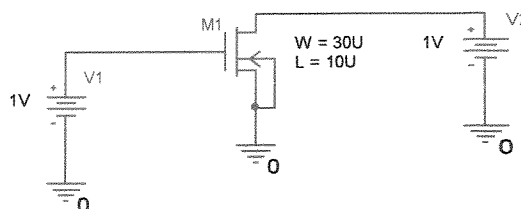
02/14/2005 Y.Z.

ENEE302 HW1

1.  
(a)



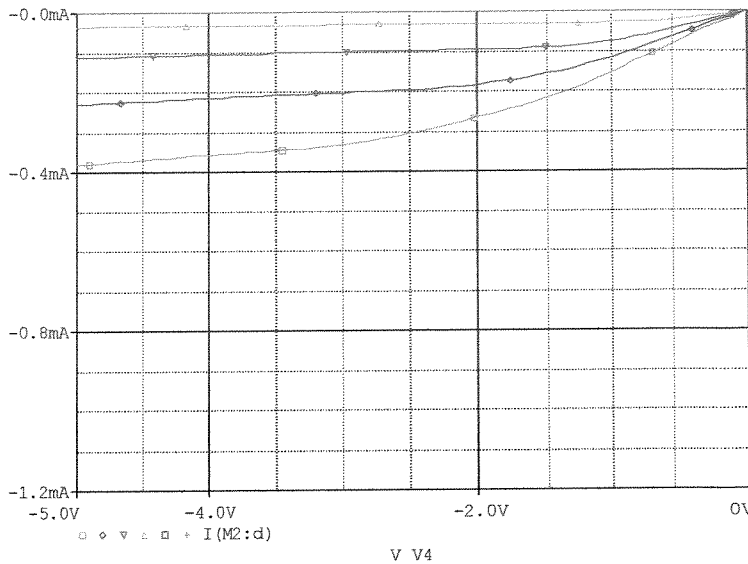
PARAMETERS:  
V1 = 1V  
V3 = -1V



~~V<sub>GS</sub>~~ V<sub>1</sub> = 5V  
V<sub>1</sub> = 4V  
V<sub>1</sub> = 3V  
V<sub>1</sub> = 2V  
V<sub>1</sub> = 1V, V<sub>1</sub> = 0V.

Fig. 1. I<sub>D</sub> vs. V<sub>DS</sub> for NMOS

To get these curves, do a DC primary sweep on V<sub>2</sub> (V<sub>DS-NMOS</sub>), and parametric sweep on V<sub>1</sub> (V<sub>GS</sub> for NMOS). V<sub>2</sub> is swept from 0 to 5V with 0.1V increment. V<sub>1</sub> is from 0V to 5V with 1V increment.

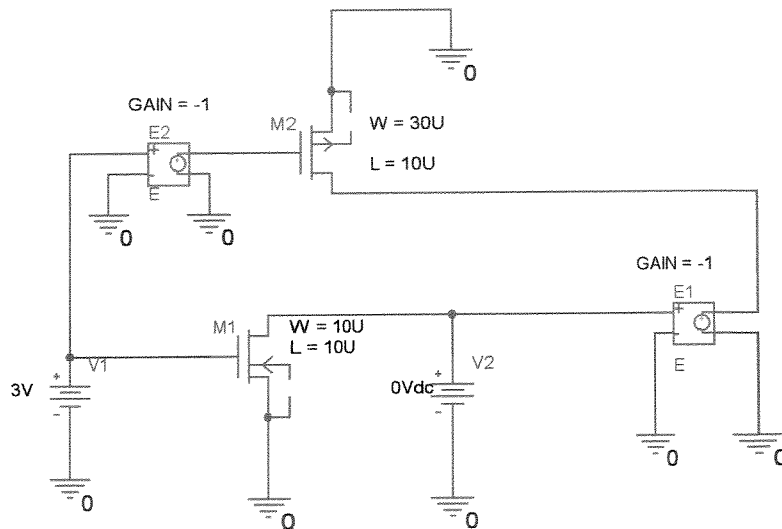


For PMOS,  
 Drain voltage swept from -5V to 0V, 0.1V increment.  
 Gate voltage swept from -5V to 0V, 1V increment.

Fig. 2.  $I_D$  vs.  $V_{DS}$  for PMOS

(b)

Choose  $W_n=10\mu\text{m}$ ,  $W_p=30\mu\text{m}$ , we get the same magnitude  $I_D$  for the same  $V_G$



To plot both NMOS and PMOS curves on one graph, I use  $V_1$  to control the gates of both NMOS and PMOS. I use  $V_2$  to control the drain of both transistors. I found for  $W_p=30\mu\text{m}$ ,  $W_n$  should be close to  $10\mu\text{m}$  to match the two drain currents. Below is the graph for  $W_p=30\mu\text{m}$ ,  $W_n=10\mu\text{m}$ , we see the currents are almost at the same magnitude for different  $V_{DS}$ .

for PMOS and NMOS

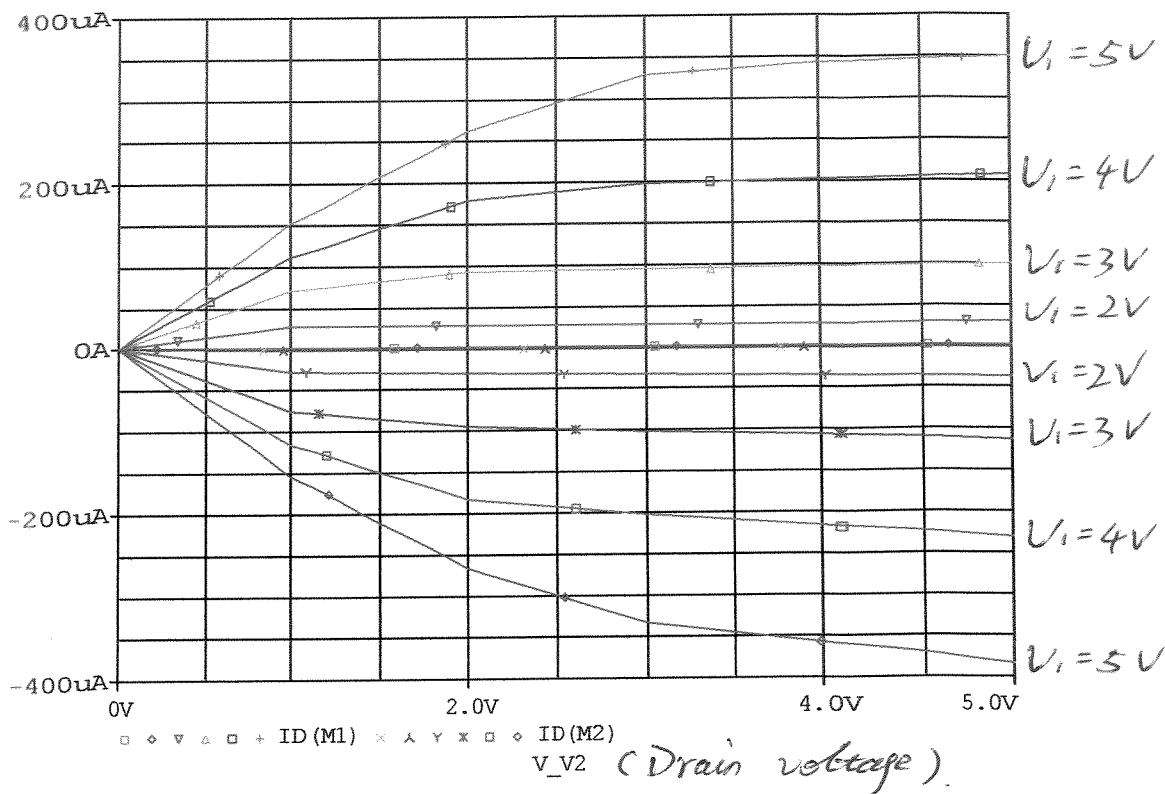


Fig. 3 Adjust the width of NMOS to achieve same magnitude of  $I_D$  for NMOS and PMOS

1(c). To calculate  $k_p'$  and  $V_{t0}$  for PMOS,  
 I choose two lines from the above graph,  
 one line for  $|V_{GS}| = 3V$ , the other for  $|V_{GS}| = 2V$ .  
 I selected one point from each line, and measured  
 the  $I_D$  and  $V_{DS}$  for the point.

When  $V_{GS} = -3V$ ,  $V_{DS} = -0.575V$ ,  $\Rightarrow I_D = 50\mu A$

when  $V_{GS} = -2V$ ,  $V_{DS} = -0.41V$ ,  $\Rightarrow I_D = 20\mu A$

Use the equations for PMOS in triode region.

$$\begin{cases} 50\mu A = k_p' \cdot \frac{30\mu}{10\mu} \left[ (-3 - V_t) \times (-0.575) - \frac{1}{2} \times 0.575^2 \right] \\ 20\mu A = k_p' \cdot \frac{30\mu}{10\mu} \left[ (-2 - V_t) \times (-0.41) - \frac{1}{2} \times 0.41^2 \right] \end{cases}$$

Solve the  $k_p'$  and  $V_t$  from the two equations.

$$I \text{ got } V_t = -0.62V, \quad k_p' = 1.39 \times 10^{-5} \text{ A/V}^2$$

The same thing applies to NMOS. For NMOS  
 when  $V_{GS} = 3V$ ,  $V_{DS} = 0.64V$ ,  $\Rightarrow I_D = 50\mu A$   
 when  $V_{GS} = 2V$ ,  $V_{DS} = 0.45V$ ,  $\Rightarrow I_D = 18\mu A$ .

$$\Rightarrow \begin{cases} 50\mu A = k'_n \cdot \frac{10\mu}{10\mu} \left[ (3 - V_t) \times 0.64 - \frac{1}{2} \times 0.64^2 \right] \\ 18\mu A = k'_n \cdot \frac{10\mu}{10\mu} \left[ (2 - V_t) \times 0.45 - \frac{1}{2} \times 0.45^2 \right] \end{cases}$$

$$\Rightarrow \begin{cases} V_t = 0.825 \text{ V} \\ k'_n = 4.213 \times 10^{-5} \text{ A/V}^2 \end{cases}$$

Check these values with the one in the library  
 (Bicmos 12.lib).

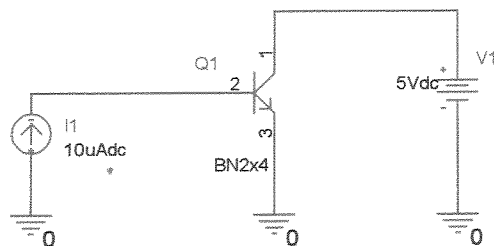
The model values are

$$\text{For PMOS} \begin{cases} V_t = -0.89 \text{ V} \\ k'_p = 1.9 \times 10^{-5} \text{ A/V}^2 \end{cases}$$

$$\text{For NMOS} \begin{cases} V_t = 0.85 \text{ V} \\ k'_n = 5.04 \times 10^{-5} \text{ A/V}^2 \end{cases}$$

Calculations are close to the model values.

2. (a)



I do DC sweep on  $V_1$  ( $V_{CE}$ ) from 0V to 5V,  
 0.1V increments. I do parametric sweep  
 on  $I_1$  ( $I_B$ ) from 0  $\mu A$  to 50  $\mu A$ , 10  $\mu A$  increments.

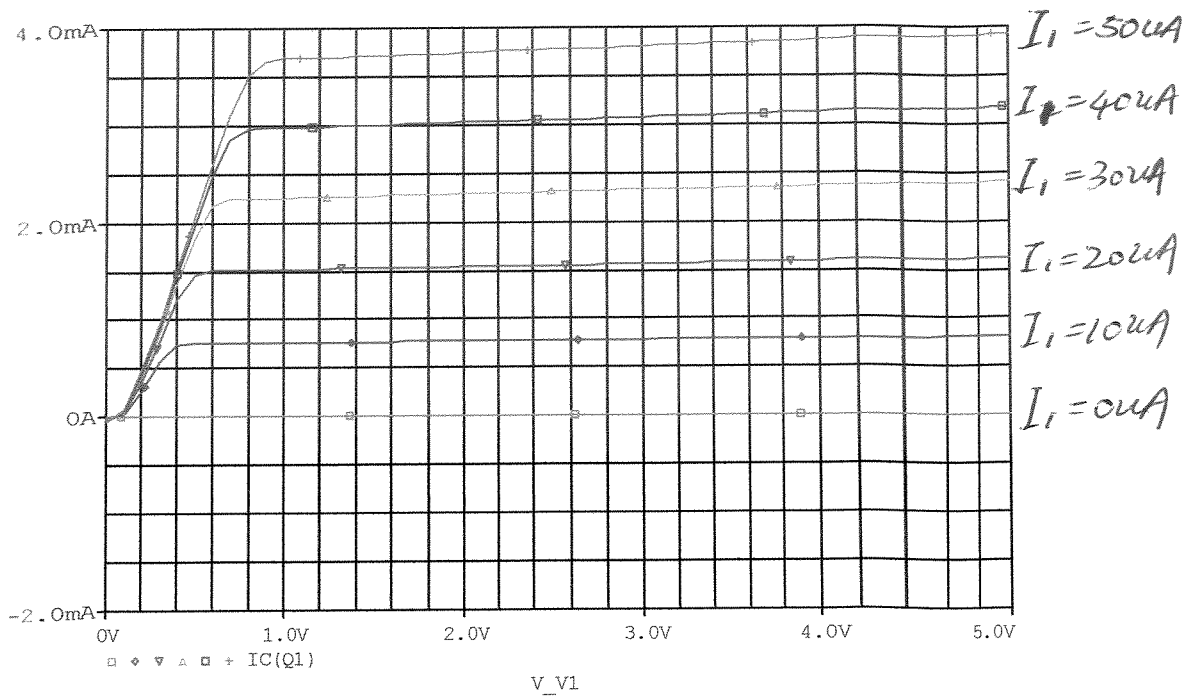
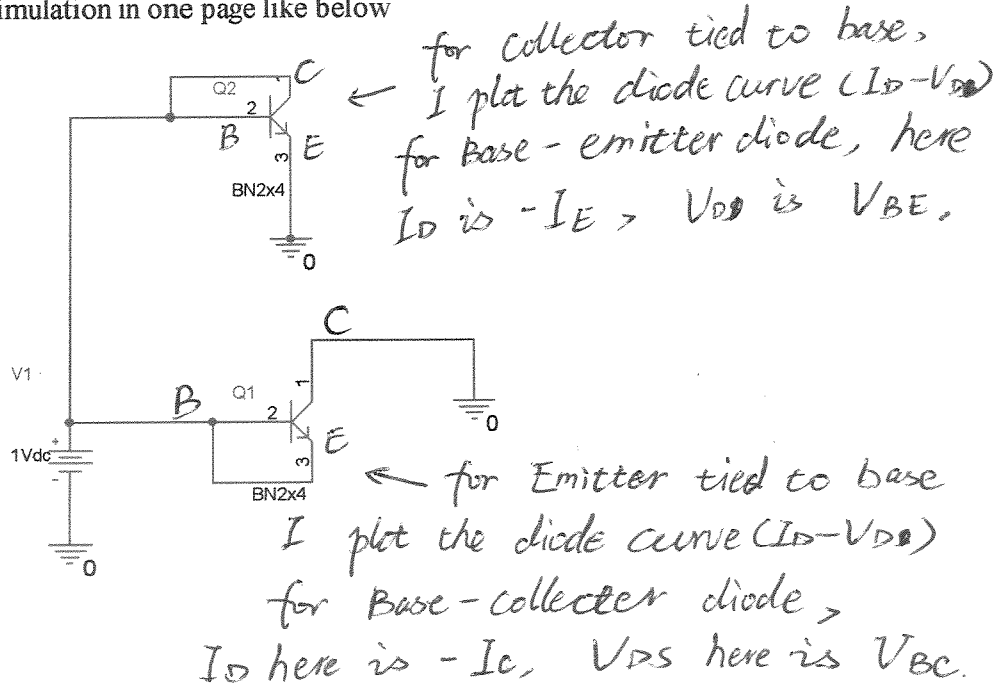
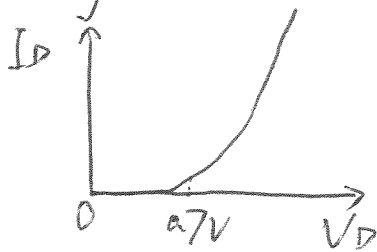


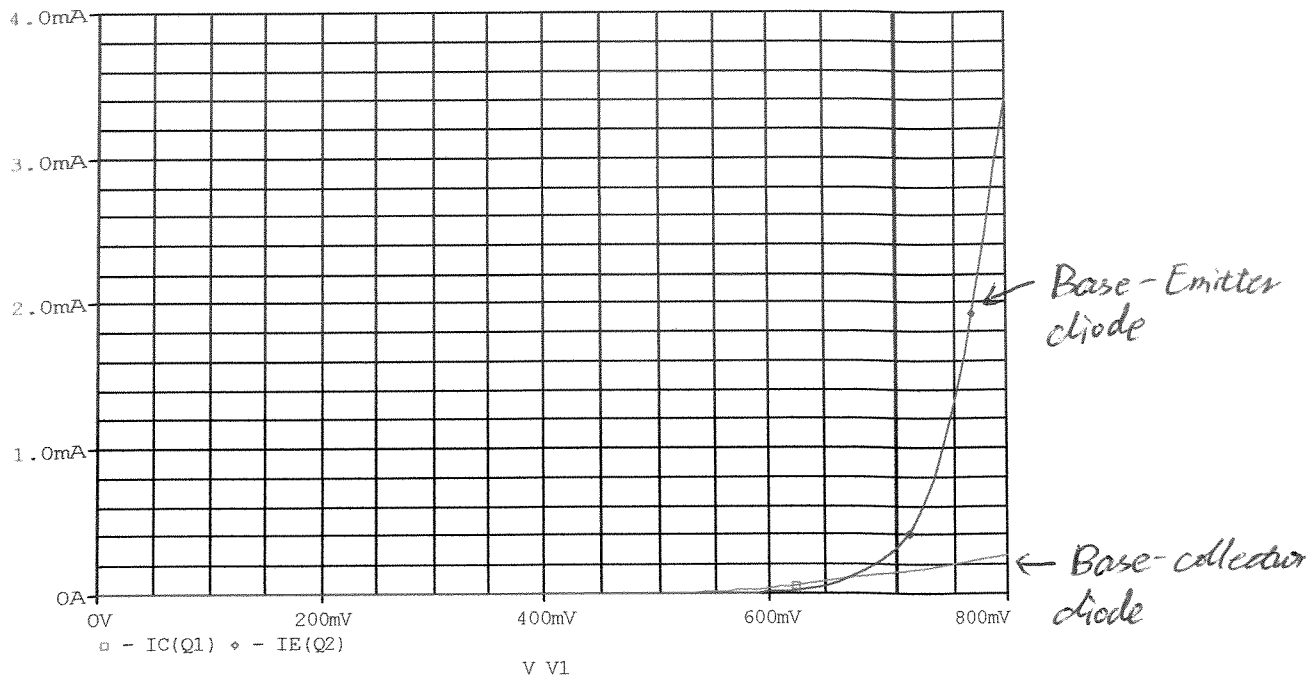
Fig. 4. Collection current vs.  $V_{CE}$  for different base current  $I_b$

b. You can do both simulation in one page like below



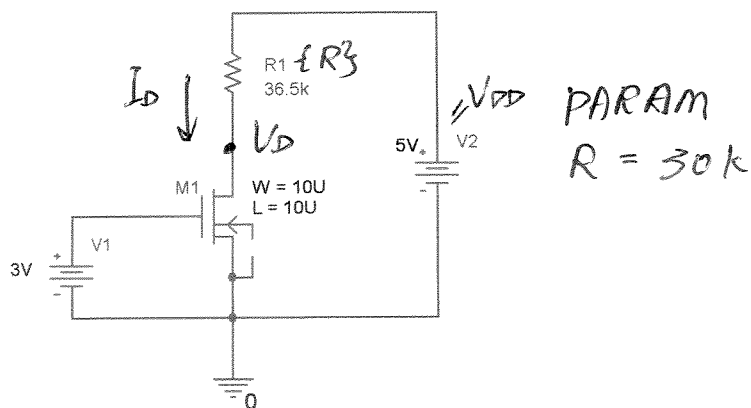
Originally a diode curve looks like below:



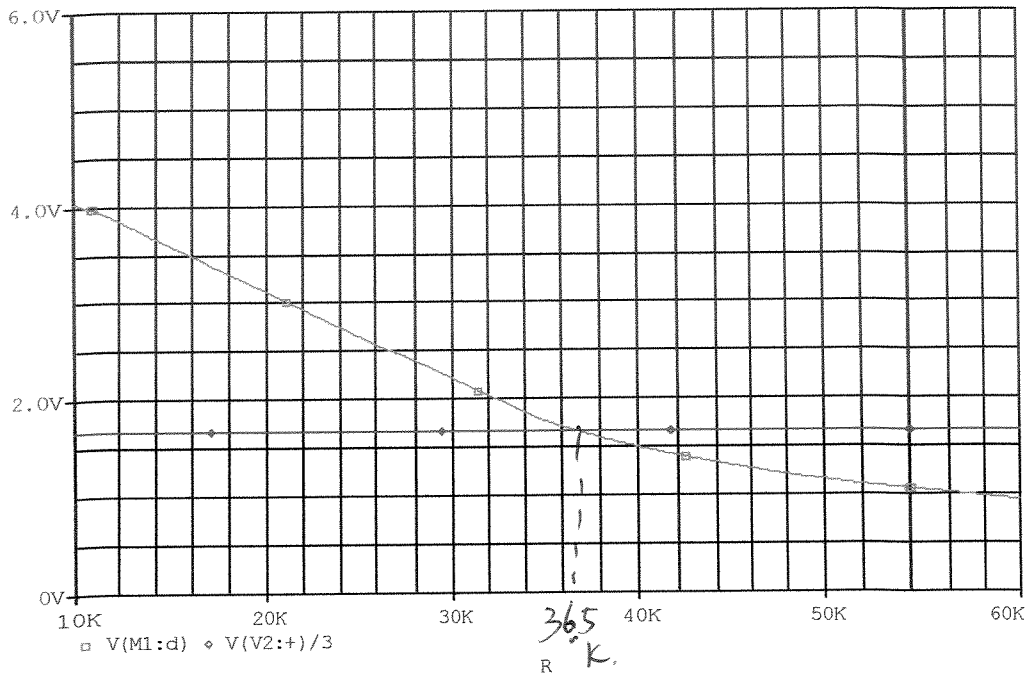


We see the BE diode conducts much more current than the B-C diode. Note even the B-C diode,  $I_D$  is around 0.2 mA at 0.8V. Definitely not on PA scale.

3.



I do a primary DC sweep on Global parameter R from 10k to 60k, 0.1k increment. I plot the  $V_D$ , and  $V_D/3$  on the same graph below. The point the two curves intersect is the R I am looking for.  $R \approx 36.5k$ .



To calculate by hand, first we know for  $V_D = 1.67 \text{ V}$ , NMOS is in triode region, so.

$$\begin{cases} I_D = k_n' \frac{W}{L} \left[ (V_{GS} - V_t) V_D - \frac{1}{2} V_D^2 \right] \text{ (NMOS)} \\ I_D = \frac{V_{DD} - V_D}{R} \quad \text{(the same } I_D \text{ flows through } R) \end{cases}$$

We plug in  $k_n' = 4.213 \times 10^{-5} \text{ A/V}^2$  (from problem 1(c))

$V_t = 0.83 \text{ V}$  (from problem 1(c)),  $V_{GS} = 3 \text{ V}$ ,

$W = 10\mu$ ,  $L = 10\mu$ .  $V_{DD} = 5 \text{ V}$ , then

We have two unknowns  $I_D$  and  $R$  in the two equations,  $\Rightarrow$

$$I_D = 9.4 \times 10^{-5} \text{ A}$$

$$R = 35.4 \text{ k}\Omega.$$

Agrees well with PSPICE simulation.