

EENE 302 Spring 2005

Digital Electronics; sections 0201, 0202

Time Schedule

Tu Th 15:30-16:45; CHM 01150127; 0201=M 15:00 (ARM 0105); 0202=M 13:00 (JMP 2202)

Tuesday	Thursday	Tuesday	Thursday
	01/27/05 Organize Transistors, Spice Chap 4 MOS transistor, pp. 235-261	02/01/05 Begin paper choice Chap 3, diodes, pp. 147- 163; Chap 5, BJT, pp. 377-402; 487-490	02/03/05 Chap 10, pass transistor logic and inverters pp. 949-962; 990-991
02/08/05 Preliminary paper choice Chap 7 diff pairs, pp. 687-718	02/10/05 Final paper choice	02/15/05 Chap 10 CMOS Logic Gates, pp. 963-974	02/17/05 Chap 10, Domino Logic pp. 996-998
02/22/05 Choose Commentators Chap 11, Memory, ring oscillator pp. 1013-1027	02/24/05 Chap 9, D/A-A/D pp. 922-934 Chap 11, ECL pp. 1052-1063	03/01/05 Signup for presentations Chap 13, Schmitt trigger, pp. 1188-1196	03/03/05 Chap 13 555, pp. 1198-1203
03/08/05 Chap 11, sense amplifiers, addressing, pp. 1038-1048	03/10/05	03/15/05 Begin Circuit Paper Presentations	03/17/05 Midterm
03/22/05 Spring recess	03/24/05 Spring recess	03/29/05	03/31/05 Begin Results Presentations
04/05/05	04/07/05	04/12/05	04/14/05
04/19/05	04/21/05	04/26/05	04/28/05
05/03/05	05/05/05	05/10/05 Reports Due	05/12/05 Last Class
Sa 05/14/05 Final Exam 10:30 - 12:30			