

name	email	Commentated by/date	Commentator for/date	
paper				
Agrawal, Pankhuri	pankhuri@wam.umd.edu			
Ahmad, Mohamed Safwat	mohamed@wam.umd.edu			
H. Elwan, H. Alzaher and M. Ismail, "A New Generation of Global Wireless Compatibility," Circuits and Devices, Vol. ??, No. ??, January 2001, pp. 7 - 19.				
Ahmadi, Amir Ali	amalahmadi@yahoo.com			
M. M . Hella and M. Ismail, "2GHz controllable power amplifier in standard CMOS process for short-range wireless applications," IEE Proceedings - Circuits, Devices and Systems, Vol. 149, No. 516, October/December 2002, pp. 363 - 368.				
Alhamarneh, Mohammad A	malhamar@mail.umd.ed u			
C. Yoo, M.-K. Kim and W. Kim, "A Static Power Saving TTL-to-CMOS Input Buffer," IEEE Journal of Solid-State Circuits, Vol. 30, No. 5, May 1995, pp. 616 - 620.				
Atamas, Nicholas	natamas@mail.umd.edu			
C. P. Chong, K. C. Smith and Z. G. Vranesic, "Design of an A/D Converter Using Complementary Three-Input Amplifier," Proceedings of the European Conference on Circuit theory and Design, September 5-8, 1989, pp. 218 - 222.				
Bafford, Brock Edward	bbafford@mail.umd.ed u			
G. Palmisano, G. Palumbo and S. Pennisi, "High-Performance and Simple CMOS Unity-Gain Amplifier," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 3, March 2000, pp. 406 - 409 ??? missing reference page ???				
Banes, Adam Richard	banes@wam.umd.edu			
R. G. Meyer, "Low-Power Monolithic RF Peak Detector Analysis," IEEE Journal of Solid-State Circuits, Vol. 30, No. 1, January 1995, pp. 65 - 67.				
Bansal, Jagmohan	jagbansal17@hotmail. com			
R. W.-K. Lam and C.-K. Li, "Optimized transmission gate-based CMOS full adders: design and analysis," International Journal of Electronics, Vol. 88, No. 9, 2001, pp. 1001 - 1013.				
Boyd, Jonathan Albert	jboyd@wam.umd.edu			
P. O'Connor, G. Gramegna, P. Rehak, F. Corsi and C. Marzocca, "CMOS Preamplifier with High Linearity and Ultra Low Noise for X-Ray Spectroscopy," IEEE Transactions on Nuclear Science, Vol. 44, No. 3, June 1997, pp. 318 - 325.				
Cattungal, Ajay P	ajayc@wam.umd.edu			

Cerritos, Miguel Humberto	mctwinbrook@hotmail. com			
R. Ahola and K. Halonen, "A 1.76-GHz 22.6mW Delta-Sigma Fractional-N Frequency Synthesizer, IEEE Journal of Solid-State Circuits, Vol. 38, No. 1, January 2003, pp. 138 - 140.				
Combs, Francis Joseph	fjcombs@wam.umd.edu			
Y. Perelman and R. Ginosar, "A Low-Light-Level Sensor for Medical Diagnostic Applications," IEEE Journal of Solid-State Circuits, Vol. 36, No. 10, October 2001, pp. 1553 - 1558.				
Craciunescu, Cosmin Octavi	ccraciun@wam.umd.edu			
F.-J. Huang and K. K. O, "Single-Pole Double-Throw CMOS Switches for 900-MHz and 2.4-GHz Applications on p- Silicon Substrates," IEEE Journal of Solid-State Circuits, Vol. 39, No. 1, January 2004, pp. 35 - 41.				
Dave, Apurva S	adave@wam.umd.edu			
B. M. Ballweber, R. Gupta and D. J. Allstot, "A Fully Integrated 0.5-5.5-GHz CMOS Distributed Amplifier," IEEE Journal of Solid-State Circuits, Vol. 35, No. 2, February 2000, pp. 231 - 239.				
Ducao, Amon Brigoli	abducao@wam.umd.edu			
C. Kim, S.-M. Yoo and S.-M. Kang, "NMOS Energy Recovery Logic," ??? need reference source ??? Proceedings of ???, 1999, pp. 310 - 313.				
Ebirim, Emeka	ebirime@wam.umd.edu			
Edut, Aviram	aviram13@hotmail.com			
Ellison, Jason Jeffrey	jjelliso@mail.umd.ed u			
K. Ramkumar and K. Nagaraj, "A Ternary Schmitt Trigger," IEEE Transactions on Circuits and Systems, Vol. CAS-32, No. 7, July 1985, pp. 732 - 735.				
Esmaili Sardari, Saeed	sesmaili@mail.umd.ed u			
G. Bertuccio, L. Fasoli and M. Sampietro, "MOSFET Diode as a Feedback Reset Element on Charge Amplifiers," IEEE Transactions on Nuclear Science, Vol. 46, No. 3, June 1999, pp. 757 - 760.				
Fenlon, Matthew Edward	fenlon@mail.umd.edu			
D. V. Morozov and A. S. Korotkov, "A Realization of Low-Distortion CMOS Transconductance Amplifier," IEEE Transactions on Circuits and Systems - I, Vol. 48, No. 9, September 2001, pp. 1138 - 1141.				
Gondal, Farrukh Mehmood	gondal@wam.umd.edu			
S. Minae, G. Topcu and O. Cicekoglu, "Active only integrator and differentiator with tunable time constants," International Journal of Electronics, Vol. 90, No. 9, 2003, pp. 581 - 588.				

Green, Mark Douglas	greenmd@mail.umd.edu			
R. Berriane, J.-L. Sanchez and J. Jalade, "MOS-Gated Optically Triggered Thyristor: A New Galvanically Insulated High Voltage Integrated Switch," Solid-State Electronics, Vol. 39, No. 6, 1996, pp. 863 - 869.				
Hamilton, Nicholas Wayne	nwhamilt@wam.umd.edu			
D. Kehrer, H. D. Wohlmuth, H. Knapp, M. Wurzer and A. L. Scholtz, "40-Gb/s 2:1 Multiplexer and 1:2 Demultiplexer in 120-nm Standard CMOS," IEEE Journal of Solid-State Circuits, Vol. 38, No. 11, November 2003, pp. 1830 - 1837.				
Holt, Sean Rion	srholt@wam.umd.edu			
J. Doyle, Y. J. Lee, Y.-B. Kim, H. Wilsch and F. Lombardi, "A CMOS Subbandgap Reference Circuit with 1-V Power Supply Voltage," IEEE Journal of Solid-State Circuits, Vol. 39, No. 1, January 2004, pp. 252 - 255.				
Ilkovich, Daniel Kevin	ilkovich@wam.umd.edu			
H. Sjoeland and S. Mattisson, "A 100-MHz CMOS Wide-Band IF Amplifier," IEEE Journal of Solid-State Circuits, Vol. 33, No. 4, April 1998, pp. 631 - 634.				
Ivanov, Nikola	ivanovn@mail.umd.edu			
G. Palmisano, G. Palumbo and R. Salerno, "A 1.5-V High Drive Capability CMOS Op-Amp," IEEE Journal of Solid-State Circuits, Vol. 34, No. 2, February 1999, pp. 248 - 252.				
Kim, John	johnkim@wam.umd.edu			
K. C. Saraswat, J. D. Meindl and J. Berger, "A High Voltage MOS Switch," IEEE Journal of Solid-State Circuits, Vol. SC-10, No. 3, June 1975, pp. 136 - 142.				
Lang, Omer Avraham	lango@wam.umd.edu			
B. J. Hosticka, "Dynamic CMOS Amplifiers," IEEE Journal of Solid-State Circuits, Vol. SC-15, No. 5, October 1980, pp. 887 - 894				
Lee, Lienhua A	jodie@wam.umd.edu			
M. Tartagni and R. Guerrieri, "A Fingerprint Sensor Based on the Feedback Capacitive Sensing Scheme," IEEE Journal of Solid-State Circuits, Vol. 33, No. 1, January 1998, pp. 133 - 142.				
Lee, William Weiham	willee@wam.umd.edu			
D. J. F. Doyle and W. A. Lane, "Circuit Modeling of Bipolar Transistors from BiCMOS," IEEE Journal of Solid-State Circuits, Vol. 24, No. 1, February 1989, pp. 189 - 193.				
Manneh, Lamin rsamurai19@h otmail.com	rsamurai19@hotmail.com			
Changed on 03/09/04 to: U. Cam, A. Toker and H. Kuntman, "CMOS FTFN realization based on translinear cells," Electronics Letters, Vol. 36, No. 15, July 20, 2000, pp. 1255 - 1256.				
Mathur, Pratik	pratik@mail.umd.edu			
S. P. Kosta, V. Patel, S. Kosta and Y. P. Kosta, "Green biomass bistable transistor multivibrator," International Journal of Electronics, Vol. 90, No. 2, 2000, pp. 117 - 120.				

Miller, Iain Arthur	milleri@wam.umd.edu			
F. Herzel, G. Fischer and H. Gustaf, "An Integrated CMOS RF Synthesizer for 802.11a Wireless LAN," IEEE Journal of Solid-State Circuits, Vol. 38, No. 10, October 2003, pp. 1767 - 1770.				
Mokwunye, Joseph Azuka	mokwunye@wam.umd.edu			
G. Giustolisi and G. Palumbo, "A Detailed Analysis of Power-Supply Noise Attenuation in Bandgap Voltage References," IEEE Journal of Solid-State Circuits, Vol. 50, No. 2, February 2003, pp. 185 - 196.				
Momoh, Adeyinka Adebola	adeyinka@wam.umd.edu			
G. Schuppener, C. Pala and M. Mokhtari, "Investigation on Low-Voltage Low-Power Silicon Bipolar Design Topology for High-Speed Digital Circuits," Solid-State Circuits, Vol. 35, No. 7, July 2000, pp. 1051 - 1054.				
Monajemi, Babak	babakm@wam.umd.edu			
Changed 03/11/04 to: J. C. Jensen and L. E. Larson, "A 16-GHz Ultra-High-Speed Si-SiGe HBT Comparator," IEEE Journal of Solid-State Circuits, Vol. 38, No. 9, September 2003, pp. 1584 - 1589.				
Muralidharan , Akilesh	akil_ugrad@yahoo.com			
B. Nauta and M. Dijkstra, "Analog Video Line Driver with Adaptive Impedance Matching," Proceedings of the International Solid State Circuits Conference, 1998, pp. 20.1-1 - 20.1- ??? wrong last two pages ???				
Musser, Doug Allen	dmusser@wam.umd.edu			
Y. J. Yoon, H. I. Kwon, J. D. Lee, B. G. Park, N. S. Kim, U. R. Cho and H. G. Byun, "Synchronous Mirror Delay for Multiphase Locking," IEEE Journal of Solid-State Circuits, Vol. 39, No. 1, January 2004, pp. 150 - 156.				
Orkin, Scott Ross	orkinman@mail.umd.ed u			
G. Weil, W. L. Engl and A. Renz, "Integrated Pacemakers," IEEE Journal of Solid-State Circuits, Vol. SC-5, No. 2, April 1970, pp. 67 - 73.				
Page, Austin Markley	pagea@wam.umd.edu			
C.-C. Wang, P.-M. Lee, C.-F. Wu and H.-L. Wu, "High Fan-In Dynamic CMOS Comparators with Low Transistor Count," IEEE Transactions on Circuits and Systems - I, Vol. 50, No. 9, September 2003, pp. 1216 - 1220.				
Park, Edward	edpark@wam.umd.edu			
B. J. Maundy and P. Aronhime, "A novel CMOS first-order all-pass filter," International Journal of Electronics, Vol. 89, No. 9, 2002, pp. 739 - 743.				
Ram, Arun	aram1@wam.umd.edu			
A. A. Ciubotaru, "Absolute-Value Circuit Using Junction Field-Effect Transistors," IEEE Transactions on Circuits and Systems - II, Vol. 50, No. 8, August 2003, pp. 481 - 483.				
Rosekrans, Sean Christophe	srosekra@mail.umd.ed u			
K.-W. Ho and H. C. Luong, "A 1-V CMOS Power Amplifier for Bluetooth Applications," IEEE Transactions on Circuits and Systems - II, Vol. 50, No. 8, August 2003, pp. 445 - 449.				

Sobota, Justin Timothy	jsobota@wam.umd.edu			
M. G. Johnson, "A Symmetric CMOS NOR Gate for High-Speed Applications," IEEE Journal of Solid-State Circuits, Vol. 23, No. 5, October 1988, pp. 1233 - 377.				
Sochurek, Joseph Patrick	minimure@wam.umd.edu			
W. Hioe, K. Maio, T. Oshima, Y. Shibahara, . Doi, K. Ozaki and S. Arayashiki, "0.18-um CMOS Bluetooth Analog Receiver with -88-dBm Sensitivity," IEEE Journal of Solid-State Circuits, Vol. 39, No. 2, February 2004, pp. 374 - 377.				
Soltanzadeh, Maryam	ironi2222@hotmail.com			
A. Hajimiri, "Distributed Integrated Circuits: an Alternative Approach to High-Frequency Design, IEEE Reference source 2002, pp. 1 - 7.				
Toussaint, Gary Andrew Val	gavsaint@wam.umd.edu			
O. Minato, T. Masuhara, T. Sasaki, H. Nakamura, Y. Sakai, T. Yasui and K. Uchibori, "2Kx8 Bit Hi-CMOS Static RAM's" IEEE Journal of Solid-State Circuits, Vol. SC-15, No. 4, August 1980, pp. 656 - 660.				
Ucal, Mehmet Ekrem	ekremucal@msn.com			
i. M. Filanovsky and H. P. Baltas, "Simple CMOS Analog Square-Rooting and Squaring Circuits," IEEE Transactions on Circuits and Systems - I, Vol. 39, No. 4, April 1992, pp. 312 - 315.				
Ukoha, Chidimma Ojiabo	cukoha@wam.umd.edu			
T. Reimann, F. Krummenacher and M. Declercq, "High speed BiCMOS current mode differential track-and-hold circuit," Electronics Letters, Vol. 30, No. 21, October 13, 1994, pp. 1730 - 1732.				
Vani, Roshan Praful	rvani@wam.umd.edu			
D. Radhakrishnan, "Low-voltage low-power CMOS full adder," IEE Proceedings: Circuits, Devices and Systems, Vol. 148, No. 1, February 2001, pp. 19 - 22+ ???				
Wang, Johnny Tsantsu	jtwang@wam.umd.edu			
H. Darabi, S. Khorram, H.-M. Chien, M.-A. Pan, S. Wu, S. Moloudi, J. C. Leete, J. J. Rael, M. Syed, R. Lee, B. Ibrahim, M. Rofougaran and A. Rofougaran, "A 2.4-GHz CMOS Transceiver for Bluetooth," IEEE Journal of Solid-State Circuits, Vol. 36, No. 12, December 2001, pp. 2016 - 2023.				
Warren, Brandon Kyle	brandonw@mail.umd.edu			
Changed 04/22/04 back to F. Lu and H. Samueli, "A High-Speed CMOS Full-Adder Cell Using a New Circuit Design Technique - Adaptively-Biased Pseudo-NMOS Logic," Proceedings of the ... IEEE, 1990, pp. 562 - 565. Changed on 03/24/04 to: G. Palmisano and G. Palumbo, "Very efficient CMOS low-voltage output stage," Electronics Letters, Vol. 31, No. 21, October 12, 1995, pp. 1830 - 1831.				
Weinstein, Matthew Bryan	weinmatt@mail.umd.edu			

Changed 04/06/04 to: F. J. Kub, K. K. Moon, I. A. Mack and F. M. Long, "Programmable Analog Vector-Matrix Multipliers," IEEE Journal of Solid-State Circuits, Vol. 25, No. 1, February 1990, pp. 207 - 214.				
Whitelock, John Stuart	jwhitelo@wam.umd.edu			
M. Vesterbacka, "A 14-Transistor CMOS Full Adder with Full Voltage-Swing Nodes," ??? reference source???, 1999, pp. 713 - 722.				
Wu, Cheng- Chien	ccwu@wam.umd.edu			
R. R. Harrison and C. Charles, "A Low-Power Low-Noise CMOS Amplifier for Neural Recording Applications," IEEE Journal of Solid-State Circuits, Vol. 38, No. 6, June 2003, pp. 958 - 964.				
Zuckerman, Jay	jzuck@mail.umd.edu			
N. Zhuang and H. Wu, "A New Design of the CMOS Full Adder," IEEE Journal of Solid-State Circuits, Vol. 27, No. 5, May 1992, pp. 840 - 844.				

Total number of students: 56