

## ENEE 302 Spring 2004

Digital Electronics; sections 0201, 0202

## Time Schedule

Tu Th 15:30-16:45; CHM 0127

Tuesday	Thursday	Tuesday	Thursday
01/27/04 freezing rain cancellation	01/29/04 Organize Transistors, Spice Chap 4 MOS transistor, pp. 235-261	02/03/04 Begin paper choice Chap 3, diodes, pp. 147- 163; Chap 5, BJT, pp. 377-402; 487-490	02/05/04 Chap 10, pass transistor logic and inverters pp. 949-962; 990-991
02/10/04 Preliminary paper choice Chap 7 diff pairs, pp. 687-718	02/12/04 Final paper choice	02/17/04 Chap 10 CMOS Logic Gates, pp. 963-974	02/19/04 Chap 10, Domino Logic pp. 996-998
02/24/04 Choose Commentators Chap 11, Memory, ring oscillator pp. 1013-1027	02/26/04 Chap 9, D/A-A/D pp. 922-934 Chap 11, ECL pp. 1052-1063	03/02/04 Signup for presentations Chap 13, Schmitt trigger, pp. 1188-1196	03/04/04 Chap 13 555, pp. 1198-1203
03/09/04 Chap 11, sense amplifiers, addressing, pp. 1038-1048	03/11/04 Begin Circuit Paper Topic (2 min) Presentations	03/16/04	03/18/04 Midterm
03/23/04 Spring recess	03/25/04 Spring recess	03/30/04 Begin Results Presentations	04/01/04
04/06/04	04/08/04	04/13/04	04/15/04
04/20/04	04/22/04	04/27/04	04/29/04
05/04/04	05/06/04 [Reports Due moved to 05/11]	05/11/04 Last Class Reports Due	W 05/19/04 Final Exam 10:30 - 12:30