ENEE 417 - Spring 2003
   Weeks #8-#13
   Base Paper Circuit

  These periods will be concentrated upon your circuit taken from the base paper you have chosen.

1. Design the circuit and run Spice simulations; then construct and test. Use automated testing.

2. Make a vlsi layout for your circuit, do a Spice extraction and compare with the results with your simulations and measurements.

  If interested and willing to make measurements on the fabricated chip, make a submission to MOSIS for which you will need to insert the validated circuit in a set of pads.

3. Write a term paper based upon your results and summarizing your study. Be sure to emphasize what you did and your results while including a study of the base paper circuit. Prepare a computer presentation for an oral report on your base paper study. Submit a written report within a day of the oral report.