ENEE 417 Spring 2003
Microelectronics Design Laboratory
Time Schedule: lectures M 14:00-14:50 EGR 2112
Laboratory: M 08:00-10:50 or Tu 13:00-15:50 or W 16:00-18:50 or Tu 16:00-18:50 ; AVW 1330

Week beginning Tuesday 01/28	Week beginning Tuesday
01/28/03 Familiarize with equipment LabView and MAGIC tutorials Initiate Basic Paper choice	02/04/03 Basic Paper preliminary choice Continue Lab View, DAQ, and MAGIC tutorials
02/11/03 Final Basic Paper choice Transistor curve tracing a) DAQ-LabView, b) Tektronix curve tracer; c) Spice, d) Op-amp curve trace	02/18/03 Snow closure
02/25/03 Parts List for Basic Paper Finish Transistor Curve Tracer Automated Testing - data acquisition VLSI layout	03/04/03 CMOS Vthreshold determination circuit
03/11/03 Finish Vthreshold via VLSI circuit layout and LABView	03/18/03 Ring Oscillator - automated testing and VLSI layout Super capacitors
03/25/03 Spring Break	04/01/03 Basic paper designs
04/08/03 Base paper designs Basic paper measurements	04/15/03 Base paper measurements
04/22/03 Basic paper measurements	04/29/02 Oral Reports
05/06/02 Last Class Week Written Reports Due to TAs	