PRACTISE PROBLEM

ENEE –302

05/10/02 **DK**

1.(a) What function should the circuit given in Fig.1. implement?

(b) Run this circuit using PSpice and check the output by using MNMOSIS (NMOS 2 micron) and MPMOSIS (PMOS 2 micron).What changes have to be made for this circuit to function properly?

(c) Given that the value of p=2, calculate the value of n for MNMOSIS NMOS and MPMOSIS PMOS (hint: consult their models and note that TOX is the same for both types and observe the meaning of KP) .

Fig.1.