

ENEE 302 Paper Choices - Spring 2002

Name/==> commentate for	email	commentated by /paper
Alexander, Mark Aaron ==> Karikari, Kwasi	markalex@wam.umd.edu	Karikari, Kwasi
T. E. Browne, A. I. Onuorah, A. Motayed, and S. N. Mohammad, "Fundamental investigation of the comparative behaviour of small-signal BJT Darlington and cascode amplifiers," International Journal of Electronics, Vol. 38, No. 7, 2001, pp. 737 - 749.		
Alexis, Darcy Victor ==> Masawe, Furia	dvalexis@wam.umd.edu	Masawe, Furia Thomas
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Bacon, Brian Vincent ==> Zaitsev, Elena	bvbacon@wam.umd.edu	Zaitsev, Elena
M.-J. Chen, Y.-B. Gu, J.-Y. Huang, W.-C. Shen, T. Wu, and P.-C. Hsu, "A Compact High-Speed Miller-Capacitance-Based Sample-and-Hold Circuit," IEEE Transactions on Circuits and Systems - I, Vol. 45, No. 2, February 1998, pp. 198 - 201.		
Baran, David George ==> Chamberlain, Brian [???? see Mehta; moved from Popkin, Russell]	dbaran@wam.umd.edu	Chamberlain, Brian
[04/11/02] J. G. Delgado-Frias and J. Nyathi, "A High-Performance Encoder with Priority Lookahead," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 9, September 2000, pp. 1390 - 1393.		
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Bharucha, Zain Pheroze ==> Purohit, Yogeeta	zpb@wam.umd.edu	David, Ronald Jones
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Brooks, Ian Barnett ==> Larrow, Jarrett	ibrooks@wam.umd.edu	Larrow, Jarrett Kevin
V. C. Vincence, C. Galup-Montoro, and M. C. Schneider, "A High-Seing MOS Cascode Bias Circuit," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 11, November 2000, pp. 1325 - 1328.		
Chamberlain, Brian Lee ==> Baran, David George	chamberb@wam.umd.edu	Baran, David George
[04/16/02] S. M. Mishra, S. S. Rofail and K. S. Yeo, "Design of high performance double edge-triggered flip-flops," IEE Proceedings, Circuits, Devices and Systems, Vol. 147, No. 5, October 2000, pp. 283 - 299		
Chiou, Tzu-Hsiu ==> Silva, Prasad	thchiou@wam.umd.edu	Tawasha, Farid Samir
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Chiu, Wang-Wen (Vincent) ==> Lewis, Steven	harvreev@yahoo.com	Lewis, Steven Matthew
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David, Ronald Jones	rjdavid@wam.umd.edu	Purohit, Yogeeta Rajendrab
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Favinger, Gregory Stephen	favinger@wam.umd.edu	Koelsch, Donald James
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[drop 04/11/02] Hale, Dennis Paul	haledp@wam.umd.edu	Hannon, Patrick Michael
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M. Hiraki, K. Yano, M. Minami, K. Sato, N. Matsuzaki, A. Watanabe, T. Nishida, K. Sasaki, and K. Seki, "A. 1.5-V Full-Swing BiCMOS Logic Circuit," IEEE Journal of Solid-State Circuits, Vol. 27, No. 11, November 1992, pp. 1568 - 1574.		
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Hayes, John Raymond	hayes@wam.umd.edu	Seidman, Seth Jacob
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Seidman, Seth Jacob ==> Hayes, John	seidman@wam.umd.edu Hayes, John Raym
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Silva, Prasad Prometheus ==> Tawasha, Farid Samir	pgalpoth@wam.umd.edu Chiou, Tzu-Hsiu
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Singh, Kunal ==> Nguyen-Viet, Tuan	kunals@wam.umd.edu Nguyen-Viet, Tuan Kim
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Sun, Scott Cheng ==> Sharma, Aman	scsun@wam.umd.edu Boadi-Boateng, Kofi
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Tawasha, Farid Samir ==> Chiou, Tzu-Hsiu	tawashaf@wam.umd.edu Silva, Prasad Prometheus
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