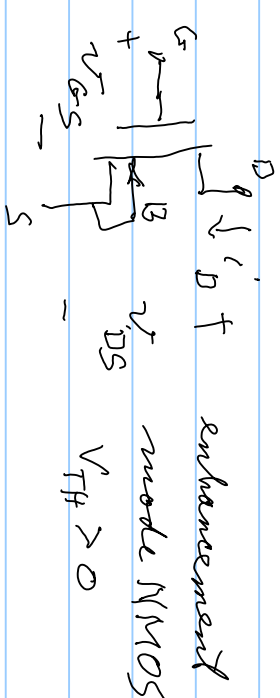


MOS



$$i_D(v_{DS}, v_{GS}) = I_D + \frac{\partial I_D}{\partial v_{DS}} (v_{DS} - V_{DS}) + \frac{\partial I_D}{\partial v_{GS}} (v_{GS} - V_{GS}) + \dots$$

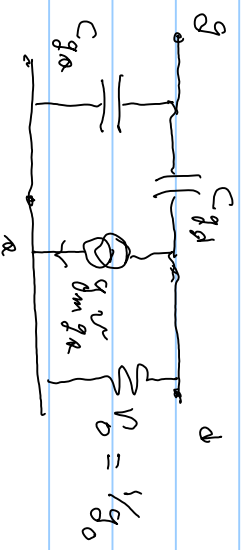
$$\Rightarrow i_D - I_D = i_D'$$

$$v_{DS} - V_{DS} = v_{dR}, \quad v_{GS} - V_{GS} = v_{gR}$$

ignores for small signal

$$i_D' = \frac{\partial I_D}{\partial v_{DS}} \Big|_{Q} v_{dR} + \frac{\partial I_D}{\partial v_{GS}} \Big|_{Q} v_{gR}$$

$$= g_o v_{dR} + g_m v_{gR}$$



C of channel =  $W \cdot L \cdot C_{ox}$  ,  $C_{ox} = \epsilon_{SiO_2} / \text{thickness of oxide}$

$\epsilon_{SiO_2} = 8.854 \times 10^{-12} \frac{F}{m}$

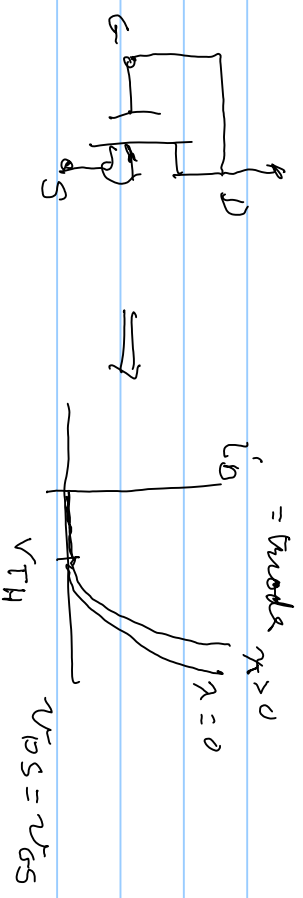
$g_0 \approx \frac{I_D}{\lambda}$  ,  $g_m = \text{mod } V_{DS} = V_{DS} \& \text{ find } \Delta I_D / \Delta V_{GS} \text{ for small } \Delta V_{GS}$

Threshold eqn. ( $V_{DS} > 0, V_{GS} > 0$ )  $\leftarrow = V_{TH}$  ( $V_{TH} = V_{TD}$  if  $V_{BS} = 0$ )

$I_D = \begin{cases} 0 & \text{when } V_{GS} \leq V_{TH} \text{ cutoff} \\ \frac{K_P}{2} \cdot \frac{W}{L} \cdot \begin{cases} (V_{GS} - V_{TH})^2 & \text{saturation} \\ (2(V_{GS} - V_{TH})V_{DS} - V_{DS}^2) & \text{when } V_{GS} - V_{TH} > V_{DS} \end{cases} \end{cases}$

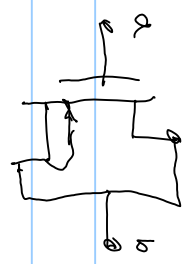
PR 266

drain connected



to make a capacitor

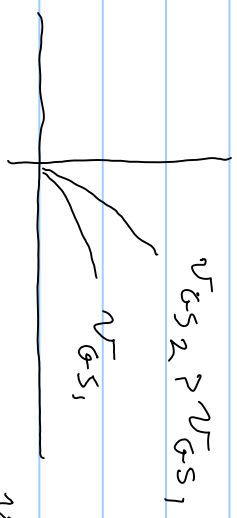
$I_D$



$\Rightarrow$



$$C = C_{ox} W \cdot L$$

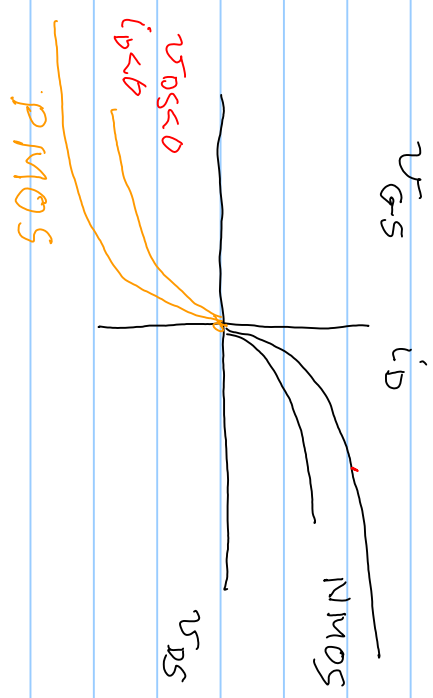
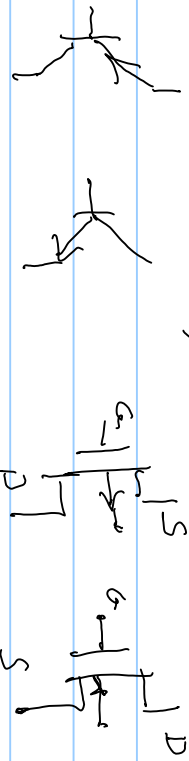


$V_{DS}$

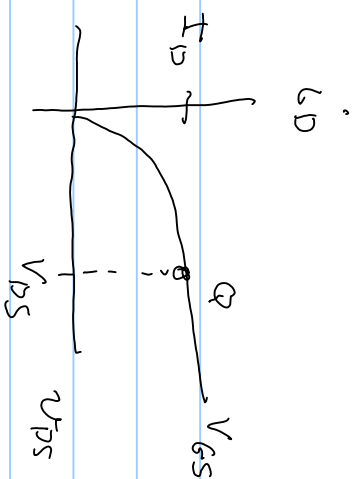
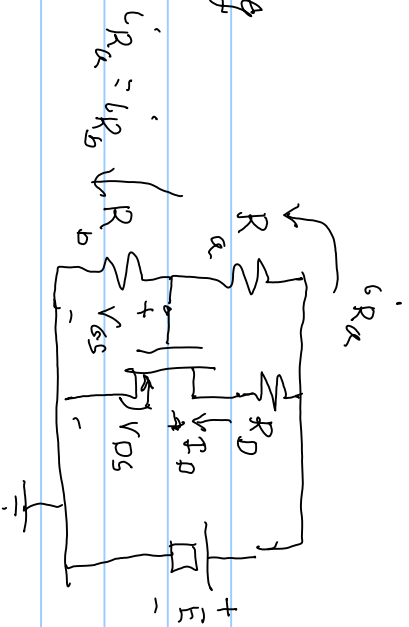
gives a voltage variable resistor

$$I_D \approx \frac{1}{2} C_{ox} (V_{GS} - V_{TH})^2 \cdot \frac{W}{L} = \frac{I_{D0}}{V_{DS}}$$

PMOS or MPN, PMOS or NMOS



Biasing

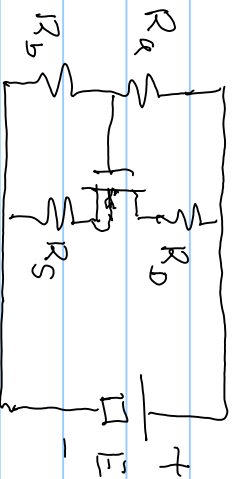


$$V_{GS} = \frac{R_b}{R_a + R_b} \cdot E \quad ; \quad I_D = (E - V_{DS}) / R_D \Rightarrow R_D = \frac{E - V_{DS}}{I_D}$$

$$(R_a + R_b) V_{GS} = R_b E \Rightarrow R_a V_{GS} = R_b (E - V_{GS}) \quad , \quad \frac{R_a}{R_b} = \left( \frac{E}{V_{GS}} - 1 \right)$$

normally choose  $R_b$  large then  $R_a = \left( \frac{E - V_{GS}}{V_{GS}} \right) R_b$

To prevent temperature run away we add  $R_S$



$$V_{DS} = E - (R_D + R_S) I_D$$

$$V_{GS} = \frac{R_b}{R_a + R_b} \cdot E - R_S I_D$$

Have  $\frac{v_o}{v_i} = \frac{v_{dA}}{v_{gA}} - g_m R_L = A_v = \text{voltage gain}$

