

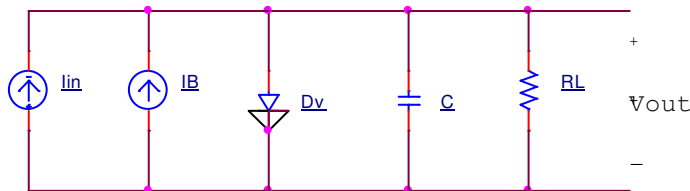
Open book open notes but not open computers; 100 points total (75 minutes);  
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1. (100 points, 75 min)

For the following Vanadium diode circuit assume the diode,  $D_v$ , is described by

$$V = V_d + R_d(I(I^2 - 10^{-6})) \quad V_d = 1V, R_d = 6 \times 10^8 \text{ V/A}^3$$

and  $C = 20\text{nFd}$ .  $I_B = 2\text{mA}$  is a bias current and  $I_{in}$  is a small signal current.



- Sketch the diode  $V$  vs.  $I$  curve for  $-2\text{mA} < I < +2\text{mA}$  giving the local maxima and minima values of  $V$ .
- Add a load line passing through the current bias  $I_B = 2\text{mA}$  point and the  $Q$  point at  $I=0$ .
- Find the value of the load resistance,  $R_L$ , to give this load line.
- Find the small signal diode resistance  $r_d$  at the  $Q$  point.
- Give the small signal differential equation for  $V_{out}(t)$  with  $I_{in}(t)$  as forcing function.
- Give the small signal transfer function  $T(s) = V_{out}/I_{in}(s)$ .
- If the small input current is  $I_{in}(t) = 0.001 \cos(2\pi \cdot 10^{10} t)$ , find the resulting output voltage,  $V_{out}(t)$