

## ENEE 303H Fall 2015

Analog and Digital Electronics; Honors

Time Schedule;

Tu Th 11:00 – 12:15 Room EGR 3114

Tuesday	Thursday	Tuesday	Thursday
09/01/15 Organize Overview of transistors MCAD PSpice for transistors	09/03/15 Spice Diodes BJTs & MOSFETs	09/08/15 Load lines ODE Solutions	09/10/15 Preliminary paper choice
09/15/15 Biasing	09/17/15 Differential Pairs	09/22/15 Final paper choice	09/24/15 Current Mirrors
09/29/15 Small Signal Equivalent Circuits	10/01/15	10/06/15 Admittance Matrices	10/08/15 Signup for presentations Frequency Response
10/13/15 Choose Commentators Inverters	10/15/15 Logic Gates	10/20/15 Memory Signup for TI watch study	10/22/15 Oscillators Clocks
10/27/15 Filters	10/29/15 Midterm	11/03/15 Begin TI watch Study	11/05/15 TI watch Study
11/10/15	11/12/15 Begin 1st oral summary (4 per period for 2+2/3 periods)	11/17/15	11/19/15 Begin Base Paper Presentations (3/period for 3+1/3 periods)
11/24/15	11/26/15 Thanksgiving recess	12/01/15	12/03/15 [End Base Paper Presentations]
12/08/15 Reports due Next period	12/10/15 Last Class Reports due		
			M 12/14/15 08:00-10:00 Final Exam Notebooks due