File G/coursesF13/303H/303HF13hmrk3.doc RWN 09/20/13 303 Fall 2013 Homework 3 – due Tu 10/01/13

1. 50 points (amplifier response)

For the following amplifier assume the 2N2222 is biased at IC=10.5mA, VCE=2V and IB=60uA (as done in class). Check on Spice that this bias holds and if not recalculate Ra & Rb so that it does. Then run transient analysis with vin a sine wave of 1mV amplitude first at 1KHz and then at 100MEGHz. Compare the outputs including amplitude and phases. Then repeat with a lower Rb of 100KOhm keeping the bias as above.



2. 50 points (current mirror)

For the following BJT current mirror using 2N3904 transistors choose R for a design of an input current Iin of 3mA in R (calculate first using VBE=0.7V and then refine by running Spice; record both values of R). For Iin=3mA calculate the theoretical output current when Vout_ideal = Vcc-RIin as well as for all Vout in the range

Vout_ideal \leq Vout \leq Vcc.

Then run Spice and compare the Spice Iout results with your theoretical calculations.

