

ENEE 303H Fall 2013 11/18/13  
Paper Choices

Name/Paper	Email address	presentation Dates	Commentating dates
Albarnaz Farias, Daniel		Tu 11/19/13 #g) Tu 12/10/13 #a) (RWN)	Tu 12/10/13 #c) (RWN)
Wei Li, "A Transistor-Only Low-Pass Filter with Adjustable Bias and Small Phase Shift at High Frequencies," IEEE Journal of Solid-State Circuits, Vol. 28, No. 5, May 1995, pp. 610 - 612.			
Alter, Hirsch Tzvi		Th 11/14/13 #a) Th 11/21/13 #a)	Th 11/21/13 #b)
William McMurray, "Resonant Snubbers with Auxiliary Switches," Proceedings of the IEEE Conference on ???, 1989, pp. 829 - 834.			
Anderson, Daniel C		Tu 11/19/13 #e) Tu 11/26/13 #c)	Tu 12/03/13 #b)
Jinn-Shyan Wang, and Po-Hui Yang, "A Pulse-Triggered TSPC Flip-Flop for High-Speed Low-Power VLSI Design Application, Proceedings of the IEEE Conference on ???, 1998, pp. II-93 - II-95.			
Banks, David Joshua		Tu 11/19/13 #b) Th 11/26/13 #a) (RWN)	Th 11/21/13 #c)
Mark G. Johnson, "A Symmetric CMOS NPR Gate for High-Speed Applications," IEEE Journal of Solid-State Circuits, Vol. 23, No. 5, Oct0ber 1988, pp. 1233 - 1236. [replaced by above 10/31/13] Ramiz Daniel, Sung Sik Woo, Lorenzo Turicchia and Rahul Sarpeshkar, "Analog Transistor Models of Bacterial Genetic Circuits," Proceedings of the IEEE Conference on ???, 2011, pp. 333 - 336.			
Borchers, Benjamin Barrett		Th 11/14/13 #c) Tu 11/26/13 #b) (RWN)	Tu 11/26/13 #a)
Michael Walter Payton and Fat Duen Ho, "A Physically-derived Large-signal Nonquasi-static MOSFET model for Computer Aided Device and Circuit Simulation Part-IIthe CMOS NOR Gate and the CMOS NAND Gate," Proceedings of the IEEE conference on ???, 2005, pp. 5657 - 5661. {backup paper} Michael Walter Payton and Fat Duen Ho, "A Physically-derived Large-signal Nonquasi-static MOSFET Model for Computer Aided Device and Circuit Simulation Part-I MOSFETs and CMOS Inverters," Proceedings of the IEEE Conference on ???, 2005, pp. 4154 - 4158. [replaced by above 10/28/13]Takehiko Adachi, Taiki Ueno and Shoji Izumiya, "A High Frequency Cascode Oscillator with Negative Resistance Enhancement Circuit and Its Analysis," Proceedings of the IEEE Conference on ???, 2005, pp. 522 - 525.			
Gibb, Connor Matthew		Tu 11/19/13 #3c) Th 11/21/13 #b)	Th 12/05/13 #c)
Vibhor Gupta, "Working and Analysis of the H-Bridge Motor Driver Circuit Designed for Wheeled Mobile Robots," Proceedings of the IEEE Conference on ???, 2010, pp. 441 - 444. [replaces 09/24/13]Kodjo Agbossou, Jean-Luc Dion, Sylvian Carignan, Meftah Adelkrim and Ahmed Cheriti, "Class D Amplifier for a Power Piezoelectric Load," IEEE Transactions on Ultrasonics, Ferroelectrics, and Frequency Control, Vol. 47, No. 4, July 2000, pp. 1036 - 1041.			
Gilboy, Matthew Ryan		Th 11/14/13 #d) Tu 12/03/13 #b)	Tu 11/26/13 #c)

<p>Kom-Fa Lin, Yin-Tsung Hwang, Ming-Hwa Sheu and Cheng-Che Ho, "A Novel High-Speed and Energy Efficient 10-Transistor Full Adder Design," IEEE Transactions on Circuits and Systems - I, Vol. 54, No. 5, May 2007, pp. 1050 - 1059.  [changed to above 11/12/13] Christian Peters, Jonas Handweker, Dominic Maurath, and Yiannos Manoli, "A Sub-500 mV Highly Efficient Active Rectifier for Energy Harvesting Applications," IEEE Transactions on Circuits and Systems -I, Vol. 58, No. 7, July 2011, pp. 1542 - 1550.</p>			
Haller, Daniel Austin		Th 11/14/13 #f) Tu 12/03/13 #c)	Tu 11/26/13 #b)
<p>Kimihiro Niship, Taiki Yasuda, and Taknori Tombe, "A Basic System Using Simple Analog Motion Detection Circuit Based on the Biological Vision System and Mobile Robot," Proceedings of the 18<sup>th</sup> IEEE International Symposium on Robot and Human Interactive Communication, Toyama, Japan, September 27, 2009, pp. 494 - 497.  [replaced by above 11/14/13]Hossam Hahmooud gamal ElDin, Mohammed ElAnzeery, Mohamed Abd ElAziz and Saad ElBagouri, "Novel Radio Frequency Energy Harvesting Model," Proceedings of the 2012 IEEE International Power Engineering and Optimization Conference, Melaka, Malaysia, June 6-7 2012, PP. 209 - 213.</p>			
Homble, Nicolas Q		Th 11/14/13 #h) Th 12/05/13 #a)	Th 12/05/13 #b)
<p>Rajeev Kumar and Vimal Kant Pandey, "A New 5-Transistor XOR-XNOR Circuit Based on the Pass Transistor Logic," Proceedings of the 2011 World Congress on Information and Communication Technologies, 2011, pp. 667 - 671.</p>			
Kanga, Justin Ardeshir		Tu 11/19/13 #d) (RWN) Tu 12/10/13 #b) (RWN)	Tu 12/10/13 #a) (RWN)
<p>Budhaditya Majumdar and Sumana Basu, "Low Power Single Bitline 6T SRAM Cell with High Read Stability," Proceedings of the 2011 International Conference on Recent Trends in Information Systems, pp. 169 - 174.  [changed to above 11/18/13]R. A. Henle, "A Multistable Transistor Circuit," Proceedings of the AIEEE Summer General Meeting, November 1955, pp. 568 - 571.</p>			
Lukyanenko, Platon V		Th 11/14/13 #b) Tu 12/03/13 #a) (RWN)	Tu 12/03/13 #c)
<p>Manoj Kumar, Sandeep K. Arya and Sujata Pandey, "Single bit full adder design using 8 transistors with novel 3 transistors XNOR gate," International Journal of VLSI design &amp; Communication Systems, Vol. 2, No. 4, December 2011, pp. 47 - 59.  [replaced by above 11/13/13][author??] "Random Number Generation with a Simple Transistor Junction Noise Source," [reference data ???]</p>			
Orlando, Kyle Richard		Th 11/14/13 #e) Th 12/05/13 #c) (RWN)	Tu 12/03/13 #a)
<p>Hung Tien Bui, Abdul Karim Al-Sheraidah, and Yuke Wang, "New 4-Transistor XOR and XNOR Designs," Proceedings of the IEEE Conference on ???, 2000, pp. 25 - 28.</p>			
Sawyer, Ryan Michael		Th 11/14/13 #g) Th 12/05/13 @b)	Th 12/05/13 #a)
<p>Khanittha Kaewdang, Wanlop Surakampontrorn, and Nobou Fujii, "A Design of Controllable Gain Class B Push-pull Current Amplifier, Proceedings of the IEEE Conference on ???, 2005, pp. 1577 - 1580.</p>			
Stubbs, Kevin Diemer		Tu 11/19/13 #f) Tu 12/10/13 #c) (RWN)	Tu 12/10/13 #b) (RWN)
<p>M. Alioto, R. Mita, and G. Palumbo, "Analysis and Comparison of Low-Voltage CML D-Latch," Proceedings of the IEEE Conference on ???, 2002, pp. 737 - 740.</p>			

Tracy, Mitchell Fremont		Tu 11/19/13 #a) Th 11/21/13 #c) (RWN)	Th 11/21/13 #a)
T. Matsumoto, L. O. Chua, and K. Tokumasu, "Double Scroll Via a Two-Transistor Circuit," IEEE Transactions on Circuits and Systems, Vol. CAS-33, No. 8, August 1986, pp. 828 - 835.			

Number of students = 15