ENEE 303H Fall 2013 – Midterm Exam Add On Due Tu 12/10/13
Take Home. Open book open notes; 35 points total (1 hour max). Good luck
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For the NMOS transistor assume: KP=5x10⁻⁴, VTO=1, LAMBDA=0.01, W/L= 1.
Cinf is very large so assume it to be a short for signal.

1. (35 points)

For the following circuit all resistors are of the same resistance R with R chosen such that the drain to source bias voltage is $V_{DS}=4V$; Vdd=8V.

- a) Show that the transistor is biased in the saturation region and with that give the value of the drain bias current I_D .
- b) Find the value of R
- c) Give numerically the transistor's g_m and g_o .
- d) Draw the small signal equivalent circuit including Cgs & Cgd using generic symbols (without numerical values).
- e) When Cgd=0 find (without numerical values) the small signal (transresistance) gain, Vo/Iin(s) and give its poles and zeros (Vo measured with respect to ground).
- f) Evaluate numerically the poles and zeros when Cgs=5pFd.

