## RWN 12/26/11 ENEE 303H Fall 2011 Analog and Digital Electronics Time Schedule; sections 0101

Tu Th 09:30 – 10:45 Room EGR 2116

Tuesday	Thursday	Tuesday	Thursday
	09/01/11	09/06/11	09/08/11
	Organize	Spice	Load lines
	Overview of transistors	Diodes	ODE Solutions
		BJTs & MOSFETs	
09/15/11	09/15/11	09/20/11	09/22/11
Preliminary paper	09/10/11	Final paper choice	Current Mirrors
choice	Differential Pairs		
Biasing			
09/27/11	09/29/11	10/04/11	10/06/11
Signup for presentations	Choose Commentators	Admittance Matrices	Frequency Response
Small Signal			1 7 1
Equivalent Circuits			
10/11/11	10/13/11	10/18/11	10/20/11
Inverters	Logic Gates	Memory	Oscillators
			Clocks
10/25/11	10/27/11	11/01/11	11/03/11
		MIDTERM	Begin
			1 <sup>st</sup> oral summary
			(5 per period [+1] for 3
11/08/11	11/10/11	11/15/11	periods) 11/17/11
11/06/11	11/10/11	RWN unavailable	Begin Base Paper
		KWIV anavanaore	Presentations
			(3 per period for 6
			periods)
11/22/11	11/24/09	11/29/11	12/01/11
	Thanksgiving		
	recess		
12/06/11	12/08/11	12/13/11	
	End Base Paper	Last Class	
	Presentations	Reports due	F 12/16/11
			08:00-10:00
			Final Exam
			Notebooks due