

Paper Choices

Name/Paper	Email address	presentation Dates	Commentating dates
Buzduga, Alexandru		Th 11/04/10 d) Th 11/11/10 b)	Tu 11/23/10 a) Tu 11/30/10 a)
M. Jeong, S. Takagi, Z. Czarnul and N. Fujii, "Design of a Novel Linear 3-Input CMOS OTA and Its Application to Filter Realization," Proceedings of the IEEE Asia Pacific Conference on Circuits and Systems, Seoul, Korea, November 1996, pp. 121 - 124.			
Datta-Chaudhuri, Timir Bar		Tu 11/16/10 a) Tu 11/09/10 a)	Th 12/02/10 a) Th 11/18/10 a)
P. Livi and G. Indiveri, "A current-mode conductance-based silicon neuron for address-event neuromorphic systems," Proceedings of the 2009 IEEE ISCAS, May 2009, pp. 2898 - 2901.			
Gavini, Chinmoy		Tu 11/09/10 b) Th 11/04/10 a)	Tu 11/30/10 c) Tu 12/07/10 c)
Z. M. M. Myo, A. M. Aung, and Z. M. Naing, "Design and Implementation of Active Band-Pass Filter for Low Frequency RFID (Radio Frequency Identification) System," Proceedings of the International Multiconference of Engineers and Computer Scientists, 2009, Vol. 1, pp. 364 - 367. [replaces: 10/01/10]			
S. Nikoloudis and C. Psychalinos, "Multiple Input Single Output Universal Biquad Filter with Current Feedback Operational Amplifiers," Circuits, Systems, and Signal Processing, published online, April 30, 2010. 14 pages.			
Gyawali, Bipin		Tu 11/09/10 b) Tu 11/16/10 c)	Th 12/02/10 b) Tu 12/07/10 b)
M. Milkovic, "Current Gain High-Frequency CMOS Operational Amplifiers," IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 4, August 1985, pp. 845 - 851.			
Huang, Le		Th 11/11/10 d) Th 11/11/10 a) RWN	Th 11/18/10 b) Tu 11/23/10 b) RWN
R. R. Harrison and C. Charles, "A Low-Power Low-Noise CMOS Amplifier for Neural Recording Applications," IEEE Journal of Solid-State Circuits, Vol. 38, No. 6, June 2003, pp. 958 - 965.			
Jendzurski, John		Th 11/04/10 a) Tu 11/09/10 c)	Tu 12/07/10 c) Tu 11/30/10 c)
C. G. Haigh and R. Jeffers, "The Design of an Audio-frequency Active RC Band-pass Filter for a Specific Requirement," The Radio and Electronic Engineer, Vol. 42, No. 8, August 1972, pp. 373 - 380.			
Jiang, Fei		Th 11/11/10 b) Th 11/04/10 d)	Tu 11/30/10 a) Th 11/18/10 a) RWN
M. Sagbas, U. E. Ayten and H. Sedef, "Current and voltage transfer filters using a single active device," IET Circuits, Devices & Systems, Vol. 4, No. 1, 2010, pp. 78 - 86.			
Jun, Mi Jung		Tu 11/09/10 d) Th 11/04/10 b)	Th 12/02/10 c) Th 11/18/10 c)
K. Dejhan, P. Toooprakai, S. Mitatha, F. Cheevasuvit and C. Soonyeean, "1V Full Swing Bootstrapped CMOS Inverter Circuit," Proceedings of ICSE2002, Penang, Malaysia, pp. 275 - 278.			
Lee, Andrew Wuham		Tu 11/16/10 d) Tu 11/16/10 b) RWN	Tu 11/30/10 b) Tu 11/23/10 c) RWN

C.-F. Liao and S.-J. Liu, "A Broadband Noise-Canceling CMOS LNA for 3.1-10.6-GHZUWB Receiver," Proceedings of the IEEE 2005 Custom Integrated Circuits Conference, pp. 8-5-1 - 8-5-4.			
Madore, Christine Michelle		Tu 11/16/10 b) Th 11/04/10 c)	Tu 12/07/10 a) Th 11/18/10 b)
M. Mahowald & R. Douglas, "A silicon neuron," Nature, Vol. 354, No. 6354, December 26, 1991, pp. 515 - 518.			
Rzasa, John Robertson		Tu 11/09/10 a) Tu 11/16/10 a)	Tu 11/18/10 a) Th 12/02/10 a)
J.-K. Seon, "A fully integrated CMOS inverse sine circuit for computational systems," International Journal of Electronics, Vol. 97, No. 8, August 2010, pp. 867 - 882.			
Vahdat, Khadijeh A		Th 11/04/10 b) Tu 11/09/10 d)	Th 11/18/10 c) Th 12/02/10 c)
F. Sandoval-Ibarra, E. J. Vargas-Calderon, J. A. Herrera-Barragan, and J. M. Garcil-Leon, "Designing a CMOS Differential Pair," Proceedings of the IEEE 15 th CONIELECOMP 2005, 4 pages.			
Xiao, Bo		Tu 11/16/10 c) Tu 11/09/10 b)	Tu 12/07/10 b) Th 12/02/10 b)
O. T. Inan, and T. A. Kovacs, "An 11 uW, Two-Electrode Transimpedance Biosignal Amplifier With Active Current Feedback Stabilization," IEEE Transactions on Biomedical Circuits and Systems, Vol. 4, No. 2, April 2010, pp. 93 - 100.			
Xie, Ting Dropped 10/13/10		Tu 11/09/10 e) Tu 11/02/10 e) RWN	Th 11/18/10 e) Tu 11/23/10 e) RWN
Anonymous, "General Magnetic field sensors," Philips Semiconductors data Sheet, June 12, 1998.			
Yeh, Jen-Hao		Th 11/04/10 c) Th 11/11/10 c)	Tu 11/23/10 c) Tu 12/07/10 a)
M. Blaszczyk and R. A. Guinee, "A novel modeled true random binary number generator for key stream generation in cryptographic applications," Proceedings of the 2008 IEEE Conference on Military Communications, San Diego, November 2008, pp. 1 - 7.			
Zhao, Minshu		Th 11/11/10 d) Tu 11/16/10 d)	Tu 11/23/10 b) Tu 11/30/10 b)
G. R. Karimi and E. Nazari, "An Ultra low Voltage Amplifier Design Using Forward Body Bias Folded Cascode Topology for 5GHz Application," Proceedings of the 2010 5 th IEEE Conference on Industrial Electronics and Applications, pp. 1838 - 1842. [changed from 10/08/10}A. Basu, R. W. Robucci, and P. E. Hasler, "A Low-Power, Compact, Adaptive Logarithmic Transimpedance Amplifier Operating Over Seven Decades of Current," IEEE Transactions on Circuits and Systems - I, Vol. 54, No. 10, October 2007, pp. 2167 - 2177.			

Number of students = 16