

Paper Choices

Name/Paper	Email address	presentation Dates	Commentating dates
Abrams, David Wesley		Tu 11/10/09 g) Tu 12/01/09 c)	Th 11/05/09 a) Th 12/03/09 a)
K. Koli and K. Halonen, "Low-voltage MOS-transistor-only precision current peak detector with signal independent discharge time constant," Proceedings of the IEEE International Symposium on Circuits and Systems, June 9-12, 1997, Hong Kong, pp. 1992 - 1995.			
Bauman, Scott Eliot		Tu 11/03/09 a) Th 11/12/09 a) → Tu 12/08/09 b)	Tu 11/03/09 c) Th 11/19/09 c)
K. Nishimura, K. Hirachi, S. Komiyama and M. Nakaoka, "Two Buck Choppers Built-In Single Phase One Stage PFC Converter with Reduced DC Voltage Ripple and Its Specific Control Scheme," [ref. ???, ieee explore] IEEE 2008, pp. 1378 - 1383.			
Chang, Mu-Tien		Tu 11/03/09 d) Tu 11/17/09 a)	Tu 11/03/09 b) Th 11/19/09 a)
F. Cannillo, C. Toumazou and T. S. Lande, "Nanopower Subthreshold MCML in Submicrometer CMOS Technology," IEEE Transactions on Circuits and Systems - I, Vol. 56, No. 8, August 2009, pp. 1598 - 1611.			
Gandapur, Tamoor		Tu 11/10/09 f) Tu 11/24/09 b)	Tu 11/10/09 d) Tu 11/24/09 a)
I. M. Filanovsky and H. Baltas, "CMOS Schmitt Trigger Design," IEEE Transactions on Circuits and Systems - I, Vol. 41, No. 1, January 1994, pp. 46 - 49 Changed from 11/10/09 D. L. Butler and R. J. Baker, "Low-Voltage Bandgap Reference Design Utilizing Schottky Diodes," [ref.????]			
Goldman, Marc Gregory		Tu 11/10/09 a) Tu 12/08/09 b)	Tu 11/03/09 e) Th 11/19/09 b)
Dropped 11/10/09 ? W. Wattanapanitch, Michale Fee and R. Sarpeshkar, "An Energy-Efficient Micropower Neural Recording Amplifier," IEEE Transactions on Biomedical Circuits and Systems, Vol. 1, No. 2, June 2007, pp. 136 - 147.			
Haile, Benyam Tamene		Tu 11/10/09 c) Th 11/19/09 b)	Tu 11/10/09 a) Tu 12/08/09 b) → Th 11/19/09 a)
O/ M. Filanovsky and V. A. Piskarev, "Sensing and Measurement of dc Current Using a Transformer and RL-Multivibrator," IEEE Transactions on Circuits and Systems, Vol. 38, No. 11, November 1991, pp. 1366 - 1370.			
Hung, Ling		Tu 11/03/09 b) Th 11/19/09 a)	Tu 11/10/09 c) Tu 11/17/09 b)
C.-H. Wu, C.-H. Lee, W.-S. Chen and S.-I. Liu, "CMOS Wideband Amplifiers Using Multiple Inductive-Series Peaking Technique," IEEE Journal of Solid-State Circuits, Vol. 40, No. 2, February 2005, pp. 548 - 552.			
Lee, Tsung-Hsueh		Tu 11/03/09 e) Tu 11/17/09 b)	Tu 11/03/09 d) Tu 11/17/09 a)
Y. L. Wong, M. H. Cohen and P. A. Abshire, "A 1.2-GHz Comparator with Adaptable Offset in 0.35-um CMOS," IEEE Transactions on Circuits and Systems - I, Vol. 55, No. 9, October 2008, pp. 2584 - 2594.			
Mehraei, Golbarg		Th 11/05/09 g) Tu 12/01/09 a)	Th 11/05/09 f) Tu 11/17/09 c)

G. Indiveri, "A Low-Power Adaptive Integrate-and-Fire Neuron Circuit," [ref. ??? IEEE explore] 2003, pp. IV-820 - IV-823.			
Ogunbiyi, Martins O		Th 11/05/09 a) Th 12/03/09 a)	Tu 11/10/09 g) Tu 12/01/09 c)
A. M. Ismail and A. M. Soliman, "Novel CMOS Wide-Linear-Range Transconductance Amplifier," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 8, August 2000, pp. 1248 - 1253.			
Park, Myung Hwan		Th 11/05/09 d) Tu 12/08/09 a)	Tu 11/03/09 a) Th 11/12/09 a) → Tu 12/08/09 b)
R. Zhang, H. D. D. de S. Cavalcante, Z. Gao, D. J. Gauthier, J. E. S. Cocolar, M. M. Adams and D. P. Lathrop "Boolean Chaos," Http://arxiv.org/pdf/0906.4124 Changed from 11/04/09: L. O. Chua, "The Genesis of Chua's Circuit," AEU, Vol. 46, No. 4, 1992, pp. 250 - 257.			
Rodriguez, Miguel		Th 11/05/09 c) Tu 11/17/09 c)	Th 11/05/09 g) Th 12/03/09 c)
E. I. Ortiz-Rivera and F. Z. Peng, "Analytical Model for a Photovoltaic Module Using the Electrical Characteristics Provided by the Manufacturer Data Sheet," [ref. ???]			
Shakeel, Hamza		Tu 11/10/09 d) Tu 11/24/09 a)	Tu 11/10/09 f) Tu 11/24/09 b)
S.-I. Liu and Y.-S. Hwang, "CMOS Four-Quadrant Multiplier Using Bias Feedback Techniques," IEEE Journal of Solid-State Circuits, Vol. 29, No. 6, June 1994, pp. 750 - 752.			
Tao, Tao		Th 11/05/09 b) Tu 11/24/09 c)	Tu 11/10/09 b) Tu 12/08/09 c)
C. P. Raj P and S. L. Pinjare, "Design and Analog VLSI Implementation of Neural Network Architecture for Signal Processing," European Journal of Scientific Research, Vol. 27, No. 2, 2009, pp. 199 - 216.			
Tschirhart, Paul Kenton		Th 11/05/09 f) Th 12/03/09 c)	Th 11/05/09 c) Tu 12/01/09 a)
P. Hafliger and H. K. Riis, "A Multi-Level Static Memory Cell," [ref. ???]			
Wang, Hao		Tu 11/10/09 b) Tu 12/08/09 c)	Th 11/05/09 b) Tu 11/24/09 c)
C. T. Charles and D. J. Allstot, "A 2-GJz CMOS Variable Gain Amplifier Optimized for Low Noise," Proceedings of the 2006 ISCAS (ieee explore) pp. 2305 - 2308.			
Wang, Haoyu		Tu 11/10/09 e) Tu 12/01/09 b)	Th 11/05/09 e) Th 12/03/09 b)
R. R. Harrison and C. Charles, "A Low-Power Low-Noise CMOS Amplifier for Neural Recording Applications," IEEE Journal of Solid-State Circuits, Vol. 38, No. 6, June 2003, pp. 958 - 965.			
Yesilkoy, Filiz		Tu 11/03/09 c) Th 11/19/09 c)	Th 11/05/09 d) Tu 12/08/09 a)
N. M. Nelson and P. A. Abshire, "Chopper Modulation Improves OTA Information Transmission," [Ref. ???] IEEE pp. 2275 - 2278.			
Zhao, Wei		Th 11/05/09 e) Th 12/03/09 b)	Tu 11/10/09 e) Tu 12/01/09 b)
R. Hidayat, K. Dejhan, P. Moungnoul and Y. Miyanaga, "OTA-Based High Frequency CMOS Multiplier and Squaring Circuit," Proceedings of the 2008 International Symposium on Intelligent Signal Processing and Communication Systems, Bangkok, 2008, 4 pages.			

Number of students = 19