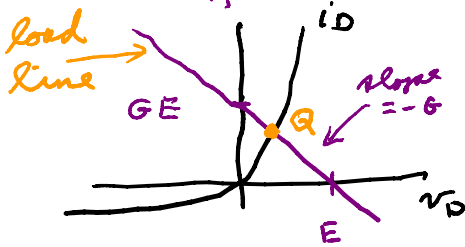
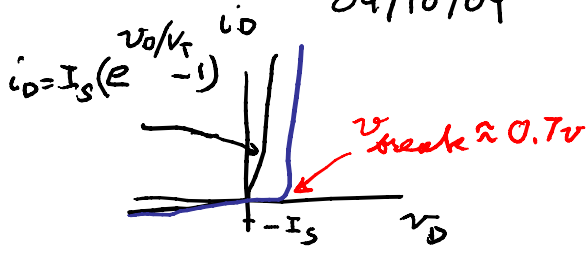
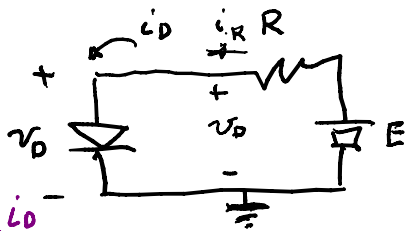


Load lines

EE303H
09/10/09



KVL $v_D = E - R i_R$

but $i_R = i_D$ by KCL

$$i_R = -\frac{(v_D - E)}{R} = -\frac{v_D}{R} + \frac{E}{R}$$

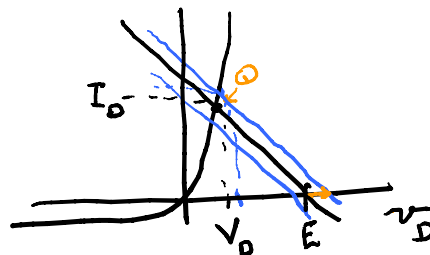
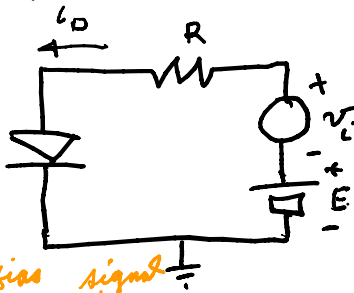
$$= -G v_D + GE$$

law of components

Q = operating point of this circuit
= quiescent point
= bias point

R = load resistor on the diode

To use put a signal on top of E, v_i



total $i_D = I_D + i_d$
bias $v_D = V_D + v_d$
signal

if v_i is small i_d is small
" " " " " "

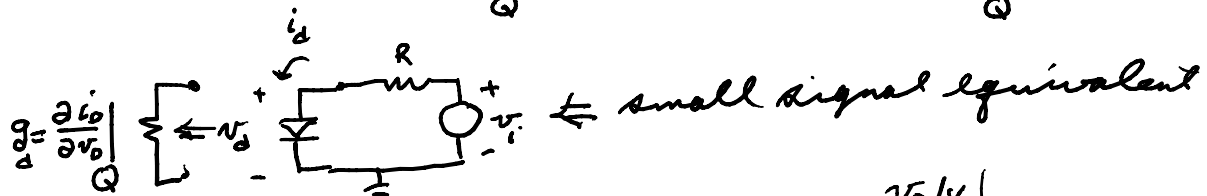
$$i_D - I_D = i_d, \quad V_D - V_D = v_d \Rightarrow$$

To evaluate i_d vs. v_d use Taylor series expansion

$$i_D = i_D \Big|_Q + \frac{\partial i_D}{\partial v_D} \Big|_Q (v_D - V_D) + \frac{1}{2!} \frac{\partial^2 i_D}{\partial v_D^2} (v_D - V_D)^2 + \dots$$

if v_d is small, i.e. $|v_D - V_D|$ is small

$$\approx i_D = I_D + \left. \frac{\partial i_D}{\partial v_D} \right|_Q v_d \Rightarrow i_D - I_D = i_d = \left. \frac{\partial i_D}{\partial v_D} \right|_Q v_d$$



$$i_D = I_S (e^{v_D/V_T} - 1); \quad \left. \frac{\partial i_D}{\partial v_D} \right|_Q = \frac{I_S}{V_T} \cdot e^{v_D/V_T} \Big|_{v_D = V_D}$$

$$g_d = \frac{I_S}{V_T} e^{V_D/V_T}$$

