

ENEE 303H Fall 2009

Analog and Digital Electronics

Time Schedule; sections 0101 & 0102

Tu Th 14:00-15:15; JMP 1109

Tuesday	Thursday	Tuesday	Thursday
09/01/09 Organize Overview of transistors Spice	09/03/09 Diodes & Load lines	09/08/09 BJTs & MOSFETs	09/10/09 Biasing Preliminary paper choice
09/15/09 Current Mirrors Final paper choice	09/17/09 Small Signal Equivalent Circuits	09/22/09 Admittance Matrices	09/24/09 Signup for presentations Frequency Response
09/29/09 Choose Commentators	10/01/09 Inverters Logic Gates	10/06/09 Memory	10/08/09 Oscillators Clocks
10/13/09	10/15/09	10/20/09	10/22/09 Begin 1 st oral summary (6 per period for 5 periods)
10/27/09	10/29/09	11/03/09	11/05/09 MIDTERM
11/10/09	11/12/09 Begin Base Paper Presentations (4 per period for 8 periods)	11/17/09	11/19/09
11/24/09	11/26/09 Thanksgiving recess	12/01/09	12/03/09
12/08/09	12/10/09 Last Class End Base Paper Presentations Most reports due		
			Th 12/17/09 10:30-12:30 Final Exam Notebooks due