

ENEE 610 Fall 2008 10/28/08

Paper Choices

Name/Paper	Email address	presentation Dates	Commentating dates
Askari Farahani, Foad		W 11/12/08 #4 M 12/08/08 #4	W 11/12/08 #3 W 12/03/08 #1
M. Milkovic, "Current Gain High-Frequency CMOS Operational Amplifiers," IEEE Journal of Solid-State Circuits, Vol. sc-20, No. 4, August 1985, pp. 845 - 851.			
Bansal, Chetan R		W 11/12/08 #2 W 12/03/08 #2	W 11/12/08 #1 M 12/08/08 #2 (RWN)
S. Mitra, S. Fusi and G. Indiveri, "A VLSI spike-driven dynamic synapse which learns only when necessary," Proceedings of ISCAS 2006, pp. ???			
Brown, Cassaundra R		W 11/12/08 #1 M 12/08/08 #2	W 11/12/08 #2 W 12/03/08 #2 (RWN)
L. Fortuna, M. Frasca, M. Gioffre, M. L. Rosa, N. Malgnino, A. Marcellino, D. Nicolosi, L. Occhipinti, F. Porro, G. Sicurella, E. Umana and R. Vecchione, "On the way to plastic computation," IEEE Circuits and Systems Magazine, Vol. 8, No. 3, pp. 6 - 18.			
Chiang, Jui-Ping		M 11/10/08 #1 M 11/24/08 #1	M 11/17/08 #2 W 12/03/08 #3
P. R. Kinget and M. S. J. Steyaert, "A 1-GHz CMOS Up-Conversion Mixer," IEEE Journal of Solid-State Circuits, Vol. 32, No. 3, March 1997, pp. 370 - 376.			
Dave, Khyati Kishorkumar		M 11/17/08 #2 W 12/03/08 #3	M 11/10/08 #3 M 12/01/08 #2
M. D. Scott, B. E. Boser and K. S. J. Pister, "An Ultralow-Energy ADC for Smart Dust," IEEE Journal of Solid-State Circuits, Vol. 38, No. 7, July 2003, pp. 1123 - 1129.			
Dornajafi, Mahsa		W 11/12/08 #3 W 12/03/08 #1	W 11/12/08 #4 M 12/08/08 #4
K. Koli and K. Halonen, "Low voltage MOS-transistor-only precision current peak detector with signal independent discharge time constant," Proceedings of the 1997 IEEE International Symposium on Circuits and Systems, Hong Kong, June 9-12, 1997, pp. 1992 - 1995.			
George, Ayanah Sintelee		M 11/17/08 #3 M 12/18/08 #3	M 11/17/08 #4 M 12/01/08 #3
A. Cichocki and R. Unbehauen, "Neural Networks for Solving System of Linear Equations and Related Problems," IEEE Transactions on Circuits and Systems - I, Vol. 39, No. 2, February 1992, pp. 124 - 138.			
Johnson, Terrell		M 11/17/08 #4 RWN M 12/01/08 #3 RWN	M 11/17/08 #3 M 12/08/08 #3
D. Senderowicz, S. F. Dreyer, J. H. Huggins, C. F. Rahim and C. A. Laber, "A Family of Differential NMOS Analog Circuits for a PCM Codec Filter Chip," IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 6, December 1982, pp. 1014 - 1023.			
Lang, Garrett Seth		M 11/17/08 #1 M 12/08/08 #1	?Need to change? W 11/05/08 #1 M 11/24/08 #2
A. Demosthenous and M. Panovic, "Low-Voltage MOS Linear Transconductor/Squarer and Four-Quadrant Multiplier for Analog VLSI," IEEE Transactions on Circuits and Systems - I, Vol. 52, No. 9, September 2005, pp. 1721 - 1731.			
Liu, Benben		M 11/10/08 #2 M 12/01/08 #1	M 11/10/08 #4 M 11/24/08 #3
B. Razavi, "Design of a 100-MHz 10mW 3-V Sample-and-Hold Amplifier in Digital Bipolar Technology," IEEE Journal of Solid-State Circuits, Vol. 30, No. 7, July 1995, pp. 724 - 730.			

Safar, Mohammad A A A		M 11/10/08 #3 M 12/01/08 #2	M 11/10/08 #1 M 11/24/08 #1
E. Yuce and S. Minaei, "A Modified CFOA and Its Applications to Simulated Inductors, Capacitance Multipliers, and Analog Filters," IEEE Transactions on Circuits and Systems - I, Vol. 55, No. 1, February 2008, pp. 266 - 275.			
Shiu, John Chung-En		M 11/10/08 #4 M 11/24/08 #3	M 11/10/08 #2 M 12/01/08 #1
A. K. M. Kamruzzaman Mollah, R. Rosales, S. Tabatabaei, J. Cicalo and A. Ivanov, "Design of a Tunable Differential Ring Oscillator with Short Start-Up and Switching Transients," IEEE Transactions on Circuits and Systems - I, Vol. 54, No. 12, December 2007, pp. 2669 - 2681.			

Number of students =12