

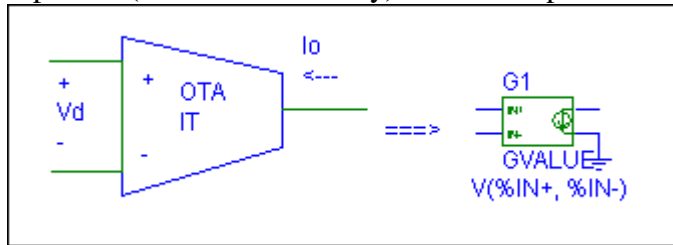
610 Fall 2008 – Homework 5

1. Synthesize using the Richards' function (and compare the results of a) with b)):

a) $y(s) = (s^2+s+1)/(s^2+s+4)$

b) $z(s) = (s^2+s+1)/(s^2+s+4)$ (which is in Secs. 8.5,6 of the book via Brune & Bott-Duffin)

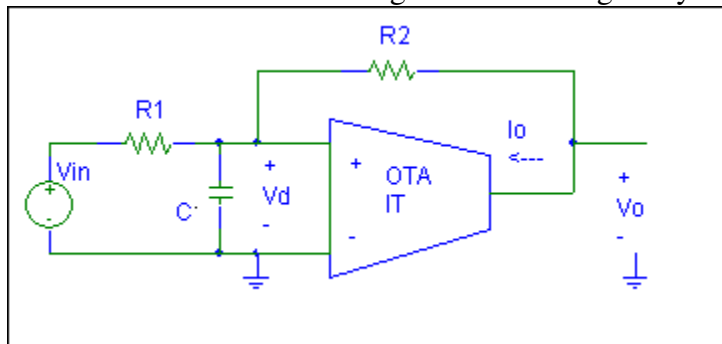
2. This problem is associated with hysteresis. First is the diagram of an OTA (Operational Transconductance Amplifier) where in Spice the idealized OTA can be simulated using a GVALUE component (in the ABM library) with the expression for I_o as the gain



An OTA is constructed as a differential pair with an “active” load to give the output to be the difference current of the pair collector (or drain) currents. In the NPN BJT pair case the current difference, I_o , is derived to be

$$I_o = -I_T \cdot \tanh(V_d / (2V_T))$$

where I_T is the tail current. Also input current (into the difference voltage V_d leads) is assumed to be 0. With proper choice of I_T and R_1 the following circuit should give hysteresis.



- Using semistate variables $x = [V_d \ I_o]^T$ set up semistate equations assuming input $u = V_{in}$ and output $y = [I_o \ V_o]^T$.
- For this circuit show that the DC load line equation is (where $G_1 = 1/R_1$)

$$I_o = -G_1(V_d - V_{in})$$
- Sketch the OTA curve of I_o versus V_d with the load line curves of I_o versus V_d for several important values of V_{in} . From that show that this circuit has hysteresis at DC for some values of R_1 given I_T . Calculate the smallest R_1 , call it R_{min} , such that hysteresis will result (for this you can set the tangents equal for the OTA curve and the load line). Find the voltages, $\pm V_j$, at which the hysteresis jumps. Sketch I_o versus V_{in} showing the hysteresis.
- Choose $R_1 = 2R_{min}$, $R_2 = R_1$, $I_T = 104 \mu A$ ($= 4 \times 26 \mu A$), $V_T = 26 mV$, $C = 0$, and run a transient response in Spice using an input voltage that is triangular via the piecewise linear voltage source, VPWL, starting at 0, going slowly negative to twice the hysteresis negative break voltage, and then to positive twice the hysteresis positive jump voltage, and then back to the most negative value. Plot I_o versus V_{in} .
- Calculate, at DC, V_o versus V_{in} and sketch the result; compare with Spice results.