

## ENEE 303 Analog and Digital Electronics – Base Paper Choices

name	Section/ email	First talk date	Second talk date	Commentator dates
Paper				
Adkins, Daniel R	0101	Tu 10/28/08 #11	Th 11/13/08 #4	Tu 11/25/08 #5
P. Duchene, M. J. Declercq and S. M. Kang, "Simple CMOS Transition Accelerator Circuit," Electronics Letters, Vol. 27, No. 4, February 14, 1991, pp. 300 - 302.				
Alam, Niel J	0102	Tu 11/04/08 #6	Tu 12/09/08 #3	Tu 12/09/08 #4
L. Alvado, J. Tomas, S. Renaud-Le Masson and V. Douence, "Design of an Analogue ASIC Using Subthreshold CMOS Transistors to Model Biological Neurons," Proceedings of the IEEE Custom Integrated Circuits Conference, 2001, pp. 6-4.1 - 6-4.4 = pp. 97 - 100. Changed from the following on 10/14/08: H. C. Lin, T. B. Tan, G. Y. Chang, V. van der Leest and N. Formigoni, "Lateral Complementary Transistor Structure for the Simultaneous Fabrication of Functional Blocks," Proceedings of the IEEE, Vol. ?, No. 12, December 1964, pp. 1491 - 1495.				
Apolonio, Leonardo Mark	0101	Tu 11/04/08 #9	Tu 12/02/08 #1	Th 11/13/08 #6 (RWN changed)
D. J. Lynes and D. A. Hodges, "Memory Using Diode-Coupled Bipolar Transistor Cells," IEEE Journal of Solid-State Circuits, Vol. SC-5, No. 5, October 1970, pp. 186 - 191.				
Asendorf, Nicholas Andrew	0102	Tu 10/28/08 #2	Tu 11/25/08 #4	Tu 11/25/08 #1
C. T. Armijo and R. G. Meyer, "A New Wide-Band Darlington Amplifier," IEEE Journal of Solid-State Circuits, Vol. 24, No. 4, August 1989, pp. 1105 - 1109.				
Aung, Sai H	0101	Tu 11/04/08 #10	Tu 12/09/08 #4	Tu 12/09/08 #3
F. Corsi and C. Marzocca, "An Approach to the Analysis of the CMOS Differential Stage with Active Load and Single-Ended Output," IEEE Transactions on Education, Vol. 46, No. 3, August 2003, pp. 325 - 328.				
Bardack, Avi	0102	Tu 11/04/08 #4	Th 12/04/08 #2	Th 12/04/08 #1
M. De Matteis, S. D'Amic and A. Baschiotto, "A 600mV 1.32mW 75dB-DR 4 <sup>th</sup> -order Baseband Analog Filter for UMTS Receivers," [publication data?]				
Brisentine, Robert Allen	0101	Tu 10/28/08 #1	Tu 11/11/08 #3	Th 11/20/08 #3
W. Li, "A Transistor-Only Low-Pass Filter with Adjustable Bias and Small Phase Shift at High Frequencies," IEEE Journal of Solid-State Circuits, Vol. 28, No. 5, May 1991, pp. 610 - 612.				
Buxton, Brian John	0101	Tu 10/21/08 #5	Tu 11/11/08 #1	Th 12/11/08 #4
H. Y. Yang and R. Sarpeshkar, "A Time-Based Energy-Efficient Analog-to-Digital Converter," IEEE Journal of Solid-State Circuits, Vol. 40, No. 8, August 2005, pp. 1590 - 1601.				
Cervino, Noel Philip	0101	Tu 10/28/08 #6	Tu 11/11/08 #5	Tu 11/11/08 #3
A. Kielbasinski, "Another Simple Transistor-Only Limped-Distributed Tunable Low-Pass Filter," [??? Reference data]				
Chandhok, Ajay	0101	Tu 10/21/08 #10	Th 12/11/08 #1	Tu 12/02/08 #3
Y. Chang, J. Choma, Jr., and J. Wills, "The Design and Analysis of a RF CMOS Bandpass Filter," Proceedings of the IEEE International Symposium on Circuits and Systems, May 28-31, 2000, Geneva, pp. II-625 - II-628 Changed from 12/02/08 D. K. Shaeffer and T. H. Lee, "A 1.5-V, 1.5-GHz CMOS Low Noise Amplifier," IEEE Journal of Solid-State Circuits, Vol. 32, No. 5, May 1997, pp. 745 - 759.				

Chang, Benjimin C	0101	Th 10/16/08 #7	Tu 11/11/08 #4	Th 11/06/08 #1
F. A. Levinzon, "Ultra-Low-Noise High-Input Impedance Amplifier for Low-Frequency Measurement Applications," IEEE Transactions on Circuits and Systems - I, Vol. 55, No. 7, August 2008, pp, 1815 - 1822.				
Chaudhari, Rohit Kumar	0102	Tu 10/21/08 #2	Tu 12/02/08 #2	Th 11/20/08 #5
J. P. Alegre, S. Celma, m. T. Sanz and J. M. Garcia del Pozo, "Low-ripple fast-settling envelope detector," Electronics Letters, Vol. 42, No. 18, August 31. 2006. pp. ?????				
Codd, David Andrew	0101	Tu 10/28/08 #8	Tu 12/09/08 #1	Tu 12/09/08 #2
C. Galup-Montoro, M. C. Schneider and I. J. B. Loss, "Series-Parallel Association of FET's for High Gain and High Frequency Applications," IEEE Journal of Solid-State Circuits, Vol. 29, No. 9, September 1994, pp. 1094 - 1101.				
Dang, Tri Van	0101	Tu 10/21/08 #6	Th 11/20/08 #4	Tu 11/25/08 #2
W. Gai, G. Chen and E. Seevinck, "Quadratic-translinear CMOS multiplier-divider circuit," Electronic Letters, Vol. 33, No. 10, May 8, 1997, pp. 860 - 861. Changed from 11/06/08				
W. Wu, M. A. T. Sanduleanu, X. Li and J. R. Long, "17GHz RF Front-Ends for Low-Power Wireless Sensor Networks," IEEE Journal of Solid-State Circuits, Vol. 43, No. 9, September 2008, pp. 1909 - 1919.				
Desai, Sumil	0102	Tu 11/04/08 #3	Th 12/04/08 #1	Th 12/04/08 #2
W. Lei, "A Transistor-Only Low-Pass Filter with Adjustable Bias and Small Phase at High Frequencies," IEE Journal of Solid-State Circuits, Vol. 28, No. 5, May 1993, pp. 610 - 612.				
Drewry, Brian Boyd	0101	Th 10/30/08 #11	Th 11/06/08 #5	Th 11/06/08 #4
N. Idir, R. Bausiere and J. J. Franchaud, "Active Gate Voltage Control of Turn-on di/dt and Turn-off dv/dt in Insulated Gate Transistors," IEEE Transactions on Power Electronics, Vol. 21, No. 4, July 2006, pp. 849 - 855.				
Gagliolo, Nicolas Eric	0101	Th 10/30/08 #10	Th 12/04/08 #3	Th 12/04/08 #4
B. Gosselin, M Sawn and C. A. Chapman, "A Low-Power Integrated Bioamplifier with Active Low-Frequency Suppression," IEEE Transactions on Biomedical Circuits and Systems, Vol. 1, Nol. 3, September 2007, pp. 184 - 192.				
Goldin, Paul	0102	Tu 10/28/08 #10	Tu 11/11/08 #6	Tu 11/18/08 #5
P. T. Chen, "Design and Applications of 2-6.5GHz Transistor Amplifiers," IEEE Journal of Solid-State Circuits, Vol. SC-9, No. 4, August 1974, pp. 154 - 158.				
Gyawali, Bipin	0101	Th 10/30/08 #3	Tu 11/18/08 #6	Tu 11/18/08 #2
D. V. Kerns, Jr., "Optimization of the Peaking Current Source," IEEE Journal of Solid-State Circuits, Vol. SC-21, No. 4, August 1986, pp. 587 - 590 (missing p. 588?) Changed to 11/18/08				
H. G. Jungmeister and D. Schmidt, "High-Speed Pulse Circuits Obtained by Computer-Aided Nonlinear Analysis of Step-Recovery Diodes," IEEE Journal of Solid-State Circuits, Vol. SC-5, No. 4, August 1970, pp. 135 - 141.				
Harripersad, Suresh	0102	Tu 11/04/08 #8	Th 11/20/08 #6	Tu 12/09/08 #5
D. L. Lee and P. O. Lauritzen, "A Common-Base Transistor Differential Amplifier," [reference source = IRE Proceedings ???], p. 33,				
Hirsh, Matthew Thomas	0101	Th 10/16/08 #5	Tu 11/18/08 #3	Th 11/06/08 #2 (RWN)
H. Shao and C.-Y. Tsui, "A Robust, Input Voltage Adaptive and Low Energy Consumption Level Converter for Sub-threshold Logic," IEEE {????} 2007, pp. 312 - 315.				
Hobgood, Paul Taylor	0101	Th 10/16/08 #6 (RWN)	Th 11/06/08 #3 (RWN)	Th 12/04/08 #5 (RWN) On 10/16/08

				Moved from Tu 11/11/08 #2 (RWN)
A. D. Downey and G. M. Wierzba, "A Class-G/FB Audio Amplifier," IEEE Transactions on Consumer Electronics, Vol. 53, No. 4, November 2007, pp. 1537 - 1545.				
Howard, Jessica May	0101	Th 10/16/08 #8 (RWN)	Th 11/20/08 #2	Th 11/20/08 #1
C.-K. Liang, R.-J. Yang and S.-I. Liu, "An All-Digital Fast-Locking Programmable DLL-Based Clock Generator," IEEE Transactions on Circuits and Systems - I, Vol. 55, No. 1, February 2008, pp. 361 - 369.				
Hull, Warren Eugene	0101	Tu 10/21/08 #7	Tu 11/25/08 #5	Tu 11/11/08 #6 (RWN)
Y. Inabe and K. Kataoka, "An Integrated JK Flip-Flop Circuit," IEEE Journal of Solid-State Circuits, Vol. SC-12, No. 4, August 1977, pp. 403 - 406. or S. W. Mehranfar, "A Technology-Independent Approach to Custom Analog Cell Generation," IEEE Journal of Solid-State Circuits, Vol. 26, No. 3, March 1991, pp. 386 - 393.				
Jamison, Kasey Lynn	0101	Th 10/16/08 #4	Th 11/06/08 #4	Th 11/06/08 #5
S. Jain and V. Agarwal, "An Integrated Hybrid Power Supply for Distributed Generation Applications Fed by Nonconventional Energy Sources," IEEE Transactions on Energy Conversion, Vol. 23, No. 2, June 2008, pp. 622 - 631.				
Jandorf, Adam Lee	0102	Tu 10/28/08 #5	Tu 11/25/08 #3	Th 11/13/08 #5
W. Li, "A Transistor-Only High-Pass Filter with Adjustable Q Factor," IEEE Transactions on Circuits and Systems -I, Vol. 40, No. 2, February 1993, pp. 136 - 140. Changed from 11/23/08: A. A. Khan, "Analog Linear-Scale Ohmmeter," IEEE Transactions on Instrumentation and Measurement, Vol. 24, No. 2, June 1975, pp. 150 - 155.				
Kosgi, Naresh Chandra	0102	Tu 10/21/08 #4	Th 11/20/08 #5	Tu 12/02/08 #2
T. Lehmann and M. Cassia, "1-V Power Supply CMOS Cascode Amplifier," IEEE Journal of Solid-State Circuits, Vol. 36, No. 7, July 2001, pp. 1082 - 1086. Changed 11/13/08 from: S. Karimi, P. Poure, S. Saadate and E. Gholipour, "FPGA-based fully digital controller for three-phase shunt active filters," International Journal of Electronics, Vol. 95, No. 8, August 2008, pp. 805 - 818.				
Kumar, Abhishek	0102	Tu 10/28/08 #4	Th 11/13/08 #5	Tu 11/25/08 #3
11/04/08 Y.-F. Liu, L.Ge and S. Zheng, "Dual Channel Current-Source Gate Drivers for High-Frequency DC-DC Converters," IEEE ..., 2008, pp. 613 - 618.				
Lockyear, Alexander Garber	0102	Th 11/04/08 #1	Tu 12/02/08 #5	Th 12/02/08 #4
I. Takahashi, J. Itoh and G. J. Su, "Toward 99% Efficiency for Transistor Inverters," IEEE Industry Applications Magazine, Vol. ?, No. ?, November/December 1996, pp. 39 - 46.				
McMurray, Stephen Ward	0101	Th 10/16/08 #2	Th 11/06/08 #1	Tu 11/11/08 #4
S.-W. Sin, S.-P. U and R. P. Martins, "Generalized Circuit Techniques for Low-Voltage High-Speed Reset- and Switched-Opamps," IEEE Transactions on Circuits and Systems - I, Vol. 55, No. 8, September 2008, pp. 2188 - 2201.				
Moore, David Michael	0101	Th 11/04/08 #2	Tu 12/02/08 #4	Tu 12/02/08 #5

T. Ge and J. S. Chang, "Bang-Bang Control Class-D Amplifiers: Power-Supply Noise, : IEEE Transactions on Circuits and Systems -II, Vol. 55, No. 8, August 2008, pp. 723 - 727.				
Morales, Denis Isael	0102	Th 10/30/08 #4	Th 12/04/08 #5	Th 11/13/08 #3 (RWN)
R. R. Harrison and C. Charles, "A Low-Power Low-Noise CMOS Amplifier for Neural Recording Applications," IEEE Journal of Solid-State Circuits, Vol. 38, No. 6, June 2003, pp. 958 - 965.				
Mubarik, Aiysha	0101	Th 10/30/08 #2	Tu 11/18/08 #2	Tu 11/18/08 #6
H. Lin and C.-J. Liang, "A Sub-1V Bandgap Reference Circuit Using Subthreshold Current," Proceedings of the IEEE International Symposium on Circuits and Systems, Vol. 5, May 2005, pp. 4253 - 4256. Changed from 11/18/08				
J. A. Archer, J. F. Gibbons and G. M. Purnaiya "Use of Transistor-Simulated Inductance as an Interstage Element in Broadband Amplifiers," IEEE Journal of Solid-State Circuits, Vol. SC-3, No. 1, March 1968, pp. 12 - 21.				
Murray, David Matthew	0102	<del>Th 10/16/08 #10 (RWN)</del>	<del>Tu 11/11/08 #2 (RWN)</del>	<del>Th 12/04/08 #5 (RWN)</del>
Dropped 10/15/08 C.-Y Yang, G.-K. Dehng, J.-M. Hsu and S.-I. Liu, "New Dynamic Flip-Flops for High-Speed Dual-Modulus Prescaler," IEEE Journal of Solid-State Circuits, Vol. 33, No. 10, October 1998, pp. 1568 - 1571. Changed from the following on 10/14/08 K.-H. Sung and L.-Sup, "Comments on "New Dynamic Flip-Flops for High-Speed Dual-Modulus Prescaler," IEEE Transactions on Solid-State Circuits, Vol. 35, No. 6, June 2000, pp. 919 - 920.				
Myers, Austin Oliver	0102	Tu 10/28/08 #3	Th 12/04/08 #4	Th 12/04/08 #3
S. Minaer and E. Yuce, "A new full-wave rectifier circuit employing single dual-X current conveyor," International Journal of Electronics, Vol. 95, No. 8, August 2008, pp. 777 - 784.				
Nguyen, Albert T	0101	Th 10/30/08 #8	Tu 11/25/08 #2	Th 11/20/08 #4
S. Sheikhaei, S. Mirabbasi and A. Ivanov, " 0.18um CMOS pipelined encoder for a 5GS/s 4-bit flash analogue-to-digital converter," Canadian Journal of Electrical Computer Engineering, Vol. 30, No. 4, Fall 2005, pp. 183 - 186.				
Parikh, Amit Rajesh	0101	Tu 10/21/08 #9	Tu 12/02/08 #3	Tu 12/02/08 #1
A. C. van der Woerd and W. A. Serdijn, "Low-Voltage Low-Power Controllable Preamplifier for Electret Microphones," IEEE Journal of Solid-State Circuits, Vol. 28, No. 10, October 1993, pp. 1052 - 1055.				
Peitsch, Christopher Micha	0101	Tu 10/21/08 #3	Th 11/20/08 #3	Tu 11/11/08 #5
F. Gambini, M. Conti, S. Orcioni, F. Ripa and M. Caldari, "Physical modeling in System C-WMS and real time synthesis of electric guitar effects," [??? Reference source].				
Phelps, Peter M	0102	Th 10/16/08 #9	Th 11/06/08 #6	Th 11/13/08 #4
Y. B. Gu, S.-F. Yueh, T.-W. Chang and K.-225.C. Huang, "CMOS voltage reference with multiple outputs," IET Circuits, Devices, Systems, Vol. 2, No. 2, 2008, pp. 222- 226.				
Qiu, Lehong	0102	Th 10/16/08 #1 (RWN)	Th 12/11/08 #3 (RWN)	Th 12/11/08 #1 (RWN)
Rcd. 10/16/08 X. Fan, H. Zhang and E. Sanchez-Sinencio, "A Noise Reduction and Linearity Improvement Technique for a Differential Cascode LNA," IEEE Journal of Solid-State Circuits, Vol. 43, No. 3, March 2008, pp. 588 - 599.				
Salaets, Natalie	0102	Th 10/30/08 #9	Th 11/13/08 #2	Th 11/13/08 #1

Michele				
M. H. Zarifi, J. Frounchi, S. Asgarifar, M. B. Nia and F. Dehkhoda, "A High Speed Low Power Analog to Digital Converter for Biomedical Application," Proceedings of the IEEE CCECE/CCGEI, Niagara Falls, Canada, May5-7, 2008, pp. 001269 - 001272.				
Saleem, Muhammad Farooq	0102	Tu 10/21/08 #11	Th 11/13/08 #3	Th 12/11/08 #2 (RWN)
Y. Ding and R. Harjani, "A High-Efficiency CMOS +22-dBm Linea Power Amplifier," IEEE Journal of Solid-State Circuits, Vol. 40, No. 9, September 2005, pp. 1895 - 1900.				
Schauf, Amber Dawn	0102	Th 10/16/08 #12 (RWN)	Th 12/11/08 #4 (RWN)	Th 12/11/08 #3 (RWN)
S. Panda and N. P. Padhy, "Power Electronics Based FACTS Controller for Stability Improvement of a Wind Energy Distribution System," International Journal of Electronics, Circuits and Systems, Vol. 1, No. 1, [date??], pp. 30 - 37.				
Shahami, Soheil	0101	Th 10/30/08 #1	Tu 11/18/08 #1	Tu 11/18/08 #4
E. Farquhar and P. Hasler, "A Bio-Physically Inspired Silicon Neuron," Proceedings of the International Symposium on Circuits and Systems, 2004, pp. I-309 - I-312.				
Sheikh, Sani	0101	Th 10/30/08 #5	Tu 11/18/08 #4	Tu 11/18/08 #1
K. Dejhan, P. Tooprakai, S. Mitatha, F. Cheevasuvit and C. Soonyeekan, "1 V Full Swing Bootstrapped CMOS Inerter Circuit," Proceedings of the ICSE 2002, Penang, Malaysia, pp. 275 - 278. Changed from 11/18/08				
W. Lo and E. S. Yang, "A p-n-p-n Binary Full Adder," IEEE Journal of Solid-State Circuits, Vol. ??, No. ??, June 1969, pp. 175 - 178.				
Sher, Daniel Joseph	0102	Tu 10/28/08 #7	Tu 11/18/08 #5	Tu 11/18/08 #3
V. Stojanovic and V. G. Oklobdzija, "Comparative Analysis of Master-Slave Latches and Flip-Flops for High-Performance and Low-Power Systems," IEEE Journal of Solid-State Circuits, Vol. 34, No. 4, April, 1998, pp. 536 - 548.				
Sichler, Jennifer Ann	0102	Tu 10/21/08 #8	Th 11/20/08 #1	Th 11/20/08 #2
S. H. K. Embabi, A. Bellaouar and K. Islam, "A Bootstrapped Bipolar CMOS (B2CMOS) Gate for Low-Voltage Applications," IEEE Journal of Solid-State Circuits, Vol. 30, No. 1, January 1995, pp. 46 - 53.				
Taylor, Brian Peterson	0102	Th 10/16/08 #11 (RWN)	Th 11/13/08 #6 (RWN)	Th 11/06/08 #3 (RWN)
L.-J. Pu and Y. P. Tividis, "Transistor-Only Frequency-Selective Circuits," IEEE Journal of Solid-State Circuits, Vol. 25, No. 3, June 1990, pp. 821 - 832.				
Thomson, Robert Steven	0102	Th 10/30/08 #6	Th 12/11/08 #2	Tu 11/11/08 #1
B. Calvo, S. Celma, M. T. Sanz, J. P. Alegre and F. Aznar, "Low-Voltage Linearly Tunable CMOS Transconductor with Common-Mode Feedforward," IEEE Transactions on Circuits and Systems - I, Vol. 55, No. 3, April 2008, pp. 715 - 721.				
Tousley, Benjamin Francis	0102	Tu 11/04/08 #5	Tu 11/25/08 #1	Tu 11/25/08 #4
S. R. Schenck and T.. J. Johnson, "MOS Current Mirror with High Impedance Output," United States Patent Number 4,471,292, September 11, 1984, pp. 1 - 6.				
Vu, Tung T	0101	Tu 11/04/08 #7	Tu 12/09/08 #5	Th 11/20/08 #6
L. S. August and R. R. Circle, "Advantages of Using a PMOS FET Dosimeter in High-Dose Radiation Effects Testing," IEEE Transactions on Nuclear Science, Vol. NS-31, No. 6, December 1984, pp. 1113 - 1115.				
Winter, Benjamin Scott	0101	Th 10/16/08 #3	Th 11/06/08 #2	Th 11/06/08 #6

R. Q. R. van der Zee and A. J. M. van Tuijl, "A Power-Efficient Audio Amplifier Combining Switching and Linear Techniques," IEEE Journal of Solid-State Circuits, Vol. 34, No. 7, July 1999, pp. 985 - 990.				
Won, Stephen Min	0102	Th 10/30/08 #7	Th 11/13/08 #1	Th 11/13/08 #2
M. Cimino, H. Lapuyade, Y. Deval, T. Taris and J.-B. Begueret, "Design of a 0.9V 2.45GHz Self-Testable and Reliability-Enhanced CMOS LNA," IEEE Journal of Solid-State Circuits, Vol. 43, No. 5, May 2008, pp. 1187 - 1194.				
Zhang, David Y	0102	Tu 10/28/08 #9	Tu 12/09/08 #2	Tu 12/09/08 #1
M. G. Johnson, "A Symmetric CMOS NOR Gate for High-Speed Applications," IEEE Journal of Solid-State Circuits, Vol. 23, No. 5, October 1988, pp. 1233 - 1236.				
Number of students = 54				