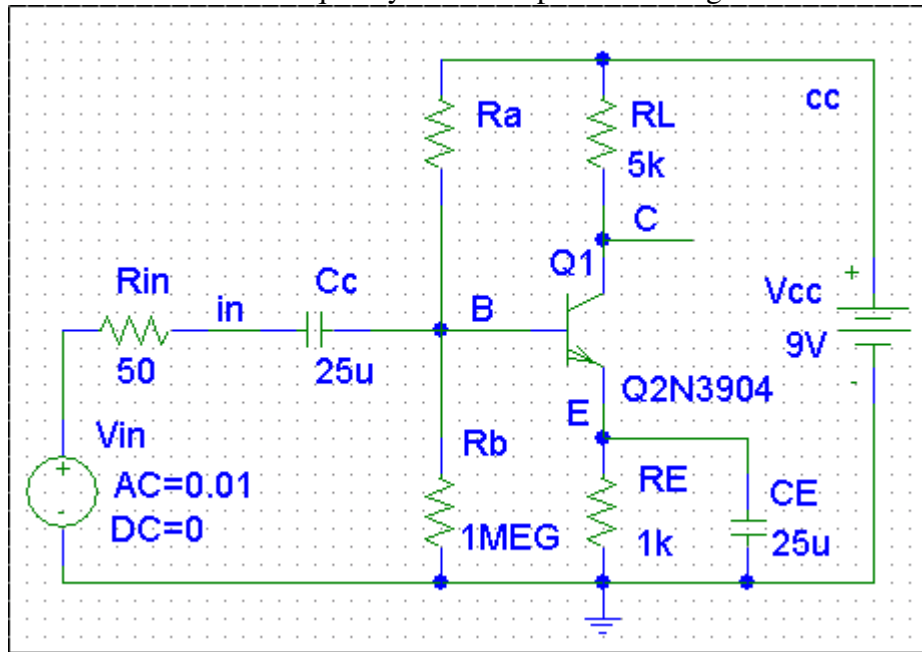
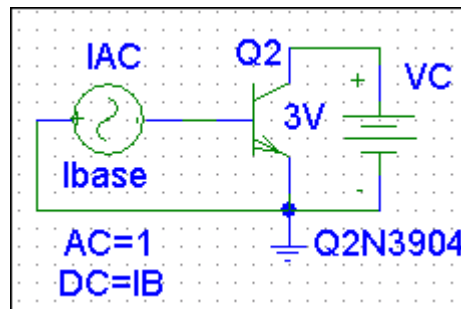


1. For the following circuit bias the transistor at $I_C=600\mu\text{A}$ and $V_{CE}=3\text{V}$.
 - a) Run Spice curves for the 2N3904 and determine the I_B to give this Q point.
 - b) With that I_B analytically determine R_a to give this bias point with the values given for $V_{cc}=9\text{V}$, $R_L=5\text{k}$, $R_E=1\text{k}$, and $R_b=1\text{MEG}$. Then run Spice and check if the bias point is as desired. Correct R_a to give the desired Q point.
 - c) Run a frequency response with output the voltage at C. Do this from 100Hz to 10GHz on a decade frequency scale and plot the voltage at C in DB.



2. Using the following circuit find f_T for the above transistor at the bias point.



3.
 - a) Using the formulas in the book, Table 5.7, p. 490, calculate all the parameters for the pi equivalent circuit of the 2N3904 as biased above. Use the VAF, TF and $C_{je}=C_{jeo}$ of the Spice model and BF and f_T found from the Spice curves at the bias I_C (use the second line of the table equations to find C_π and C_μ).
 - b) Draw the resulting small signal equivalent circuit for the circuit of problem 1 above.
 - c) Ignoring all capacitors find the center frequency voltage gain (voltage output at node C); compare with the value found via Spice in problem 1.