

Paper Choices

Name/Paper	Email address	presentation Dates	Commentating dates
Alhamarneh, Mohammad A	malhamar@mail.umd.edu	M 11/05/07 b W 11/14/07 b	W 11/07/07 b M 11/19/07 b
C. Yoo, M.-K. Kim and W. Kim, "A Static Power Saving TTL-to-CMOS Input Buffer," IEEE Journal of Solid-State Circuits, Vol. 30, No. 5, May 1995, pp. 615 - 620.			
Chang, Yuan-Shuo	hermes_edison@yahoo.com.tw	M 11/05/07 a W 11/28/07 c	W 11/07/07 a M 12/03/07 b
N. Kalantari and M. M. Green, "All-CMOS High-Speed CL Gates with Active Shunt-Peaking," Proceedings of ISCAS 2007, New Orleans, May 2007 pp. 2554 - 2557.			
Churaman, Wayne A	tessnn@mail.umd.edu	M 11/12/07 c M 12/03/07 a	W 10/31/07 b M 12/03/07 d
M. Guan and W.-H. Liao, "Comparative Analysis of Piezoelectric Power Harvesting Circuits for Rechargeable Batteries," Proceedings of the 2005 IEEE International Conference on Information Acquisition, Hong Kong and Macau, China, June 27 - July 3, 2005, pp. 243 - 246.			
Gee, Danny	dageeps@gmail.com	W 10/31/07 b M 12/03/07 d	M 11/12/07 c M 12/03/07 a
G. A. Rincon-Mora and P. E. Allen, "A Low-Voltage, Low Quiescent Current, Low Drop-Out Regulator," IEEE Journal of Solid-State Circuits, Vol. 33, No. 1, January 1998, pp. 36 - 44.			
Huang, Victor Bo-Cin	vbhuang@gmail.com	M 11/12/07 d M 12/03/07 c	M 11/05/07 d M 11/26/07 c
C. J. Debono, F. Maloberti and J. Micallef, "A Low-Voltage CMOS Multiplier for RF Applications," Proceedings of ISLEPD'00, Rapallo, Italy, pp. 225 - 227.			
Kusel, Julian Kenneth	jkusel@mail.umd.edu	M 11/12/07 b M 11/26/07 a	M 11/12/07 a M 11/19/07 a
Y.-S. Choi and D.-H. Han, "Gain-Boosting Charge Pump for Current Matching in Phase-Locked Loop," IEEE Transactions on Circuits and Systems - II, Vol. 53, No. 10, October 2006, pp. 1022 - 1025.			
Li, Bo	liletian@gmail.com	M 11/05/07 d M 11/26/07 c	M 11/12/07 d M 12/03/07 c
D. K. Shaeffer and T. H. Lee, "A 1.5-V, 1.5-GHz CMOS Low Noise Amplifier," IEEE Journal of Solid-State Circuits, Vol. 32, No. 5, May 1997, pp. 745 - 759.			
Massoud, Tarek Mohamed	tmassoud@mail.umd.edu	W 10/31/07 c M 11/19/07 c	W 10/31/07 a W 11/14/07 a
G. Indiveri, E. Chicca and R. Douglas, "A VLSI Array of Low-Power Spiking Neurons and Bistable Synapses with Spike-Timing Dependent Plasticity," IEEE Transactions on Neural Networks, Vol. 17, No. 1, January 2006, pp. 211 - 221.			
McGovern, Joseph Lawrence	jmcgovern_1@verizon.net	M 11/12/07 a M 11/19/07 a	M 11/12/07 b M 11/26/07 a
B. Razavi, "A Study of Injection Locking and Pulling in Oscillators," IEEE Journal of Solid-State Circuits, Vol. 39, No. 9, September 2004, pp. 1415 - 1424.			
Meng, Wei	weimeng@mail.umd.edu	W 11/07/07 c W 11/28/07 b	M 11/05/07 a W 11/28/07 c
Y. Zhang, K. A. Zaki, J. A. Ruiz-Cruz and A. E. Atia, "Analytical Synthesis of Generalized Multi-band Microwave Filters," IEEE, 2007, pp. 1273 - 1276.			
Monajemi, Babak	babakm@wam.umd.edu	W 10/31/07 a W 11/14/07 a	W 10/31/07 c M 11/19/07 c

M. Usama and T. Kwasniewski, "Design and Comparison of CMOS Current Mode Logic Latches," Proceedings of the IEEE ISCAS 2004, ..., pp. IV-353 - IV-356. Replaces the following 10/22/07			
A. Nabavi-Lishi and N. C. Rumin, "Inverter Models of CMOS Gates for Supply Current and Delay Evaluation," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 13, No. 10, October 1994, pp. 1271 - 1279.			
Schauer, Bryan C	schauer@mail.umd.edu	M 11/05/07 c M 11/26/07 b	W 10/31/07 d W 11/28/07 a
W. M. Lim, M. A. Do, J. G. Ma and K. S. Yeo, "A Broadband CMOS LNA for WLAN Applications," IEEE, 2003, pp. 42 - 46,			
Shey, James Walter	jshey@mail.umd.edu	W 11/07/07 a M 12/03/07 b	M 11/05/07 c M 11/26/07 b
F.-s. Lai and C.-F. Lee, "On-Chip Voltage Down Converter to Improve SRAM Read/Write Margin and Static Power for Sub-Nano CMOS Technology," IEEE Journal of Solid-State Circuits, Vol. 42, No. 9, September 2007, pp. 2061 - 2070.			
Shin, Jun Yong	cool7399@mail.umd.edu	W 10/31/07 d W 11/28/07 a	W 11/07/07 c W 11/28/07 b
R. J. Perry and K. Arora, "Using PSpice to Simulate the Photoresponse of Ideal CMOS Integrated Circuit Photodiodes," IEEE ???, 1996, pp. 374 - 380.			
Surapaneni, Sri Harsha	sri.surapaneni@gmail.com	W 11/07/07 b M 11/19/07 b	W 11/07/07 d W 11/14/07 c
W. Chai, C. Lou, Q. Wand, W. Lei and X. Zhang, "Analysis of the Driving Characteristic of CNT Triode with High Frequency," IEEE, 2006, pp. 433 - 434.			
Yeshitla, Binyam	binyam@mail.umd.edu	W 11/07/07 d W 11/14/07 c	M 11/05/07 b W 11/14/07 b
B. Meskoob and S. Prasad, "A Discrete Phase-Locked Loop for Undergraduate Laboratories," IEEE Transactions on Education, Vol. 37, No. 4, November 1994, pp. 372 - 374.			

Number of students = 16