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RWN 10/11/06

ENEE 417 Fall 2006 Microelectronics Design Laboratory

Time Schedule: lectures W 09:00-09:50 EGR 1108; Laboratory AVW 1334 Sections 0101-0105:

Tu 14:00-16:50 or M 09:00-11:50 or M 13:00-15:50 or F 09:00-11:50

Wednesday	Wednesday
(Week starts W 08/30/06) 08/30/06 Familiarize with equipment LabVIEW & DAQ tutorials; DAQ for transistor curves Tektronix and Spice transistor curve tracing Initiate Basic Paper choice	(miss M 09/04/06) 09/06/06 Spice, LabVIEW & DAQ tutorials; GPIB control Basic Paper preliminary choice Spice modeling from curve tracings
09/13/06 Final Basic Paper choice Learn MAGIC and VLSI layout Op-amp curve tracer	09/20/06 Parts List for Basic Paper Op-amp curve tracer Learn MAGIC and VLSI layout
09/27/06 Active inductor Using DAQ & LabView	10/04/06 Active inductor Using DAQ & LabView
10/11/06 Oscillators CMOS Ring Oscillator DAQ & LabView	10/18/06 Base paper designs Catch up on all test projects using DAQ & LabView
10/25/06 Base paper design testing	11/01/06 Basic paper circuit design & testing
11/08/06 Basic paper circuit measurements Base paper circuit VLSI layout	11/15/06 Finish Base paper circuit
11/22/05 (miss Th 11/23 & 24/06) Finish Base Paper Circuit and Power Point for Presentations	11/29/06 Oral Reports in Laboratory Using Power Point
12/06/06 (last lab 12/12/06) Last Class Written Reports on Base Paper Circuit Due	
No Final Exam	