ENEE 417 - Fall 2005
>   Weeks #9-#12
>   Weeks beginning W 10/26/05
>   Base Paper Circuit

These periods will be concentrated upon your circuit taken from the base paper you have chosen.

1. Design the circuit using design equations where possible.
2. Run Spice simulations; then construct and test. Use automated testing where possible.
3. Make a vlsi layout for your circuit, do a Spice extraction and compare with the results with your simulations and measurements.

If interested and willing to make measurements on the fabricated chip, you may make a submission to MOSIS for which you will need to insert the validated circuit in a set of pads.

4. Write a term paper based upon your results and summarizing your study. Be sure to emphasize what you did and your results while including a study of the base paper circuit. Prepare a computer presentation for an oral report on your base paper study. Submit a written report within a day after the oral report. A good portion of the course grade will be based upon the written report and the experimentation behind it.