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RWN 08/16/05-11/29/05

ENEE 417 Fall 2005 Microelectronics Design Laboratory

Time Schedule: lectures W 09:00-09:50 EGR 1202; Laboratory AVW 1330 Sections 0101-0105:

Tu 14:00-16:50 or M 09:00-11:50 or Th 14:00-16:50 or M 13:00-15:50 ; or F 09:00-11:50

| Wednesday | Wednesday |
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| (Week starts W 08/31/05) 08/31/05 Familiarize with equipment LabVIEW & DAQ tutorials; DAQ for transistor curves Tektronix and Spice transistor curve tracing Initiate Basic Paper choice | (miss M 09/05/05) 09/07/05 Spice, LabVIEW & DAQ tutorials; GPIB control Basic Paper preliminary choice Spice modeling from curve tracings |
| 09/14/05 Final Basic Paper choice Learn MAGIC and VLSI layout Op-amp curve tracer | 09/21/05 Parts List for Basic Paper Op-amp curve tracer Learn MAGIC and VLSI layout Ring oscillator VLSI layout |
| 09/28/05 Op-amp oscillators Using DAQ & LabView | 10/05/05 CMOS Ring Oscillator Using DAQ & LabView |
| 10/12/05 Catch Up Finish Oscillator experiments and all text projects using DAQ & LabView | 10/19/05 Current controlled BJT circuit and uses Base paper designs |
| 10/26/05 Base paper design testing | 11/02/05 Basic paper circuit design & testing |
| 11/09/05 Basic paper circuit measurements Base paper circuit VLSI layout | 11/16/05 Finish Base paper circuit |
| 11/23/05 (miss Th 11/24 & 25/05) Finish Base Paper Circuit and Power Point for Presentations | 11/30/05 Oral Reports in Laboratory Using Power Point |
| 12/07/05 (last lab 12/13/05) Last Class Written Reports on Base Paper Circuit Due | |
| Final Exam - deleted | |