ENEE 417 Fall 2004   Microelectronics Design Laboratory
Time Schedule: lectures W 09:00-09:50 EGR 1202; Laboratory AVW 1330 Sections 0101-0105:
Tu 14:00-16:50 or M 09:00-11:50 or Th 14:00-16:50 or M 13:00-15:50 ; or F 09:00-11:50

Wednesday	Wednesday
(Week starts M 08/30/04) 09/01/04 Familiarize with equipment LabVIEW & DAQ tutorials; DAQ for transistor curves Tektronix and Spice curve tracing Initiate Basic Paper choice	(miss M 09/06) 09/08/04 Basic Paper preliminary choice Tektronix and Spice curve tracing LabVIEW & DAQ tutorials; DAQ for transistor curves GPIB control
09/15/04 Final Basic Paper choice Learn MAGIC and VLSI layout Op-amp curve tracer	09/22/04 Parts List for Basic Paper Op-amp curve tracer Learn MAGIC and VLSI layout Ring oscillator VLSI layout
09/29/04 Op-amp oscillators Using DAQ & LabView	10/06/04 CMOS Ring Oscillator Using DAQ & LabView
10/13/04 Catch Up Finish Oscillator experiments and all text projects using DAQ & LabView	10/20/04 Current controlled BJT circuit and uses Base paper designs
10/27/04 Base paper design testing	11/03/04 Basic paper design testing
11/10/04 Basic paper measurements Base paper VLSI layout	11/17/04 Finish Base paper
11/24/04 (miss Th 11/25) Finish Base Paper and Power Point for it	12/01/04 Oral Reports
12/08/04 RWN to APCCAS Last Class Written Reports Due	
Final Exam Cancelled mid semester	