

[correction - previously written R8 should have been R6]

1. (40 points)

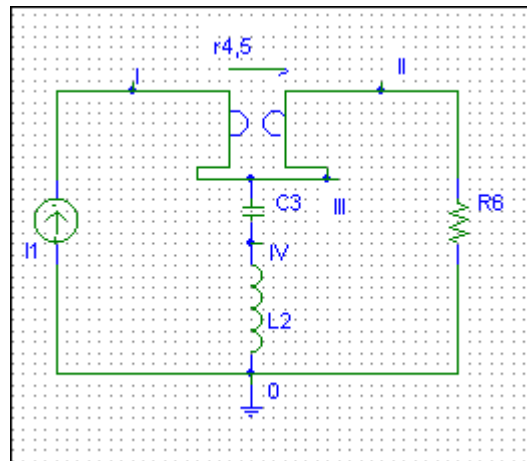
For the following circuit the current I_1 is the input and the voltage on R_6 (positive at node II) is the output.

a) Draw the adjoint circuit for the unfed (i.e. without I_1) 2-port circuit and an open circuit load (across R_6 with R_6 kept inside the 2-port circuit).

b) Terminate the adjoint circuit 2-port of a) so that it may be used to calculate the sensitivity to $r=r_{4,5}$ of the transfer function $T(s)$ which is the voltage across R_6 (positive at the top) divided by the input current, $T(s) = v_{II}/I_1$.

c) Find the sensitivity of v_{II}/I_1 to $r=r_{4,5}$ using the adjoint circuit $S_r^{T(s)}$.

d) Repeat c) for the sensitivity of v_{II}/I_1 to C_3 , $S_{C_3}^{T(s)}$.



2. (40 points)

For the circuit and transfer function of problem 1. above

a) Set up the semistate equations using the voltage on the capacitor (positive at the top) and the current (down) through the inductor and the voltage across R_6 (positive at the top) as the semistate vector, $x = [v_{C3}, i_{L2}, v_{II}]^T$.

b) Change the output from v_{II} to v_I and use the equations of part a) to eliminate v_{II} to get a set of state variable equations and from those find the input impedance as the transfer function.

c) Set up a PSpice schematic for these last equations using only (two) C and (as many as needed) G elements along with the driving current source. Submit a printout of your schematic.

d) Run Spice for 2secs with $C_3=L_2=R_6=r_{4,5}=2$ and $I_1=3\sin(5\pi t)$. Plot $v_{II}(t)$.